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## MICRO JOURNAL

VOLUME VII ISSUE III • Devoted to the 68XX User • March 1985  
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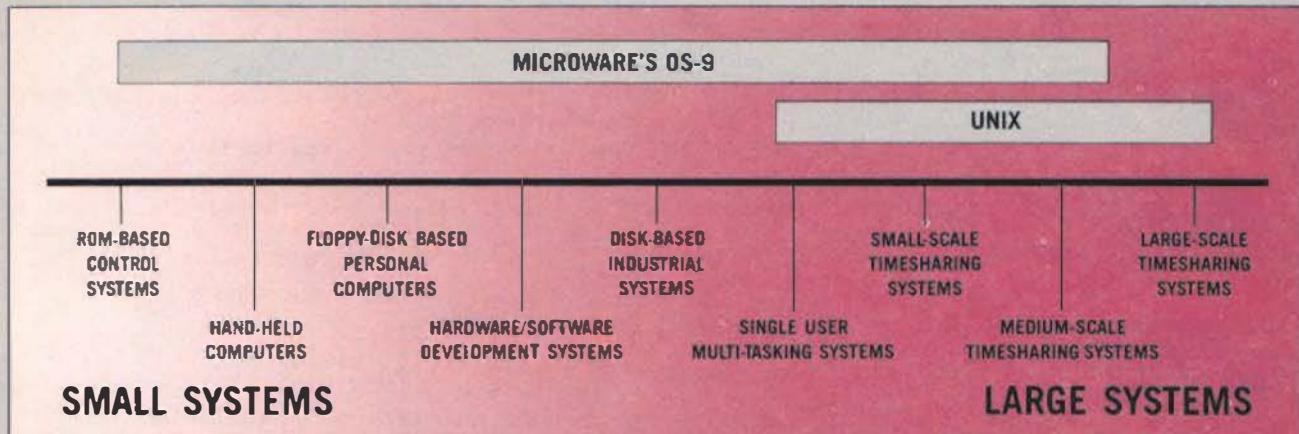
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**OS-9™**

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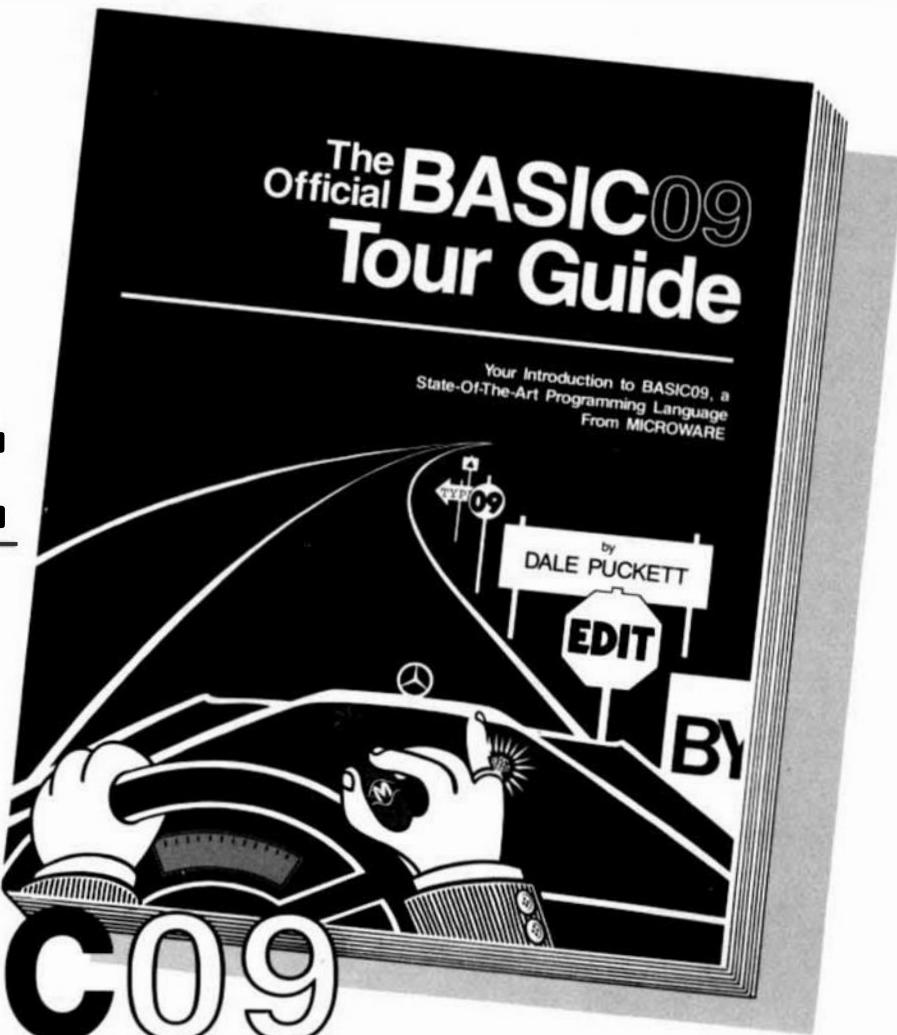
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# FLEX™ USER NOTES THE 6800-6809 BOOK

By: Ronald W. Anderson

As published in 68 MICRO JOURNAL™

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JTW

The publishers of 68 MICRO JOURNAL are proud to announce the publication of Ron Anderson's FLEX USER NOTES, in book form. This popular monthly column has been a regular feature in 68 MICRO JOURNAL SINCE 1979. It has earned the respect of thousands of 68 MICRO JOURNAL readers over the years. In fact, Ron's column has been described as the 'Bible' for 68XX users, by some of the world's leading microprocessor professionals. Now all his columns are being published, in whole, as the most needed and popular 68XX book available. Over the years Ron's column has been one of the most popular in 68 MICRO JOURNAL. And of course 68 MICRO JOURNAL is the most popular 68XX magazine published.

As a SPECIAL BONUS all the source listing in the book will be available on disk for the low price of: FLEX™ format only — 5" \$12.95 — 8" \$16.95 plus \$2.50 shipping and handling, if ordered with the book. If ordered separately the price of the disks will be: 5" \$17.95 — 8" \$19.95 plus \$2.50 shipping and handling.

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All TEXT files in the book are on the disks.

LOGO.C1	File load program to offset memory — ASM PIC
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M.C2	Output a file to modem (or another port) — ASM
PRINT.C3	Parallel (enhanced) printer driver — ASM
MODEM.C2	TTL output to CRT and modem (or other port) — ASM
SCIPKG.C1	Scientific math routines — PASCAL
U.C4	Mini-monitor, disk resident, many useful functions — ASM
PRINT.C4	Parallel printer driver, without PFLAG — ASM
SET.C5	Set printer modes — ASM
SETBAS1.C5	Set printer modes — A-BASIC (And many more)

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NOTE: C1..C2, etc.= Chapter 1, Chapter 2, etc.

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# Flex User Notes

Ronald W. Anderson  
3540 Sturbridge Court  
Ann Arbor, MI 48105

## Once More

I have from time to time in this column made a plea that I wish you out there would heed. I don't mean to be harpy or negative in saying this, but it is time for a repeat. This column is my donation to the 68XX users. It is done without compensation other than to be able to keep the software that I review. I enjoy it greatly, though at one time I had considered stopping in order to devote more time to writing freelance for pay. On long consideration, I decided that if I were to get paid for a regular column, it would cease to be any fun, and would turn into hard work. On that basis, I continue (haven't missed a month in 5 years now).

Since this is a donated effort, you must realize that I work a full time job in addition to this. The column does not really take all that much of my time to write (as some of you realize by my sometimes very disorganized writing). Now to the point. I would REALLY appreciate it if readers would not take advantage of the fact that my phone number is listed by calling me for help, advice, or answers to questions. As I have said before, your call forces me to give you my attention at YOUR convenience. If you want advice or help with something and you think I might be able to give you a hand, PLEASE take the time to write a letter. I need to add a plea to see that your address is somewhere on the LETTER, not just on the envelope. Envelopes tend to get lost or thrown away, and a couple of times when I was getting at answering a letter, I have found that I had no address to which to send the reply. (I hasten to add here that I don't have all the answers, and that some letters do, now and then, fall in a crack somewhere and do not get answered.) The advantage to you is that I can spend more time thinking about my reply. Perhaps I will see something of interest for all the readers and spend some time writing some code for you, that we can all share. The advantage to me is that I can answer your letter at MY convenience some night at midnight or 1 AM, when most of you wouldn't dare call me.

If you do call, be prepared for a very cool answer. A few weeks ago, I had a particularly trying week at work, and I had spent about 5 hours on the phone with various computer related conversations, all in the evening. When one poor unsuspecting reader called me on Saturday as I was on my way out the door on an errand, I was abrupt and just plain rude. The reader, of course had no way of knowing the circumstances, but he was kind enough to send me a letter outlining what he wanted to discuss on the phone. His response was not unkind in any way, and I am going to discuss his thoughts here. I'm sorry Kent for the way I answered the phone that Saturday. I apologize publicly.

## COCO Again

The letter was from Kent Meyers of Le Roy MN. It was in response to my discussion of the COCO in the November '84 Micro Journal. I had agreed with a reader's letter about Radio Shack providing poor documentation. Kent reminded me of the very nice service manuals available from Radio Shack for the computer and the old disk interface and pointed out that perhaps the reader who wrote to complain about lack of documentation on the new improved disk interface had bought the interface too soon and that the documentation simply was not yet published. Kent, you could be correct on that, and in fact probably are. I appreciate being reminded that the hardware documentation is available.

I could say something here about the software documentation being disorganized. Try finding out how to use the matching language routines mentioned in the manual in connection with reading and writing to a tape cassette, for instance. It seemed to me at the time I was looking at that, that Radio Shack included or left out information more or less by chance rather than by careful consideration.

Kent goes on to say that I was "off the mark on the question of using an 80 column terminal on the COCO. The problem of missed characters only shows up in the screen editors....". Kent indicates that he uses a line editor with the COCO. My goodness! I thought people quit using line editors 5 years ago. I certainly haven't used one to any extent for at least that time. Kent is saying that I have to use an external terminal on the COCO to get more than 51 characters on a line, and then I have to use a line editor? Forget it. I'm too spoiled by my SS-50 system! (Kent, there is the problem with the COCO and I. If you don't have a more capable system the COCO is great for learning about computing and computers.)

Kent says that the Tano Dragon corrected all the faults of the COCO. It used a built in 6551 ACIA "that completely cures the missed character syndrome". "Those who got the Dragon at the close-out price of \$139 were very lucky." Great! We have a computer that solves the COCO's problems but the manufacturer is not supplying it anymore.

"In the first paragraph about OS9, you use "multi-tasking" in reference to the use of an external terminal. This is more properly termed "multi-user". When I first got my copy of OS9, the first thing that I attempted was using the external terminal. When I discovered that it would not run reliably above 300 baud, I put OS9 on the shelf for a while... Now that I have the Oregon, though, I have begun to get into OS9... at 9600 baud with no missed characters."

I get the feeling that Kent is agreeing with my conclusions but not my terminology here. If I put an external terminal on the COCO so I can be the single user, how can you call that "multi-user"? I know what you are saying Kent, but the fact that OS9 won't let me switch to a single user mode is part of the problem. OS9 insists on running its CLOCK task and I can't shut off the "console" task that keeps the COCO keyboard live. I did find that I could reduce its allotted time slice to minimum for that task and run pretty reliably at 600 baud with an external terminal. The fact that the Oregon works so well is great for anyone who owns one, but sort of irrelevant to you COCO owners who don't have a Dragon.

You may recall that I did conclude the discussion by saying that the COCO is NOT a useless toy as some say it is. It is an excellent and inexpensive way to learn something about computing and computers. I said that you could try computing on for a fit, and if you didn't like it you wouldn't be out a large wad of money. Kent points out an additional nice feature of owning a COCO. With reference to that, Kent said "If he shops wisely, when he gets rid of his COCO all of his peripherals (disk drives, printer, etc.) can be transported and used on his new system. The only Color Computer specific components would be the computer itself and the disk controller."

Kent concluded his letter by saying that he wouldn't bother me by phone again. I replied "now if I could only get several hundred other people to understand..."

## Used Equipment

I recently found a used Centronics 737 printer for a reasonable price. It has a proportionally spaced type font and it is one of the printers for which STYLO is

configurable. It does an excellent job with text via STYLO. Only problem with the 737 is that for some unknown reason, Centronics didn't bother to build it so it would recognize a formfeed character and feed to the top of the next page. Good Grief, a printer that does beautiful proportional spacing of text and won't page! Printer Driver to the rescue! I spent the evening writing a printer driver that counts linefeeds and feeds to the top of the next page when a \$OC (formfeed) command is sent to it. That is, if the driver is on line 50 and it sees a formfeed, it outputs 16 linefeed characters to feed the paper to the top of the page. The printer driver initializes with a line count of zero. If you power up with the paper properly aligned, you never could guess that the printer doesn't honor formfeeds. While I was at it, I threw in the left margin feature that I had done in a printer driver before, and wrote a PAGE command for the printer so you can always feed a page to tear off a listing that is in the printer. On the basis that two or three of you might want this utility, I'll send hard copy to anyone who writes and asks, or copy the source to a disk you send me, with return postage. I have a version of FLEX that has a hole at SCB00 so I put the driver overflow there. I still use the "old" two file method for printing. The P.CMD file loads the PRINT.SYS file. I know, some of you laugh at this, but it nicely separates the things that are common to all print drivers out into the P.CMD so that my "Individual" print drivers can be short and simple. I see no reason to change the system but you can add the functions of P.CMD to my PRINT.SYS file if you like.

I now have print drivers for IDS Paper Tiger, Heath H-14(serial), Epson MX-80 (or 100), RX-80 (not the same as MX-80), Centronics 737 and a couple other printers that have come and gone here and at work.

Still in the line of used equipment, I got a friend and a used SS-50 bus computer together recently. The friend didn't want to spend the price of a terminal in addition to the computer, so he found a used GIMIX Video board. I volunteered to get it working with a monitor and a serial output keyboard that was available.

It soon became apparent that I would have to interface the video board at the monitor level (i.e. modify SBUG-E to jump to the appropriate routines for outputting a character to the terminal, inputting a character with echo, and for initializing the board on power up). I succeeded reasonably well in these goals. First, the board needs 2K of memory somewhere. I had added enough decoding to the old mother board so I could put the video RAM at \$E800 to \$EFF. The control ports conveniently fit on the four address per port system at \$E020. They would fit on a 16 per port system at \$E200. I decided to experiment with drivers written in PL9 and first simply wrote a test program to input characters from the terminal and echo them to the video board. The board is not the ultimate in convenience like some of the newer boards that look just like a terminal. It has a cursor row register, a cursor column register, a "last row" register and a general control register. You must program the character set to be used (though one is in ROM so you don't have to design a type font).

I found it straightforward to write a fairly large PUTCHAR procedure that handles the common case of outputting a printable character in an express mode and takes more time as required to look at control characters and handle them properly. The cursor position bears no relation to where on the screen the next character will appear. That is only a function of which memory location you write into. Keeping the two in sync at end of lines, when wrapping around from end to start, and controlling scrolling via the last line register were a little challenge, though the greatest challenge was to make the routine character handling as fast as possible. When I was done, the test was to fill the screen with characters while in a screen editor and then move the window by one screen. The screen was

rewritten in just about 1 second, so I figure the effective character handling to be near 19,200 baud, up there with the fastest of serial terminals. The drivers have software to interpret commands to place the cursor anywhere on the screen, clear the screen, backspace, and erase from cursor to the end of the current line. I made the commands the same as one of my terminals so I can help the friend configure his software to work properly with it.

#### STAR-OOS

I understand that a review of STAR-DOS is underway, which may or may not appear before this column, so this is NOT a review. However, I must give you my impressions of STAR-DOS. About a month ago a package arrived in the mail containing a disk on which was a version of STAR-DOS that would run on one of my computers. Peter Stark had sent it to me for comments and/or the finding of any remaining incompatibilities with FLEX. For those of you who have not been following the ads, I had better back up a little. STAR-OOS is Peter Stark's answer to FLEX. It was written from scratch, but done in such a way that nearly all software that was written to run under FLEX will run under STAR-DOS. In case you hadn't guessed, STAR KITS and Peter Stark are synonymous.

I unpacked the disk and booted STAR-DOS to find a strange prompt in place of the familiar \*\*\* that I have been looking at for so long. STAR-DOS: was the prompt that I saw. Any of you who have followed my column and my reviews for any length of time know my "luck" at finding bugs in software the first time I try to run it. STAR-DOS was no exception in the broad sense... What I mean is that there were still some differences between what STAR-DOS does and what FLEX does. For example, the first thing that struck me was that I couldn't specify a filename as I have always done with FLEX as FILENAME.EXT.1. STAR-DOS insisted on the other form acceptable to FLEX, namely 1:FILENAME.EXT. Peter happened to call me and ask if I had gotten the package and when I mentioned this to him he said something like "I've always done it the other way. I didn't know FLEX accepted the drive number last." When I assured him that the flex manuals have documented both forms of file specifications ever since the first Miniflex manual I have, he indicated that he would fix STAR-DOS to accept either form, and he has done that.

I had a couple other difficulties not worth mentioning since they too have been fixed. When I complained about the STAR-DOS: prompt and indicated that I wished it possible to change it to \*\*\*, Peter wrote a "PROMPT" utility that lets you change it to whatever you like. I've been impressed at Peter's willingness to track down and "adjust" any little difference between STAR-DOS and FLEX with regard to how they handled various situations. I use the word "adjust" because "fix" might imply a problem, and the little things I found were not problems, just differences between the way the two operating systems handle certain situations.

STAR-DOS comes complete with an assortment of disk utilities and a user manual. There is an "Installation" manual available at extra cost for those users who want to install STAR-DOS on different 6809 hardware, i.e. with different I/O port addresses or driver requirements. That manual is a very well written step by step procedure for getting STAR-DOS up and running on any 6809 hardware, and with any disk controller. Of course, a project as involved as writing new disk drivers is not for the computer beginner, but someone with some experience programming in Assembler would have no trouble getting the job done.

Peter has implemented one cute feature that he (and I) have found to be valuable. If you have a calendar clock board in your computer and can write an assembler program to read hours and minutes and convert them to one byte that codes hours and tenths on a 24 hour basis

(eg. 10:30 PM would be 225, 22 for the hour and 5 for the .5 hour) you can link that code to STAR-DOS so that whenever you write a file to the disk it will have not only the normal date information, but the time as well (to within 6 minutes). A utility that takes advantage of this is supplied with STAR-DOS. It is called TCAT. TCAT is a catalog utility that lists the files on the disk in reverse order of date and time. The result is that the last file you worked on is listed at the top of the catalog! If you are like me, and hit the sack at 2:30 AM after working on computing projects, the next night you say "let's see, what was I working on last?" You type TCAT and there it is before your eyes. Nice...

Though at this point I am quite sure there are not very many incompatibilities left, there may still be a few. What is impressive to the point of outweighing the possibility of a few incompatibilities, is Peter's eagerness to "adjust" these. There is one "problem" that remains that could be a deterrent to your being interested in STAR-DOS just yet. It does not support random files at this point. The next version will have that feature added. If you run software that uses random files, you might want to hold off on STAR-DOS for a while.

I should mention that Peter has several versions of STAR-DOS designed to run with various disk controller configurations. One in particular is very nicely compatible with the Peripheral Technology PT-69. When I mentioned that I had a PT-69 running, Peter sent me that version to try out, and it works quite nicely.

Now to get into trouble with TSC... Though the good folks at Technical Systems Consultants who wrote FLEX a few years back have staunchly maintained that they intend to "support" FLEX for the foreseeable future, it seems to me that they have done little more than elevate the price for FLEX to new heights. I can think of only one new software product from them that runs under FLEX that has been released in the past two years. That is their Relocatable Macro Assembler. It was released in Uniflex version a couple of years before the FLEX version finally became available. Let's face it. TSC has gone on to bigger and better things, and more power to them in their new endeavors. May they all get rich and famous. Had it not been for FLEX, the 6809 would be known only to industrial users running Motorola development systems writing programs for control applications.

The facts of the present are, however, that TSC has all but abandoned the FLEX market. Virtually all of the new software products to run under FLEX are from new and/or small software companies. To be a little realistic about things, as long as they can sell a copy of FLEX for \$250 without even advertising it, they are not going to announce that they are no longer supporting it. Now we have an alternative operating system and at a very attractive price, including very fine documentation.

See STAR-KITS or S.E. Media advertising, for pricing and ordering information - STAR-DOS/STAR-DOS+.

#### BBS Systems

A further note from Kent Meyers included the following. "I would also like to see some mention made in the Micro Journal of the only two BBS systems in the United States that are dedicated to the dissemination of Public Domain Software for 68XX computer systems. One is located in Oklahoma City, OK and the other is in Hawthorne, CA. The Flexnet system in Oklahoma City has been on line for about two years (405-728-7654) and has a special file transfer protocol to allow error free transmission of files. The California system is just coming on line (213-539-7619) and plans to offer both the Flexnet and the CP/M Xmodem protocols for file transfer. The interesting thing about this system is that it is running on a CP/M computer."

#### RX and TX

I've done a few more things to the programs USEND and UREC published here a couple of columns ago. In using them for a while, it became apparent that they were a little inconvenient in several ways. First of all, if I had several files to transmit, I had to go back and forth between the two system's terminals to type in all the proper commands. First step was to add some code to interpret a list of files on the command line at the transmit end and to transmit the filename to the receive end. I simply set up the File Control Block at the transmit end and sent the 11 characters starting at the 5th (filename and extension) to the receive end where they were placed in an FCB. The receive end then attempts to open the file and reports to the CRT the fact that the file already exists, asking if it may be deleted. That was fine when I didn't attempt to send a string of files to update existing ones on the receive end, which still necessitated typing on both terminals. The final improvement was to send the delete question back to the sending end and allow it to be answered from there. I also found that my input routines neglected to mask off the parity bit, which went undetected until I happened to try them on a system with a terminal that had parity enabled. The character input GETCHAR and GETIF routines are now properly set up to AND off the parity bit just in case.

Now I can set up a string of filenames in the command line and go at copying them from system to system. I only have to type RX at the receive end, and RX stays alive until it sees an escape at the receive end. I can therefore use TX a number of times without ever going near the receive end.

I'll include the present listings of these utilities here for your information or use. I am not yet quite finished with them, however. The final version will read the directory at the send end and do string matching just as the various versions of copy utilities do. In other words, TX .TXT will transmit all files with the extension .TXT. TX TEST will transmit TEST1, TEST2, TESTFILE, etc. I didn't mention above that presently the filenames appear on the terminals at each location as the files are opened.

I suppose that out of the SS-50 bus owners that read this, perhaps a couple hundred of you will have PL/9 and of those, two or three might have two systems. If anyone does actually use these, I'd like to know what you think of them. I think they are a good demonstration of programs that access the hardware of the system without resorting to assembler code in any way.

---

```

/* PROGRAM TO SEND A TEXT OR BINARY FILE VIA WIRE.
   BINARY FILE IS CONVERTED TO ASCII FOR TRANSMISSION.
   THIS HAS BEEN TESTED UP TO 9600 BAUD TRANSMISSION RATE.
*/

ORIGIN = 0;
STACK = $80FF;

CONSTANT
  CR = $0D,
  LF = $0A,
  $IN = '$',
  START = '$S',
  TIT = '$T',
  DELRO = '$D',
  SERBEG = '$',
  FILERO = '$F',
  READY = '$R',
  EOC = '$06',
  WAITING = '$03';

AT $E000 BYTE NODEM(2);
AT $E004 BYTE TEOR(2);

```

```

INCLUDE TRUFALSE.DEF.1;
INCLUDE FLEX.LIB.1;

PROCEDURE PUTCHAR(BYTE .DEVICE; BYTE CHAR);
REPEAT UNTIL DEVICE(0) AND 2
DEVICE(1)=CHAR;
ENDPROC;

PROCEDURE CRLF;
PUTCHAR(.TERM,CR);
PUTCHAR(.TERM,LF);
ENDPROC;

PROCEDURE PRINTBYTE.DEVICE, .STRING$; BYTE N;
N=0;
WHILE STRING(N)
BEGIN
PUTCHAR(.DEVICE,STRING(N));
N=N+1;
END;
ENDPROC;

PROCEDURE GETCHAR (BYTE .DEVICE);
REPEAT UNTIL DEVICE(0) AND 1;
ENDPROC BYTE !DEVICE(11 AND $7F);

/* GETIF RETURNS CHAR IF ONE HAS BEEN INPUT, ELSE RETURNS NULL.
THIS PROCEDURE DOES NOT WAIT FOR A CHARACTER */

PROCEDURE GETIF (BYTE .DEVICE);
IF DEVICE(0) AND 1 THEN RETURN BYTE (DEVICE(11 AND $7F));
ENDPROC BYTE 0;

PROCEDURE BINASC(BYTE CHAR, .UNIBBLE, .LNIBBLE);
LNIBBLE = CHAR AND $0F;
UNIBBLE = SHIFTCHAR,-4 AND $0F;
IF LNIBBLE < 10 THEN LNIBBLE = LNIBBLE + $30
ELSE LNIBBLE = LNIBBLE + $37;
IF UNIBBLE < 10 THEN UNIBBLE = UNIBBLE + $30
ELSE UNIBBLE = UNIBBLE + $37;
ENDPROC;

PROCEDURE SEND_FILENAME(BYTE .FCB1:BYTE N,CHR;
N=4;
WHILE N IS
BEGIN
CHR = FCB(N);
PUTCHAR(.MODEM,CHR);
IF CHR>0 THEN PUTCHAR(.TERM,$20) ELSE PUTCHAR(.TERM,CHR);
IF N=11 THEN PUTCHAR(.TERM,'.');
N=N+1;
END;
PUTCHAR(.TERM,$20);
ENDPROC;

PROCEDURE SEND I BYTE CH, CH1, CH2, BIN_FILE, INFILE(320), N;
INTEGER COUNT;
/* INITIALIZE MODEM PORT */
MODEM(0)=3;
MODEM(0)*=$15; /* INITIALIZE DIVIDE BY 16 CLOCK */
CRLF;
REPEAT
REPEAT
GET_FILENAME(.INFILE);
IF CCR AND 1 THEN /* NO MORE FILES ON INPUT LINE. */
BEGIN
CRLF;
PRINT (.TERM,'FINISHED TRANSFER');

```

```

FLEX;
END;
SET_EXTENSION(.INFILE,1); /* DEFAULT TEST FILE */
PUTCHAR(.MODEM,SENDREQ); /* REQUEST TO SEND */
REPEAT
CH = GETCHAR(.MODEM);
UNTIL CH = $1000; /* LOCK UP HERE IF RECEIVE NOT READY */
SEND_FILENAME(.INFILE);
REPEAT CH = GETIF(.MODEM) UNTIL CH <> 0;
IF CH = DELBS THEN
BEGIN
PRINT (.TERM,"DELETE EXISTING FILE? ");
CH = GETCHAR(.TERM);
PUTCHAR(.TERM,CH);
PUTCHAR(.TERM,$20);
PUTCHAR(.MODEM,CH);
IF CH <> 'Y' AND CH <> 'y' THEN
BEGIN
CRLF;
CLOSE_FILE(.INFILE);
END;
END;
UNTIL CH = READY .OR CH = 'Y' .OR CH = 'y';
IF CH <> READY THEN REPEAT CH = GETIF(.MODEM) UNTIL CH = READY;
OPEN_FOR READ I,.INFILE;
IF INFILE(1)<>0 THEN
BEGIN
REPORT_ERROR(.INFILE);
FLEX;
END;
CH = READ(.INFILE);
IF CH = $02 THEN
BEGIN
SET_BINARY1(.INFILE);
BIN_FILE = TRUE;
PUTCHAR(.MODEM,BIN);
END
ELSE
BEGIN
BIN_FILE = FALSE;
PUTCHAR(.MODEM,TITI);
END;
REPEAT CH=GETCHAR(.MODEM) UNTIL CH> START;
IF BIN_FILE THEN BINASC(CH, .CH1, .CH2);
COUNT = 0;
WHILE INFILE(1)=0
BEGIN
REPEAT
IF BIN_FILE THEN
BEGIN
PUTCHAR(.MODEM,CH1);
PUTCHAR(.MODEM,CH2);
COUNT = COUNT+2;
END
ELSE
BEGIN
PUTCHAR(.MODEM,CH1);
COUNT = COUNT + 1;
END;
IF COUNT AND $00FF = 0 THEN PUTCHAR(.TERM, 0);
CH = READ(.INFILE);
IF BIN_FILE
THEN BINASC(CH, .CH1, .CH2);
UNTIL COUNT = 10000 .OR INFILE(1)<>0
IF INFILE(1)=0 THEN
BEGIN
PUTCHAR(.MODEM,WAITING);
COUNT = 0;
REPEAT UNTIL GE11F(.MODEM)= START;
END;
END;
IF INFILE(1)<>0 THEN REPORT_ERROR(.INFILE);
CLOSE_FILE (.INFILE);
PUTCHAR(.MODEM,ENDI);
CRLF;
REPEAT CH=GETCHAR(.MODEM) UNTIL CH = START;
FOREVER;

```

```

/* PROGRAM TO RECEIVE A TEXT OR BINARY FILE VIA WIRE.
   BINARY FILE HAS BEEN CONVERTED TO ASCII BY SEND PROGRAM, AND
   THIS PROGRAM CONVERTS IT BACK TO BINARY FORM.
   PROGRAM HAS BEEN TESTED UP TO 9600 BAUD WITH NO DIFFICULTIES
*/



ORIGIN = 0;
STACK = #0FF;

CONSTANT
  CR = $0D,
  LF = $0A,
  BTM = '$',
  START = 'S',
  TST = 'T',
  SENDRD = '4',
  FILERD = 'F',
  READY = 'R',
  DELRD = 'D',
  END = '$04',
  WAITING = '$05';

GLOBAL
  BYTE OUTFILE(320);

AT $1000 BYTE DATA1(10010);
AT $E000 BYTE MODEM(2) /* 6850 MODEM PORT ADDRESS */;
AT $E004 BYTE TERM(2); /* 6850 TERMINAL PORT ADDRESS */;
AT $E00C BYTE WORKRV; /* WORKING DRIVE NUMBER */;

INCLUDE TRUFALSE.BEF.1;
INCLUDE FLE1.LIB.1; /* FLEX FILE HANDLING INTERFACE */;

PROCEDURE PUTCHAR(BYTE .DEVICE: BYTE CHAR);
  REPEAT UNTIL DEVICE(0) AND 2;
  DEVICE(1)=CHAR;
ENDPROC;

PROCEDURE GETCHAR (BYTE .DEVICE);
  REPEAT UNTIL DEVICE(0) AND 1;
ENDPROC BYTE(DEVICE(1)) AND $7F;

PROCEDURE PRINT(BYTE .STRING);BYTE N;
  N=0;
  WHILE STRINGIN
  BEGIN
    PUTCHAR(.TERM,STRING(N));
    N=N+1;
  END;
ENDPROC;

PROCEDURE CRLF;
  PUTCHAR(.TERM,$0D);
  PUTCHAR(.TERM,$0A);
ENDPROC;

PROCEDURE ASCBIN(BYTE UNIBBLE, LNBINBLE);
  IF UNIBBLE > $40 THEN UNIBBLE = UNIBBLE - $37
    ELSE UNIBBLE = UNIBBLE + $30;
  IF LNBINBLE > $40 THEN LNBINBLE = LNBINBLE - $37
    ELSE LNBINBLE = LNBINBLE + $30;
ENDPROC BYTE(SHIFT(UNIBBLE,6) + LNBINBLE);

PROCEDURE GETFILE_MODEM: BYTE N, CHAR;
  PUTCHAR(.MODEM,FILERD);
  OUTFILE(3)=WORKRV;
  N=0;
  WHILE N < 15
  BEGIN
    CHAR = GETCHAR(.MODEM);
    OUTFILE(N)=CHAR;
    IF CHAR = 0 THEN PUTCHAR(.TERM,$20) ELSE PUTCHAR(.TERM,CHAR);
    IF N = 11 THEN PUTCHAR(.TERM,'.');
    N=N+1;
  END;
ENDPROC;

N=N+1;
END;
PUTCHAR(.TERM,$20);
ENDPROC BYTE 0;

PROCEDURE GETF(Byte .DEVICE);
  IF DEVICE(0) AND 1 THEN RETURN Byte (DEVICE(1)) AND $7F;
ENDPROC BYTE 0;

/* MAIN PROGRAM STARTS HERE */

PROCEDURE REC_LOCAL : BYTE CH, CH1, BIN_FILE;
  INTEGER INDEX,LIMIT,BYTE;
  CRLF;
  MODEM(0)=3;
  MODEM(0)=$15; /* INITIALIZE DIVIDE BY 16 CLOCK */;
REPEAT
  REPEAT
    CH = GETF(.MODEM);
    IF CH = 0 THEN
      CH = GETFL.TERM;
    UNTIL CH = SENDRD .OR. CH = $10;
    IF CH = $10 THEN
      BEGIN
        CRLF;
        PRINT "EXIT RECEIVE MODE";
        CRLF;
        FLEX;
      END;
    GETFILE_MODEM;
    OPEN_FOR_WRITE (.OUTFILE);
  END;
  IF OUTFILE(1)=3 THEN
    BEGIN
      PRINT("DELETE FILE? ");
      PUTCHAR(.MODEM,'D'); /* DELETE FILE? */
      CH = GETCHAR(.MODEM);
      PUTCHAR(.TERM,CH);
      CRLF;
      IF CH = 'Y' .OR. CH = 'y' THEN
        BEGIN
          DELETE_FILE (.OUTFILE);
          OPEN_FOR_WRITE(.OUTFILE);
        END ELSE CLOSE_FILE(.OUTFILE);
      END;
      UNTIL CH = SENDRD .OR. CH = 'Y' .OR. CH = 'y';
    IF OUTFILE(1)<>0 THEN REPORT_ERROR(.OUTFILE);
    PRINT "READY TO ACCEPT ";
    CRLF;
    INDEX = 0;
    LIMIT = 0;
    PUTCHAR(.MODEM,READY); /* TELL SENDER READY */;
    CH = GETCHAR(.MODEM);
    IF CH = BIN THEN
      BEGIN
        SET_BINARY(.OUTFILE);
        BIN_FILE = TRUE;
      END
      ELSE BIN_FILE = FALSE;
    PUTCHAR(.MODEM,START);
    REPEAT
      CH = GETCHAR(.MODEM); /* WAIT FOR A CHARACTER */;
      CH = CH; /* SAVE IT */;
      IF CH <> END .AND. CH <> WAITING
        THEN
        BEGIN
          DATA(INDEX) = CH;
          INDEX = INDEX+1;
        END
        ELSE LIMIT = INDEX; /* ONE PAST LAST VALID CHAR */;
      IF LIMIT>0 THEN
        BEGIN
          PRINT " WRITING TO DISK";
          PUTCHAR(.TERM,$0D);
          INDEX=0;
          WHILE INDEX < LIMIT
          BEGIN
            OUTFILE(INDEX)=DATA(INDEX);
            INDEX = INDEX+1;
          END;
        END;
      END;
    END;
  END;
END;

```

```

IF BINFILE THEN
BEGIN
  CH = ASCIICDATA(INDEX).DATA[INDEX+1];
  INDEX = INDEX+2;
END
ELSE
BEGIN
  CH = DATA[INDEX];
  INDEX = INDEX+1;
END;
WRITEL(outfile, CH);
END;
PRINT"          ";
PUTCHAR(TERM, $ODN);
PUTCHAR(TERM, $DAA?);
LIMIT?;
INDEX=0;
END;
UNTIL CH = END;
CLOSEFILE(outfile);
FOREVER;

```

## OS9 USER NOTES

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### Perspective

Depending on my frame of mind it's right before final exams or between Thanksgiving and Christmas. In the before finals frame I know there's no time to write a program for this column. The other frame tells me that this is a good time to think about where OS-9 has been, and where it's going.

Only one important product has come from Microware this year, OS-9 68K. I don't have a computer with a 68000, and it's too early for my network to have pulled in any solid information. OS-9 68K should have an important influence on the future of OS-9, so I'll comment on it in this column, but I'll be mostly guessing.

Only one non-Microware program comes to mind as an important new program, Sculptor (used to be Sage). I don't have it either, but I have played with it. If it were inexpensive, Sculptor could have an important effect on OS-9. Sculptor is, however, very expensive. \*\*

Since Thanksgiving was in the recent past I'll count blessings.

There must be something special about OS-9 and the systems it runs on. We haven't all run off to the MS-Dos camp.

One nice thing about OS-9 is obvious from where I sit. On my left is my CoCo. It cost about \$1000 and is mostly compatible with the \$10,000 Gimix on my right. The CoCo moves along nicely. The Gimix is one of the most powerful microcomputers I know of. If that isn't enough (and of course it isn't), 68010 and even 68020 machines that run OS-9 68K are way beyond the twinkle in the eye stage. Just the 6809 spectrum, from the sub-megahertz CoCo to the 3-Megahertz chip that's supposed to be available next year, gives OS-9 systems an exceptional scope. The addition of the 68000 chips runs the highest performance OS-9 systems up near mainframes.

OS-9 is a lot like Unix. It borrows enough from Unix that I am sometime (momentarily) confused about which system I'm using. Still, OS-9 is not Unix or even a Unix look-alike. In a way that's sad; we can't just plug in all that Unix software. In another way it's an advantage; we have better performance than a Unix look-alike would, real-time capabilities that would be hard to duplicate under Unix, and reliability that wasn't designed into Unix. OS-9 doesn't look like Unix, but it is close enough that I can develop programs on my micro and move them to Unix with few changes. Moving programs in the other direction is almost as easy.

Usually the thin supply of software for OS-9 seems like a disadvantage, but even that problem has a bright side. Even with all the new CoCo OS-9 users counted in we're a small group. Too small for the big software houses to notice. If you need Framework and you need it NOW, sorry, OS-9 isn't for you; but if you are content with something a bit behind the cutting edge maybe this is the right place. It's a programming truism that analysis and design are the hard parts of creating a program. It's also true that the 808x class of micro-processors are tricky to program.

If you're a individual programmer you'll have a hard time beating the software houses in the PC market, but the big software houses don't come over here. The PC folks paid the bill for the analysis and design of lots of nice programs. The best of them were written in 808x assembler. If you combine the best of the MS-Dos database, or spread-sheet, or word processing, or communications programs you can avoid the analysis and trial-and-error that was done for the original programs. Then you can code up the result in assembler for the 6809 or 68000.

I don't think there are quite enough of us yet to support a few full-time free-lance programmers, but we're getting close. When we get there a few of the best OS-9 programmers should be able to make a tidy living coding up standard programs for us. For programmers this is a great opportunity. For everyone else? When the programs arrive they will be running under OS-9. OS-9 does things that MS-Dos doesn't.

### The View from the Ivory Tower

I don't know much about process control. That's a shame because process control is what many OS-9 systems do. I've read about it a lot, and I know a little about the underside of the computers that control the laser fusion project at the University of Rochester's Laser Lab. This is just enough knowledge to be dangerous, but I'm going to bravely (foolishly) point at an area that OS-9 isn't in but could be.

For years only two versions of OS-9 have been available, Level One and Level Two. They are different, but the difference is mostly that Level Two can handle more processes than Level One. Now that OS-9 68K is available engineers can chose OS-9 for a whole new kind of application. Robotics and "simple" vision require serious number crunching and large amounts of memory. These were out of the range of the 6809 but the 68000 can reach well into the simpler areas.

I surely don't need to tell people about the power of the 68000. What the hardware vendors don't seem certain about is the advantages of combined 68000/6809. Multiple 6809's are well understood. I/O processors make an important difference in throughput on 6809-based systems. They'll be at least as important on 68000's.

The suggestion I'd like to make is that a 6809 in a 68000-based system need not stop at simple I/O processing. A heavy interrupt load hurts the performance of a 6809 and it is good at handling them. The 68000 isn't nearly as good with interrupts as the 6809. So interposing a 6809 between a 68000 and as many sources of interrupts as possible is a good idea.

Instead of making a list of all the areas where a 6809 is more cost effective than a 68000 let me suggest a job they could do nicely as a team: I don't like sorting my returnable bottles and cans out by what used to be in them. A machine should be able to do it for me. The machine would watch a conveyor belt running by it. It would find each container as it went by and figure out what used to be in it, then it would knock it into the appropriate bin.

There are two parts to this problem: vision and manipulation. The vision part is hard. The manipulation (getting the container off the belt and into the bin) relatively easy. Just finding bottles and cans lying on a belt at any odd angle would be challenging for a 68000 if the job had to be done quickly. Recognizing characteristics like shape and features on the label might put the problem right out of a 68000's range. The conveyor belt will probably have to be slowed until the program can recognize most of the containers as they go by. Operating a tool that knocks containers into bins would be easy for a 68000 fast enough to handle the vision problems, but that processor is already running flat out peering at the passing rubbish. A 6809 could easily snag information from the 68000 and babysit the stepper motors and solenoids.

Here's the trick. There's nothing in the OS-9 model of the world that says that all processes have to be running on the same processor or using only common memory. They don't even have to be running on the same kind of processor. The requirements are that some memory must be shareable between processes (for shared modules), there must be a way to copy data from one processor's memory to another's, and each process must seem to be able to reach OS-9 with a software interrupt.

I'm sure there are problems hidden in this simple idea, but think how easy it would be to work with the system. Start a 6809-object-code camera handler, a 68000-object-code vision program, and a 6809-object-code manipulator operator. Let the camera handler and the vision share a data module with the bit-image of the scene in it. Run a pipe from the vision program to the manipulator operator.

I don't know of any microcomputer system that lets you run two processors together that smoothly. There are some hardware problems, but nothing the S100 vendors haven't already dealt with. Software is the core of the problem and we are already using an operating system who's design doesn't rule out teamwork.

#### A Simple C Function

I hadn't planned to put any code in this month's column, but I was thinking about the trouble I had raising a process's priority from inside. Some of you might be able to use my solution. I'm afraid that this is one place where Level Two is easier to work with than Level One. I only worked it out for Level Two and the conversion will be tricky.

The problem is that OS-9 has a SVC that sets a process's priority to any number you choose, but you can't ask it to raise the priority by 10. Nor is

there a simple way to ask for a process's current priority. A Level Two user has to use the F\$OPrDsc SVC to get a copy of the process's entire process descriptor and pick the priority out of there. Then he can use F\$SPrior to set the priority to that number plus the required change. That's just what one of the functions I included with this column does. The other function, Getpr, returns the current priority without changing it.

#### A Call from the Oracle

I just heard from Ken Kaplan. I persuaded him to make some prognostications about OS-9's future. You should view them in whatever you think is the correct light for remarks about the future of OS-9 from the president of Microware.

Ken believes that within the next two or three years OS-9 will grow to be bigger than Unix. When I sounded doubtful he admitted that his best evidence is covered by non-disclosure agreements with Microware's customers. He did point out that OS-9 is already bigger than Unix in Japan (which may become a key source of computers). If you count CoCos, Ken observed that more computers run OS-9 than Unix already (then he admitted that that's not all that significant). The important arguments stayed hidden.

Ken thinks we'll see at least two new major personal computers in United States running OS-9. Major means real mass-market machines like IBM or Sperry (those vendors are my suggestions, not his).

We can expect Fortran for OS-9 next year. It will be available on the 6809 first.

Networking for OS-9 is already available from Fujitsu on the FM-11. If I lived in Japan I could buy it today. Ken expects it to become available here soon. It sounds like pretty reasonable networking. It runs over any kind of connection and allows a user on the network to use devices on any connected computer.

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\*\* Editor's Note: Because of the expense of 'SCULPTOR' or 'SAGE' (the only name we have ever seen it run under), and the 'bugs' we saw in versions demonstrated to us, caution should be used in ordering a thousand dollar plus a couple hundred dollar piece of software.

I have received telephone reports that the latest versions are running fairly bug free. Fact is a few 'glow'. However, like all other major products advertised or reported on or in 68 Micro Journal, until we 'actually see' the fixed product, I will recommend caution to prospective purchasers.

We have received 'promises' for the past two or three years that a review copy was on the way, but as yet has not arrived. If and when a working version should arrive you can be certain that I will have a report for you. The only verified reports I have so far, indicate the software has certain failures that should not be present in such an expensive package. These reports are a couple of years old, but are all that I have from users who have purchased the product and have nothing to gain from 'hyping'. Until I know better - 'CAUTION' IS THE WORD!

DMW

- - -

```

1 #include <os9.h>
2 #define PRIORITYOFFSET 10
3 #define TRUE 1
4 #define FALSE 0
5
6 static char pdesc[512];
7
8 modpr(pid, delta)
9 int pid, delta;
10 {
11     register int priority;
12     if((priority = getpr(pid)) != -1)
13         return(setpr(pid, priority+delta));
14     else
15         return(-1);
16 }
17
18 getpr(pid)
19 int pid;
20 {
21     if(getpd(pid))
22         return(pdesc[PRIORITYOFFSET] & 255);
23     else
24         return(-1);
25 }
26 getpd(pid)
27 int pid;
28 {
29     struct registers reg;
30
31     reg.rg_a = pid;
32     reg.rg_x = pdesc;
33
34     if(i_os9(F_GPRDSC, &reg) == 0)
35         return(TRUE);
36     else
37         return(FALSE);
38 }

```

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Dear Mister Dibble,

Being a Micro Journal reader from the very first hour, I really like your OS9-column very much. This also was one of the reasons for adapting OS9 to my old autpc-box, after having used FLEX for many years meanwhile. With my adaptation of OS9 to a not supported system, I ran into some problems.

Since support of OS9 at this side of the atlantic is almost very close to zero, I wonder whether you could give me some info to complete my OS9-project?

First my hardware:  
I run a autoc system, using MP-A9 cpu board with 1 Mhz, about 150 Kram, using the UDISK-package. 2 five inch drives using the DC4-controller from Southwest (MF68), 2 eight inch drives using the DMA2-controller also from southwest.  
All of this runs under FLEX-9.0 without any problem.

Recently I bought OS9-level 1, version 1.2 from a local authorized microware dealer. I installed the package on my system. It works great. So I have both FLEX and OS9 available now.

This all works fine with the 8 inch drives, using the DMA2 controller.

My intention now is also to use the 5 inch drives under OS9. I might proceed in two steps: first using the drives like under FLEX by running the 'USEMF' utility, so in this way I would have 4 drives in total to my system. Next step must be to bring up a bootable system, only with the 5 inch drives.

#### Now the problems

Microware doesn't support southwest products anymore. In the past there was a version available for the older DC3-controller from auto. Unfortunately for my DC4-controller, there is nothing at all from microware.

During a recent visit in the states I bought the DC3-drivers, which from my point of view must be very similar to the DC4-drivers. Although the DC3 uses the FD1771-chip and the DC4 uses the FD1779-chip as disc-controller.

As described on the disk I created a disk with the DS9800T, SYB, CMOS and DEFS directories. I put the DC3 drivers, boot module and device descriptors under a separate directory, called DC3. I copied the SYBDEFS into the DEFS-directory as described.

Trying to assemble I get the message ER00R-can not open the DEFSFILE. So apparently I am doing something wrong. So after several evenings of frustration meanwhile I gave up (!) for now, which finally ended up into this letter to you, hoping that you could give me some info how to proceed....

An other request to you would be to supply me with an example how to connect an printer to OS9 softwarewise, by using the ACIA-driver. But my minifloppy problem has priority number one, so I will not bother you with too much items at the same time.

I would very much appreciate if you could take some time to give me some helpful info. Maybe someone at your side integrated a DC4 controller already in his system(?), who knows. And I am trying to reinvent the wheel!?

I insert some irc's, hoping this will cover at least your postage. If not please do not hesitate to let me know.

Sincerely,

Raymond Casneuf

Dear Mister Casneuf,

I remember having problems with something called DEFSFILE when I first assembled some source I bought from Microware. Instead of explicitly including each definition file in an assembler program, they build one file containing all the necessary USE statements and simply use that file. My DEFSFILE looks like:

```

*level set 1
level set 2
use /H0/DEFS/OS9Defs
ifeq level-1
use /H0/DEFS/OS9Sysdefs.ll
else
use /H0/DEFS/OS9Sysdefs.lli
endc
use /H0/DEFS/OS910Defs
use /H0/DEFS/OS9RBFDefs
use /H0/DEFS/OS9SCFDefs

```

Notice that I can select the definitions files for Level One or Level Two by changing one line. If I frequently assembled programs for Level One, I'd set the level in the program instead of the defslst file. Always including all those files makes my life easy; I don't have to worry about what file includes what symbols. If you have memory problems you may want to cut down yours defslst file. Another approach would be to skip it. Remove the "use defslst" statement from the program and replace it with a few "use" statements for definitions files.

It should be easy to add support for your printer. All you need is an appropriate device descriptor. Since you mentioned ACIA, I assume your printer has a serial interface. You may need to generate a new device descriptor for the printer. First check the device descriptors you already have. If you have one called something like PR it may be a serial printer. The device P is a parallel printer by convention. If you have PR, you may need to change its device address. You can do this with debug, see the System Programmer's Manual for a map of the device descriptor so you'll know what to alter.

If you don't already have a device descriptor that almost fits, you'd be best off assembling a new one.

Page 4-9 in the System Programmer's gives an example of a SCF device driver you can follow.

I believe I have heard from other people working on support for the DC4. I would imagine that printing this letter will be the best way to let them know about you.

Pete

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## "C" User Notes

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### INTRODUCTION

This month's column continues the definition of O'Reefe's string-handling library started in an earlier column.

### STRING-HANDLING IN C

The "strn" family of string-handling functions does not allow arbitrary contents of strings, as the processing is controlled by the terminating nulls in each string, or by the specified length. The consistent use of this family of functions would prevent many of the problems associated with null-terminated strings which have somehow lost their terminated nulls, as the length specification provides a non-data-dependent termination condition. It may also create new problems, in that the standard C libraries assume null-terminated strings, which must be assured by the programmer.

`strncat(dst, src, n)` copies up to "n" characters of "src" to the end of "dst". A null character is placed at the end of "dst" if there is room.

```
char *strncat(dst, src, n)
char *dst, *src;
int n;
{
    char *save = dst;
    while (*dst++);
    for (--dst; --n >= 0; )
        if (!(*dst++ == *src++)) return save;
    *dst = '\0';
    return save;
}
```

`strcmp(s, t, n)` compares up to "n" characters of "s" and "t". It returns a value > 0, = 0, or < 0 when "s" > "t", "s" == "t", or "s" < "t", according to the ASCII sequence of characters. It skips the equal prefixes and uses the values of the first unequal characters to determine the comparison value.

```
int strcmp(s, t, n)
char *s, *t;
int n;
{
    while (--n >= 0)
    {
        if (*s != *t++) return *s - t(-1);
        if (!*s++) break;
    }
    return 0;
}
```

`strncpy(dst, src, n)` copies up to "n" characters from "src" to "dst". It returns a pointer to the beginning of "dst". Null characters are placed at the end of "dst" to fill the string.

```
char *strncpy(dst, src, n)
char *dst, *src;
int n;
{
    char *save = dst;
    while (--n >= 0)
    {
        if (!(*dst++ = *src++))
    }
```

```
        while (--n >= 0) *dst++ = '\0';
        break;
    }
    return save;
}
```

`strnend(src, len)` returns a pointer to the end of the string pointed to by "src", of no more than "len" characters.

```
char *strnend(src, len)
char *src;
int len;
{
    while (--len >= 0 && *src++);
    return src-1;
}
```

`strnlen(src, len)` returns the number of characters up to the first null in the string pointed to by "src", or "len", whichever is smaller.

```
int strnlen(src, len)
char *src;
int len;
{
    int l = 0;
    while (--len >= 0 && *src++) ++l;
    return l;
}
```

`strnmov(dat, src, n)` copies up to "n" characters from "src" to "dat". It returns a pointer to the next character after the end of "dat".

```
char *strnmov(dat, src, n)
char *dat, *src;
int n;
{
    while (--n >= 0)
    {
        if (!(*dat++ == *src++))
        {
            src = dat;
            while (--n >= 0) *src++ = '\0';
            break;
        }
    }
    return dat;
}
```

`strnrev(dat, src, len)` copies up to "len" characters from "src" to "dat" in reverse order. It will work with completely overlapping, but not partially overlapping, source and destination strings. On each iteration, it swaps successive characters from the next positions from the front and end of each string.

```
char *strnrev(dat, src, len)
char *dat, *src;
int len;
{
    char *srcz, *srcz = src, t;
    for ( ; --len >= 0 && *srcz; srcz++);
    datz = dst + (srcz - src);
    if (len >= 0) *datz = '\0';
    while (srcz > src)
    {
        t = *--srcz;
        *--datz = *srcz;
        *datz = t;
    }
}
```

`strnrpt(dst, n, src, k)` repeats the string "src" into "dst" "k" times, but truncates the result at "n" characters.

```
int strnrpt (dst, n, src, k)
char *dst, *src;
int n, k;
{
    char *save = dst, *p;
    for ( ; --k >= 0; --dat)
        for (p = src; ; )
        {
            if (--n < 0) return dst-save;
            if (!(*dst++ = *p++)) break;
        }
    return dst-save;
}
```

The following routines convert integers to strings and strings to integers. The radix of the integer (2-36) is specified on each conversion. In case of error in conversion from string to integer, an error flag ("errno") is set to indicate the problem. The term "integer" actually indicates C type "long".

```
int2str(dat, radix, val) converts the long integer "val" to character form and copies it to the destination string "dat" followed by a terminating null. The result points to the "dat" string, unless the requested radix is out of range, in which case the result is NULL. Digits are generated in reverse order in the appropriate representation, then are placed into the output string. Using "int2str", "itos" and "ltos" are defined.
```

```
#define itos(x, y) int2str(y, 10, (long)x)
#define ltos(x, y) int2str(y, 10, x)

char *int2str(dat, radix, val)
char *dat;
int radix;
long val;
{
    static char dig_vec[] =
        "0123456789abcdefghijklmnopqrstuvwxyz";
    char buffer[33];
    char *p = &buffer[32];

    if (val < 0)
    {
        *dat++ = '-';
        val = -val;
    }
    *dat = *p = '0';
    if (radix > 36 || radix < 2)
        return NULL;
    do
        --p = dig_vec[val/radix];
    while (val % radix);
    while (*dat++ = *p++);
    return dat-1;
}

str2int(src, radix, lower, upper, &val) converts the string pointed to by "src" to an integer with radix "radix" and stores at "&val". Its value usually is a pointer to the next character after the last digit of the number converted. In case of conversion error, "errno" is set to a nonzero value, the function value is set to NULL, and "&val" is set to zero. Using "str2int", "atoi" and "atoi" are defined.
```

```
#define Maxint      0x7fffffffL
#define Minint      0x80000000L
#define MaxLong     0x7fffffffffL
#define MinLong     0x8000000000000000L
#define EDOM       1
#define ERANGE     2
int _errno;
char *_c2type[129] =
{
    /* EOF == -1 */
    37, 37, 37, 37, 37, 37, 37, 37, 37,
    37, 38, 39, 39, 39, 39, 37, 37,
    37, 37, 37, 37, 37, 37, 37, 37,
    37, 37, 37, 37, 37, 37, 37, 37,
    38, 36, 36, 36, 36, 36, 36, 36,
    36, 36, 36, 36, 36, 36, 36, 36,
    0, 1, 2, 3, 4, 5, 6, 7,
    8, 9, 36, 36, 36, 36, 36, 36,
    36, 10, 11, 12, 13, 14, 15, 16,
    17, 18, 19, 20, 21, 22, 23, 24,
    25, 26, 27, 28, 29, 30, 31, 32,
    33, 34, 35, 36, 36, 36, 36, 36,
    36, 10, 11, 12, 13, 14, 15, 16,
    17, 18, 19, 20, 21, 22, 23, 24,
    25, 26, 27, 28, 29, 30, 31, 32,
    33, 34, 35, 36, 36, 36, 36, 36
};

char *str2int(src, radix, lower, upper, val)
char *src;
int radix;
long lower, upper, &val;
{
    char *answer;
    int d, n, sign;
    long limit, scale, sofar;
    &val = 0;
    if (radix < 2 || radix > 36)

```

```
        errno = EDOM;
        return NULL;
    }
    if ((limit = lower) > 0) limit = -limit;
    if ((scale = upper) > 0) scale = -scale;
    if (scale < limit) limit = scale;
    while (*src == ' ' || *src == 't') src++;
    sign = -1;
    if (*src == '+') src++; else
    if (*src == '-') src++, sign = 1;
    if (_c2type[l+*src] >= radix)
    {
        errno = EDOM;
        return NULL;
    }
    while (*src == '0') src++;
    for (n = 0; _c2type[l+*src+n] < radix; n++);
    answer = --src;
    for (sofar = 0, scale = -1; --n >= 0; )
    {
        d = _c2type[l+*src];
        if (d < limit)
        {
            errno = ERANGE;
            return NULL;
        }
        limit = (limit+d)/radix;
        sofar += d*scale;
        if (n != 0) scale *= radix;
    }
    if (sign < 0 && sofar < -MaxLong ||
        (sofar == sign) < lower || sofar > upper)
    {
        errno = ERANGE;
        return NULL;
    }
    *val = sofar;
    errno = 0;
    return answer;
}

int atoi(src)
char *src;
{
    long val;
    str2int(src, 10, MinInt, MaxInt, &val);
    return (int)val;
}

long atol(src)
char *src;
{
    long val;
    str2int(src, 10, MinLong, MaxLong, &val);
    return val;
}
```

The "strfind" and "strrepl" functions find and insert character patterns in strings. They share the use of the "strpat" function and several auxiliary variable arrays. "strpat" is based upon R. Nigel Hospool's algorithm, as described in "Software Practice and Experience, 1988", page 585. Because the Full C compilers on the 6809 do not implement "unsigned char", the characters are limited to values 9 to 127, the ASCII code sequence.

```
#define _AlphabetSize 128
int _pat_lim;
int _pat_vec[_AlphabetSize+1];
static char *oldPat = "";
char *str2pat(pat)
char *pat;
{
    int l, i;
    if (pat == NULL)
        pat = oldPat;
    else
        oldPat = pat;
    for (l = 0; *pat++; l++);
    for (i = _AlphabetSize; --i >= 0;
        _pat_vec[i] = 1);
    for (*pat = oldPat, i = 1; --i > 0;
        *pat_vec[*pat++] = 1);
    _pat_lim = --l;
    return oldPat;
}
```

```

strfind(src, pat) returns a pointer to the first
occurrence of "pat" in "src", or returns NULL.

char *strfind(src, pat)
char *src, *pat;
{
    char *s, *p;
    int c, lastch;
    pat = _str2pat(pat);
    if (_pat_lim < 0)
    {
        for (s = src; *s++; );
        return s-1;
    }
    for (lastch = pat[c = _pat_lim]; ; c = _pat_vec[c])
    {
        for (s = src; --c >= 0; )
            if (!*s++) return NULL;
        c = *s;
        src = s;
        if (c == lastch)
        {
            for (s -= _pat_lim, p = pat; *p; )
                if (*s++ != *p++) goto not_yet;
            return s;
        not_yet:;
        }
    }
}

strrepl(dat, src, pat, rep, times) copies "src" to
"dat", replacing the first "times" non-overlapping
instances of "pat" by "rep". It returns a pointer to the null terminating
character of "dat".

char *strrepl(dat, src, pat, rep, times)
char *dat, *src, *pat, *rep;
int times;
{
    char *s, *p;
    int c, lastch;
    pat = _str2pat(pat);
    if (times <= 0)
    {
        for (p = dat, s = src; *p++ = *src++; );
        return p-1;
    }
    if (_pat_lim < 0)
    {
        for (p = dat, s = src; *p++ = *src++; );
        for (-p, s = rep; *p++ = *src++; );
        return p-1;
    }
    lastch = pat[c = _pat_lim];
    for (;;)
    {
        for (s = src, p = dat; --c >= 0; )
            if (!(*p++ = *src++)) return p-1;
        c = *s;
        src = s;
        dat = p;
        if (c == lastch)
        {
            for (s -= _pat_lim, p = pat; *p; )
                if (*s++ != *p++) goto not_yet;
            for (p = dat-_pat_lim, s = rep; *p++ = *src++; )
                *p++ = *src++;
            --p;
            if (i--times)
            {
                for (s = src; *p++ = *src++; );
                return p-1;
            }
            dat = p;
            src++;
            c = _pat_lim;
        }
        else
        {
            not_yet: c = _pat_vec[c];
        }
    }
}

```

The translate family of functions ("memtrans", "strntran", and "strtrans") translate characters from one string to another according to the contents of "from" and "to" control strings. They use a common routine "str2map.c" and several common variable areas to construct control tables for the translation process. This routine may be also used

for the purpose of constructing the mapping tables separately from the translation routines. When the translation routines are called, a NULL for the "from" or "to" parameters indicates to use the table previously defined. Because the Full C compilers on the 6809 do not implement "unsigned char", the characters are limited to values 0 to 127, the ASCII code sequence.

```

#define AlphabetSize 128
static char oldFrom = "?";
static char oldTo = "?";
char _map_vec[AlphabetSize+1];

str2map(option, from, to)
Int option;
char *from, *to;
{
    int i, c;
    if (from == NULL && to == NULL) return;
    if (from == NULL)
        from = oldFrom;
    else
        oldFrom = from;
    if (to == NULL)
        to = oldTo;
    else
        oldTo = to;
    switch (option)
    {
        case 0:
            for (i = AlphabetSize; --i >= 0; )
                _map_vec[i] = i;
        case 1:
            while (i = *from++)
            {
                _map_vec[i] = *to++;
                If (i==to)
                {
                    c = "--to";
                    while (i = *from++)
                        _map_vec[i] = c;
                    return;
                }
            }
            return;
        case 2:
            c = *to;
            for (i = AlphabetSize; --i >= 0; )
                _map_vec[i] = c;
            while (c = *from++)
                _map_vec[c] = c;
            return;
    }
}

memtrans(dat, src, from, to, len) copies "len"
characters from "src" to "dat", translating
characters in "from" to corresponding
characters in "to".
memtrans(dat, src, from, to, len)
char *dat, *src, *from, *to;
int len;
{
    str2map(0, from, to);
    While (--len >= 0) *dat++ = _map_vec[*src++];
}

strntran(dat, src, len, from, to) copies up to "len"
characters from "src" to "dat", translating
characters in "from" to "to". It fills in the
remainder of "dat" up to "len" bytes with null
characters.
strntran(dat, src, len, from, to)
char *dat, *src, *from, *to;
int len;
{
    str2map(0, from, to);
    While (--len >= 0) {
        (*dat++ = _map_vec[*src++]);
        while (--len >= 0) *dat++ = '\0';
    }
}

strtrans(dat, src, from, to) copies characters from
"src" to "dat", translating characters in
"from" to "to".
strtrans(dat, src, from, to)
char *dat, *src, *from, *to;
{

```

```

    _etr2mp(g, from, to);
    While (*det++ = _map_vec[*src++]);
}

```

Next month's column will cover several applications of O'Reefe's functions and will continue the discussion of string-handling functions in the C language.

#### C PROBLEM

The following program:

```

#include <stdio.h>
#define exp(x) if ((x) == '\t') printf(" ")
main()
{
    char c[] = "abc\tdef";
    char *p;
    for (p = c; *p; p++)
    {
        if (*p != 'c')
            exp(*p);
        else
            printf("%c", *p);
    }
}

```

outputs the following:

```
ab def
```

This is because the unmatched "if" in the definition matches the "else", despite what the indentation seems to imply, and the 'c' is not output.

The guideline illustrated by this problem is to code complete expressions or statements in definitions, to avoid such unexpected matchups.

The next problem is to write a program which translates the upper case letters in a file to lower case and drops all control characters (except carriage return) using the appropriate translate functions described earlier in this article.

#### EXAMPLE C PROGRAM

Following is this month's example C function; it is from Phil Gansu, and provides an INTROL C program to convert a FLEX binary file to SI format.

```

#include      "stdio.h"
#include      "flex.h"
#define WIDTH 32
char   count, buff(WIDTH);
int    curadd, address, sladd;
FILE   *fspin;
int    i, byte;
char  *add;

main(argc, argv)
int   argc;
char **argv;
{
    FILE   *fspin;
    int    i, byte;
    char  *add;

    if (argc != 2) {
        printf("Syntax:\thexdump filename\n");
        exit(1);
    }
    if ((fspin = fopen(argv[1], "rb")) == ERROR) {
        printf("Cannot open input file.\n");
        exit(1);
    }
    i = 0;
    add = &address;
    curadd = $ffff;
    while ((byte = getc(fspin)) != ERROR) {
        if (byte == 2) {
            add[0] = getc(fspin);
            add[1] = getc(fspin);
            if ((count = getc(fspin)) == 0)
                break;
            if (curadd == address) {
                curadd = address;
                header(i);
                i = 0;
            }
            while (1) {
                buff[i++] = getc(fspin);
                curadd++;
                if (i == WIDTH) {
                    header(i);
                    i = 0;
                }
                if (--count == 0)
                    break;
            }
        }
    }
}

```

```

        }
        if (byte == 0x16) {
            getc(fspin);
            getc(fspin);
        }
        header(i);
        printf("\n$9\n");
        exit(0);
    }

int size;
{
    char   cnt, chksum, *add;
    int   i;

    if (size != 0) {
        add = &sladd;
        if (size > WIDTH)
            cnt = WIDTH;
        else
            cnt = size;
        chksum = size + add[0] + add[1];
        printf("\n$102X$4X", cnt + 3, sladd);
        for (i = 0; i < cnt; i++) {
            printf("$02X", buff[i]);
            chksum += buff[i];
        }
        printf("$02X", chksum - 3);
    }
    sladd = curadd;
}

```

## 68000 USER NOTES

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Last month concerned the writing of programs, mostly in the C language, so that they could be easily transported between operating systems, particularly between 6809 and 68000 versions of OS-9. Mostly I dealt with trouble spots to watch out for, such as differences in compilers, libraries, and data types. I would now like to continue, covering techniques I have actually used or have seen elsewhere.

As the second part of this month's column, I have the beginning of some information on Motorola's new 32 bit microprocessor, the 68020, with more to follow next month.

#### Data Types (again)

One of the most troublesome problems encountered in porting programs between 8 bit and 16 bit computers has to do with the changing sizes of the basic data types. I mentioned this last month, and said that a program should never make rash assumptions about the size of a variable, but instead should use the sizeof function. This only takes care of one side of the problem, though. What should you do, for instance, when you want a variable to be two bytes long, regardless of the operating system? This might happen when using a file which has a fixed structure which must be exactly duplicated in all programming environments.

Here is where the pre-processor again steps in. Generally, it is possible to find data types which have a given size on various machines. The only problem is that the type might differ between machines. Instead of using the name of one of the primitive data types, then, some standardized set of special names, each of which is to have a particular size and range of legal values on all machines, is defined. As an example:

```

#define BYTE char
#define WORD short
#define LWORD long
#define UBYTE unsigned char
#define UWORD unsigned short
#define ULWORD unsigned long

BYTE tinyval;
WORD smallval;
LWORD bigval;

```

By using the defined names, instead of the primitive types, a program is protected from changing data type sizes. As it happens, these definitions will work for both the 6809 and 68000, but that is not guaranteed for all processors. Also, it still makes sense to use the defined names, since they tend to make clearer within a program the fact that a variable has been constrained to a particular size.

To encourage the use of special names such as these, it makes sense to place the definitions in a file to be referenced by an #include statement. For instance, I have taken to placing these definitions in a file named stddefs.h, placed in the same directory as stdio.h, so that the reference in every C program is #include <stddefs.h>. I chose not to place the definitions in stdio.h, as might seem logical, since I would then have to edit stdio.h with each new release of the compiler.

Assuming that each operating system uses a different disk (or disk partition) for the standard include files, separate stddefs.h files can be created, each one being particular to a single operating system or machine. On the other hand, the same file can be used in all cases, with any required differences being implemented using #ifdef/#else/#endif constructs, as discussed last month.

There is an alternate method for declaring these special names, via the **typedef** specifier. Instead of

```
#define BYTE char
```

the line could be

```
typedef char BYTE;
```

which form is used is probably just a matter of personal preference, though the fact that the **#define** is handled by the pre-processor, while the **typedef** is handled by the compiler proper can make some difference (see K & R, page 141, on **typedefs** for an example).

There is a final point to be made here in conjunction with 4 byte long variables and printf. Since long and int are synonymous on the 68000, it is never necessary to use the "l" specification in printf control strings (e.g. "%ld" instead of "%lld"). Use printf on a long variable on the 6809 without the "l", though, and everything will fall apart, since printf has no way of knowing that the variable is actually taking up 4 bytes on the stack. To defend against this, always use a special name, such as the LWORD defined above, for values which require more than 2 bytes, and always use the long descriptor "l" in printf control strings for such variables.

### Getting the Most From Your Compiler

A compiler for a particular machine will generally have some ability to create more efficient programs. For instance, 68000 C compilers will generally be able to allocate a large number of register variables, perhaps 6 or more, while 6809 C compilers only allow one. Microware's 6809 C, on the other hand, has a type class specifier of **direct** for global variables which should be referenced using direct page instructions if at all possible, instead of using longer indexed offset instructions.

The large number of register variables available with the 68000 would seem to indicate that everything in sight should be declared register if possible. If this is done, though, the code produced on the 6809 may be less efficient than possible. Consider a function with one or more arguments, like main(argc,argv). Both argc and argv should likely be declared register on the 68000. However, it may be better on the 6809 to save the only register declaration for a local variable within the function.

Again, it makes sense with the 6809 to use the **direct** specifier. Most other compilers will error out when the **direct** is encountered, though (as it happens, the Microware 68000 C compiler just ignores it).

How can portable programs use those special efficiency features which may be available? As you might expect, the pre-processor is used. The following lines occur in my stddefs.h file:

```
#ifdef OSK /* 68000 */
# define REGISTER register
# define DIRECT
#else /* 6809 */
# define REGISTER
# define DIRECT direct
#endif
```

When I wish to defer using the single register declaration for the 6809, but still wish to use the 68000 register variables, I just use the **REGISTER** declaration instead:

```
main(argc,argv)
REGISTER int argc;
REGISTER char *argv;

{ REGISTER int val;
register char *p;
```

For the 68000, all of the variables will be placed in registers. For the 6809, **REGISTER** translates to nothing, so that only the variable 'p' is placed in a register. In the same way, any global variables can be declared **DIRECT**, but the declaration will only take effect on the 6809.

### 68020

Motorola was a little slow out of the gate with the 68020, trailing the release of Intel's 8086 by a couple of years. We all know what that lead to, with the IBM PC and various PC compatibles everywhere you look. It is kind of nice, then to see Motorola's 32 bit micro, the 68020, make it out so early as compared to other 32 bit designs.

I just received the 68020 user's manual, which describes the chip from both a hardware and software viewpoint. For those of you who are interested, the book is the MC68020 32-Bit Microprocessor User's Manual. It is published by Prentice-Hall, and has a part number of MC68020UM(ADI). I'm not sure if that is a Motorola part number or the Prentice-Hall number. You can probably get it through most bookstores, though I got mine via an electronics distributor my company uses. Distributors, like Pioneer, Hamilton-Avnet, or Arrow, may be able to get the book to you more quickly.

Since the book came in very recently, I haven't had time to peruse it carefully, so all I can say now are some general first impressions. I'll be more detailed next month. For now, I will only cover a few software aspects.

The 68020 is upwards compatible, at the object code level, with the previous members of the family (68000, 68008, 68010, and 68012). There are three types of software extensions: new instructions, extended instructions, and new addressing modes.

The new instructions include two major groups of new instructions. The first group deals with a new data type, bit fields. A bit field is a string of consecutive bits, without regard to actual byte boundaries. A single bit field can be from 1 to 32 bits long, and can be offset from a base byte boundary by -2031 to (2031)-1 bits. Bit fields can be extracted, being moved right justified into a data register, with or without sign extension, as well as moved in the other direction, an insert. The other bit fields opcodes test the value of a bit field (compare with 0), set, clear, or complement a bit field, and find the first bit which is set within a bit field. These opcodes seem to be explicitly defined for use in implementing the C language's bit fields, as well as the set data type in Pascal.

The other group of new instructions consists of coprocessor communication instructions. These instructions create a method in the 68020 for interfacing with other intelligent devices which greatly extend the 68020's power. Current coprocessors include a floating point chip and a virtual memory management unit. One instruction allows access to any of the predefined coprocessor instructions. Other instructions, analogous to the 68000 instructions Scc, DBcc, Bcc, and TRAPcc (a new conditional TRAP instruction), allow processing to depend on a coprocessor's current state. There are also instructions for saving and restoring a coprocessor's current state.

Certain 68000 instructions have been extended in the 68020. For instance, the relative branch instructions BRA, BSR, and Bcc now accept 32 bit offsets, so subroutine calls are no longer limited to a 64K range. A multiply with a 64 bit result, as well as a 64 bit/32 bit divide, is now available. As mentioned above, there is a new conditional form of the TRAP instruction. There is also a new sign extend instruction, EXTB, which extends a byte value directly to a long value. This also seems to be motivated by C, since current implementations require many EXT.W, EXT.L sequences when dealing with yte variables.

There are a number of new addressing modes available. Constant offsets of 32 bits are now allowed. Index registers, in modes like (d8,An,Xn), can now be specified with a scale factor of 1, 2, 4 or 8. This means that the value in the Index register will be shifted before adding, with no extra clock cycles, so that indexed offsets into word, long word, and quad word arrays can be done without using explicit shift instructions. Finally, there is a very general indexing form, which can involve two constant offsets, two registers, and memory indirection, with every element optional. As an example, an addressing mode of ((1000,A5),D1\*4) says to add 1000 to the value in A5, giving an address in memory. The 4 byte pointer at this address is retrieved, and 4 times the value in D1 is added, giving a final address. This instruction might conceivably appear in a C program in which a pointer variable (at 1000,A5) points to an array of long integers.

There is much, much more to cover (for instance, the 68020 comes in a quad pin array package, with over 110 pins!) but I have already delayed this column too long waiting for the 68020 manual to appear. I will certainly have more to say next month, once I have a chance to read the book more closely. In the meantime, send for the manual yourself, and follow along next month.

---

## TMP/FREEFORM FILER

Reviewed by Bob Ney

The TMP"/FreeForm Filer Package is a Free Form, or "Unstructured", File Management System offered by The United Software Company, 2431 E. Douglas, Wichita, KS 67211 (316) 684-5281 (TMP is a Trademark of The United Software Company). Unlike most File Management and Data Base Management Systems, TMP/Freeform Filer does not require the definition of specific 'Fields' to make up a "structured" Record System.

The "TMP" in the Package name refers to United Software's Total Management Planning system of Software Packages, which includes the TMP/Front-End (a Menu-Controlled "System Manager" Front End Package), the TMP/Manager I (a "structured" Data Base System), TMP/PowerPlanner (renamed from TMP/Calc - an Electronic Spreadsheet Program), plus others, which are all designed to be fully "data compatible" and provide a common User Interface and Command Structure.

TMP/FreeForm Filer for OS-9 sells for \$225.00 and requires OS-9 Level II and at least 128K of Memory. You have seen it advertised with the Smoke Signals Computer Systems in the past, and United Software is now making it available for other Systems. We understand that it also runs under MS-DOS and some other Operating Systems at this time, and that FLEX and UNIFLEX versions are being considered.

In order to visualize the concept of TMP/FreeForm Filer, imagine a 3x5 Card File Drawer full of Cards of Information. TMP/Freeform Filer was designed to allow the User to rapidly locate a specific item of information that is in these cards through searching for certain Card Names and/or one of the specified Key Words on any of the Cards.

TMP/FreeForm Filer allows as many "Drawers" (Data Files) as you have Disk Storage for. Each "Drawer" may contain up to 32,000+ "Cards", and each "Card" can contain up to 9 "Pages" of 37 Columns by 13 Rows of Information. Each "Page" may contain up to 15 20-character "Key Words" for subject searches, etc. (providing a maximum of 117 "Key Words" per "Card"). The Data is maintained in two files; a Data File and a Key File (which are generated and maintained automatically by the Program). Searches for "Key Words" and/or "Card Titles" allow wild card selection

with the "\*" (match ANY NUMBER of characters) and the "?" (match any SINGLE character), but TMP/FreeForm Filer does NOT provide any combinational selections such as 'this AND this' or 'this OR that'. You CAN get a two-level selection by using both a Card Title AND a Key Word selection Mask (and proper selection of Card Titles can be a big help here). You can examine selected Cards of information on the Display, or output them to the Printer or to another file (in an ASCII Format suitable for use with a Word Processor or Editor) with TMP/FreeForm Filer, but you can only output in a 3x5 format suitable for printing on single-wide 3x5 continuous form cards (available from Computer Supply houses) or in an 8 1/2 x 11 paper format (which separates each card page with a line of asterisks).

The TMP/FreeForm Filer Disk contains several files including the Command Files, an Environment Editor for setting the Program up for your specific Terminal (with "TMP.ENV" files for several Terminals already set up), the "Help" files, and an Example File set. Two files, "FREEFORM" and "TMPENV", must be copied to your Execution Directory. Three files, "HELPFF.DHP", "HELPFF.KHP", and the "TMP.ENV" file for your specific Terminal (made up with the "TMPENV" command if your Terminal is not already provided) must be in the Data Directory which contains the "Drawer", or Data Base, that you will be working with.

Overall System Operation consists of calling up the Program with the command "freeform", which produces a "System Menu" (Fig. 1) which provides the access and overall control of the various "Drawers", or Data Bases, that may be in the present Data Directory. The "System Menu" allows the Listing of the Drawers (like a "Dir"), the Deletion of Drawers (delete a complete Data Base), and the Creation or Access of any Drawer through the selection of a menu option Number.

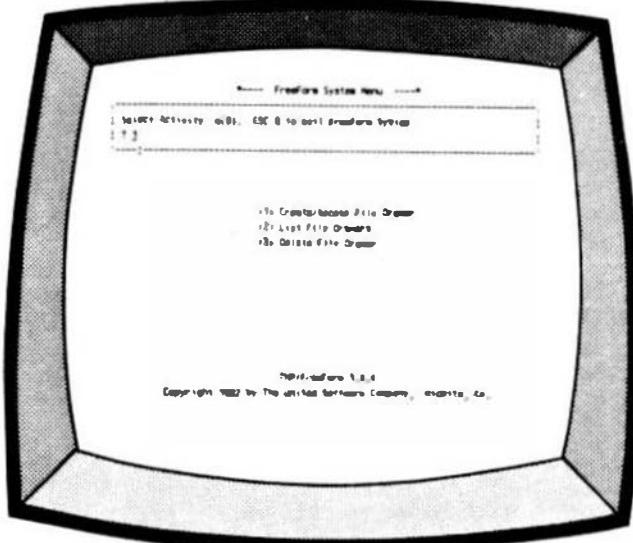


Fig. 1 -- Freeform System Menu

Should a Drawer be chosen for Creation or Access, the Program moves on to the next menu, which is the "File Drawer" menu (Fig. 2). From this menu, the User has access to overall Card File manipulations, such as Listing the Card Titles (i.e., a "Dir" of the Card Names that are in the Drawer chosen from the last menu), Outputting all or a selection of the Cards to a Printer or file, changing the Output Defaults (changing the Printer name or specifying a filename, and toggling the output format between a 3x5 Card Format or an 8 1/2 x 11 Paper Page Format), or moving on to the "Freeform Data Entry" screen for operations

on the individual Cards themselves. Again, the Menu is operated through the selection of an option Number.

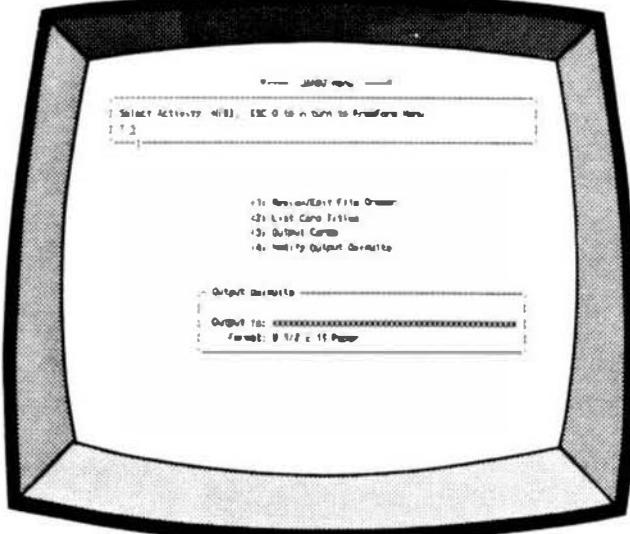


Fig. 2 -- Freeform File Drawer Menu,  
showing the Drawer (Data Base)  
named "JAN82" selected

The "Freeform Data Entry" Screen provides a two-part access to working with the Cards; first is the Card Selection operations, and then the actual Card Manipulation operations. Selection of the desired operations at this level in the Program is through "ESC <letter>" and "Control Key" operations.

On initial entry into the "Freeform Data Entry" display (Fig. 3), an 'Available Commands' block at the top of the Display provides a series of 11 Escape and Control Key operations that allow the User to select the specific Card that is to be worked on. Throughout the Program, an 'ESC Q' sequence Quits the present level of operation and moves the User back a level, or Menu, 'house cleaning' as it goes. In both levels of the "Freeform Data Entry" display, the User can "Quit" or get "Help" relevant to the level he is in. From the Card Selection level, the User can specify a Card Name, or locate a Card through either a partial-name "Search" or through a Title (Card Name) AND/OR Key Word "Mask", using either a full character Mask (i.e., giving the full Title or Key Word), or through a Wild-Card Mask using the "\*" and/or "?". Once a Card is located, it can be "Zapped" (deleted from the File), its Title can be changed, it can be output through the "Output Defaults" specified in the previous Menu, it can be "Reproduced" onto new Cards as often as you want (this feature is provided to make the use of "Templates" easy), or the User can move to the "Previous" or "Next" Card in the File. All Cards are maintained alphabetically by Card Title, so if a Search or Mask has located a Card close to the Card desired, this procedure provides an easy way to locate the desired information.

Finally, once a Card is located, a total of 15 Escape and Control Key operations allow the examination, editing, or otherwise "massaging" the information that is on the Card just located (see Fig. 4 and 5). As before, the "Quit" and "Help" commands are available; the "Quit" command saves any changes made to the Card, and the "Help" info is relevant to this level of the Program. An "Exit" is provided to leave without changing anything. The User can Insert Information, Delete a character, line, or any of the 9 pages in the Card, step to the Next or Previous pages, Find a specific Page, change the order of the Pages by Renumbering them, Add Pages, and specify the "Key Words" in the information that are to be used for

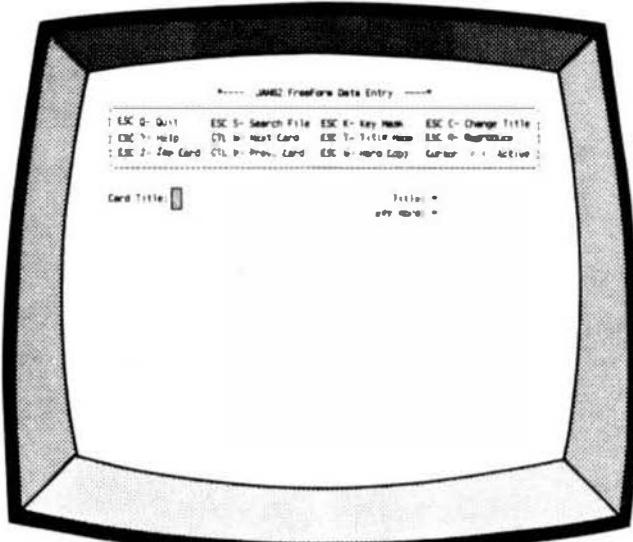


Fig. 3 -- The initial Freeform Data Entry screen

locating that specific Card. Finally, a "Save" and "Withdraw" are provided which saves a page of information to an internal buffer, which can be withdrawn onto any page at any time. The information saved to the buffer remains there as long as the Program is active, or the buffer is overwritten with another Save, and any information that may have been on a page when the buffer is withdrawn onto that page is completely replaced by the new information. Where the "Reproduce" command in the previous level produced NEW Cards, the Withdraw command at this level simply REPLACES any information that may have been on a page. Again, the primary purpose of this feature is for "Templating" a page of information.

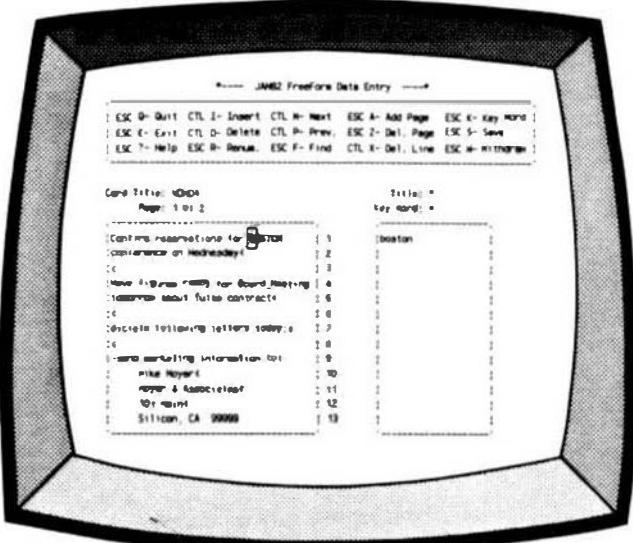
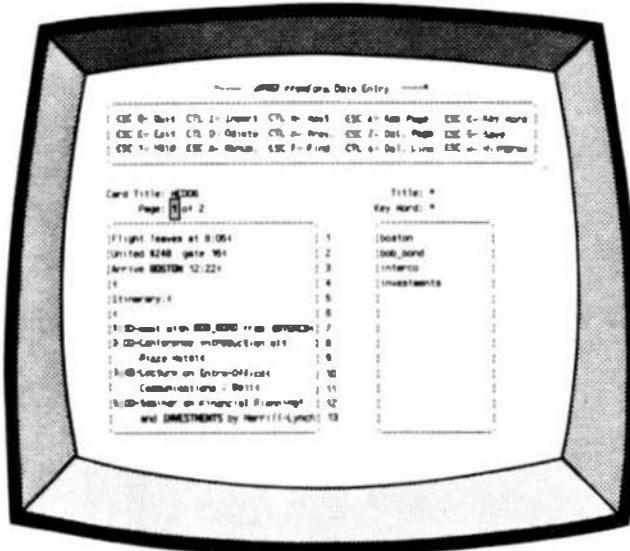


Fig. 4 -- Freeform Data Entry screen  
showing Page 1 of a 2-page Card.  
"boston" has just been selected as a Key Word.  
Notice the "Free Form" entry of information  
in this and Fig. 5's screens.



**Fig. 5 -- Freeform Data Entry Screen**  
showing 4 words defined as Key Words  
and Cursor In position to "Renumber"  
the Page Number.

In summary, calling up the Program produces a "System Menu" (Fig. 1) which allows the specification and control of the Drawers, or Data Bases, themselves. The flow moves from the "System" level to a "File Drawer" Menu (Fig. 2) for the Drawer selected, which provides overall control of the full "Deck of Cards". From the "File Drawer" Menu, the Program moves on to the "Freeform Data Entry" display, which provides an initial level of control for locating and/or manipulating a complete Card of Information (Fig. 3). Once a specific Card has been selected, control moves to the second level of the "Freeform Data Entry" (Figures 4 and 5), which allows the control and manipulation of the pages and information that is contained within the Card itself.

**TMP/FreeForm Filter** is a flexible Program that can be used for a multitude of tasks without requiring a "Computer Science" background to understand how to set the system up for your information. As with any piece of Software, the more you work with it, the more ideas for using it appear. The Menus may not appear too "palatable" to some users, but they do not interfere very much with the normal operation for the experienced Computer User (most of your actual working time is spent in the "Freeform Data Entry" Screen, where the more familiar ESC and CTL Key Commands are used), while isolating the Operating System for the newer Users.

The TMP/FreeForm Filter Documentation is several notches ABOVE what the SS-50 Bus Community is used to seeing. The first part of the Manual discusses the overall TMP System User Interface and includes a detailed Chapter on setting the Program up for your specific Terminal and preferences, while the second Section discusses TMP/FreeForm Filter Operation under OS-9. This section begins by discussing a "Real World Example" in general (in the Manual, they use a simple Appointment Calendar for their example), and then walks the new User through the use of the Program in setting up and working with this example. Finally, there are detailed chapters on the Menus and Commands, and a chapter on Interfacing FreeForm Filter with other TMP Packages. The Manual ends with a few pages of "Some Ideas", an Appendix of "Basic System Rules", an Appendix of "Errors", a Glossary, and an Index. The Manual is well laid out and easy to read.

and use (the Figures provided above came directly out of the Manual).

Since the Package is designed for quick access to a specific piece of Information, I found it a little cumbersome to have to work through the Menus just to get one item, but I have been informed by United Software that the Disk now contains a Program called "QFind" which allows the User to locate a specific Item directly from the Operating System. Another feature that would radically enhance the use of this type of Data Information System would be the capability of multi-level searches, such as "this AND that" or "this OR that but NOT such-and-such". This would allow looking for something like "high temp AND small spots AND enlarged glands" in a Drawer of Childhood Diseases, or "tall AND red OR yellow AND annual" in a Drawer of Flowers. Again, United Software indicates that that feature is on the "Things to Do" list, and will probably be provided as an additional module for TMP/Freeform Filter at a later date.

As it now stands, some thought in naming Cards and Key Words, along with the use of the Wild Card Mask capabilities, can provide similar capabilities. For example, a Card in a Drawer of "Flowers" might be named "TallRedAnnual" (up to 20 characters can be used for both Card Names and Key Words, and the "underline" character can be used to tie the words together if desired), where Card Name Masks of "red", "tall", and "annual" would all locate this Card. The same concepts can be used with Key Words. Another possibility would be to use a set of Cards with somewhat similar Names that would provide "General" subject categories which would contain the names of other Cards and/or Key Words that contained more detailed information. The first search would provide "pointers" to more specific information. The Program as it now stands is being used by Medical and Dental Researchers, so it has a lot of capability. Again, a little thought can provide unlimited possibilities.

Finally, I hit ONE snag with the Program; the "Help" Files would generate an "Error 198" on the Gilmix III System, which immediately "bombs" you out of the Program and back to the Operating System. This is a HARDWARE Trap in the Gilmix III System that indicates that the Program is trying to change the Program Memory (which is a theoretical No-No in OS-9 -- only DATA Areas can be changed!). TMP/FreeForm Filter is not unique in having this problem; I have run into it often with the Gilmix III System. Gilmix installed this Trap in the System III Computers to protect one User from another User's Program, but it sure would be nice to have a Switch to Enable/Disable this feature. Compilers, especially, have problems in this area. Anyway, United Software is looking into it, and the overall Program operation is so simple that the Help Files are seldom needed (and the Manual is so well laid out that it is easy to find the information when needed in it).

All in all, TMP/FreeForm Filter is a very useful Program, and it is hoped that it can be "ported" over to OS-9 Level 1 and the FLEX System (it is written in C, which makes it transportable -- THEORETICALLY!!).

---

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0110 B1	CC02	capa	eolchr		0288 20 62 75 66	fcc	" buffer!"
0113 39		ckdelp	rts		0290 07 04	fcb	\$07,004
			l		0292 20 20 20 73	lsrgtg	"-- source has more tracks than "
0114 00	CD1E	pinfo	jsr	pstrng	print disk info		"target!"
0117 C6	00	ldb	0088	print disk name	0281 74 61 72 67	fcc	\$07,004
0119 BE	C890	ldz	0sysfc+850		0288 07 04	fcb	"-- source or target has"
011C A6	00	pdinfl	lda	,x	028A 20 20 20 73	lnonst	" nonstandard format!"
011E 34	14	pshs	b,x		0291 20 6E 6E 6E	fcc	\$07,004
0120 00	CD0F	jsr	outch		02E5 07 04	fcc	"-- unable to read system id "
0123 55	14	puls	b,z		0303 72 65 63 6F	fcc	'record!"
0125 5A		decb			030A 07 04	fcb	\$07,004
0126 26	F4	bne	pdinfl		030C 2D 2D 20 69	lbddrn	"-- invalid drive number!"
0128 B6	20	lda	0020		0324 07 04	fcb	\$07,004
012A 00	CD0F	jsr	outch			t	
012D B6	23	lda	0023		C100	org	caddr
012F B0	CD0F	jsr	outch				
0132 5F		cirb		print volume number	C100 CC 0001	setup	l00001 set up for copy process
0133 30	03	leax	103,x		C103 FD C5AE	std	sretks
0135 BD	CD39	jsr	outdec		C106 FD C5B0	std	trgts
0138 B6	20	lda	0020		C109 F7 C5B0	stb	lastrk
013A 80	CD0F	jsr	outch		C10C B6 C5B7	xloop8	lda
013D 7F	C5B2	clr	loctab	print date	C10F B7 C5B6	sta	trkit
0140 B6	C8A3	lda	sysfc+063		C112 F6 C5B8	ldb	srdrv
0143 87	C5B3	sta	loctab+l		C115 17 01C9	lbsr	psourc
0146 8E	C5B2	ldz	0loctab		C118 17 0179	lbsr	drvsel
0149 9F		clrb			C11B FC CSAE	ldd	sretks
014A B0	CD39	jsr	outdec		C11E 10BE 0000	ldy	0buffer
014D B6	2F	lda	002f		C122 BE C5B2	idx	loctab
014F B0	CD0F	jsr	outch		C125 BD 7F	xloop1	bsr
0152 B6	C8A4	lda	sysfc+064		C127 17 00CC	lbsr	seting
0155 87	C5B3	sta	loctab+l		C12A 1029 01E7	lbvs	doread
0158 8E	C5B2	ldz	0loctab		C12E 4C	inca	pferrd
015B 5F		clrb			C12F B1 C5B8	capa	oaxtrk
015C B0	CD39	jsr	outdec		C132 22 05	bhi	xloop2
015F B6	2F	lda	002f		C134 7A C5B6	dec	countr
0161 B0	CD0F	jsr	outch		C137 26 EC	bne	xloop1
0164 B6	C8A5	lda	sysfc+065		C139 FD CSAE	xloop2	std
0167 B7	C5B3	sta	loctab+l		C13C B6 C5B7	lda	trkit
016A BE	C5B2	ldz	0loctab		C13F 87 C5B6	sta	countr
016D 5F		clrb			C142 F6 C5B8	ldb	trgdrv
016E B0	CD39	jsr	outdec		C145 17 01B3	lbsr	ptarge
0171 7E	CD24	jap	pcrlf		C148 17 0149	lbsr	drvsel
				message output routines	C14B FC C5B8	ldd	trgts
0174 BE	030C	pbddrn	Idx	0lbddrn	C14E 10BE 0000	ldy	0buffer
0177 16	C1AD	Ibra	exits		C152 BE C5B2	idx	loctab
017A BE	02E7	pbdstr	Idx	0lbdsir	C155 BD 4F	xloop3	bsr
017D 16	C1A7	Ibra	exits		C157 17 00CF	lbsr	seting
0180 BE	C23C	pdfora	Idx	0ldfora	C15A 1029 01BC	lbvs	dowrit
0183 16	C1A1	Ibra	exits		C15E 4C	inca	ofernt
0186 BE	0292	psrgtg	Idx	0isrgtg	C15F B1 C5B8	capa	maxtrk
0189 16	C198	Ibra	exits		C162 22 05	bhi	xloop4
018C BE	028A	pnonsr	Idx	0lnonst	C164 7A C5B6	dec	countr
018F 16	C195	Ibra	exits		C167 26 EC	bne	xloop3
0192 BE	026A	pmeary	Idx	0laeary	C169 FD C5B8	xloop4	loop back
0195 16	C1BF	Ibra	exits		C16C B6 CSAE	std	save next track & sector
					C16F B1 C5B8	lda	sretks
						capa	oaxtrk
0198 20 20 20 63		lscrat	fcc	-- copy entire diskette in drive "		*	
0199 30 20 74 6F		lscsdr	fcc	"0 to drive "	C174 F6 C5B8	restor	ldb
					C177 17 0100	trgdrv	read original sir
					C17A 1026 01A6	lbsr	getsr
01C4 30 3F 20		lsctrdr	fcc	"?"	C17E 31 00 50	tbne	read sir
01C7 07 04		fcb			C181 BE C5BF	ldz	pbdrv0
01C9 20 20 20 77		improt	fcc	-- write-protect source"	C184 C6 00	ldb	0000
01E0 20 64 69 73		fcb		" diskette now!"	C18B 17 0113	lbsr	13 bytes
01EE 07 0A 04		fcb			C189 BE C840	ldz	move it
01F1 20 20 20 74		ltrgna	fcc	-- target identification:"	C18C CC 0003	idd	0sysfc
020A 07 04		fcb			C18F ED 00 3E	std	000003
020C 20 20 20 73		lsrcna	fcc	-- source identification:"	C192 B6 00	lda	0lex,x
0225 07 04		fcb			C194 A7 04	sta	0000
0227 20 20 20 63		lcontn	fcc	-- continue (y/n)? "	C196 BD 0406	jsr	fas
023A 07 04		fcb			C199 1026 01B7	lbnr	pbdrv0
023C 20 20 20 73		ldfora	fcc	-- source and target have"		*	
0255 20 64 69 66		fcb		" different formats!"	C19D BE C5B0	pcopl1	complete - exit
0268 07 04		fcb			C1A0 BD CD1E	jsr	pstrng
026A 20 20 20 6E		laeary	fcc	-- not enough memory for track"			

CIA3 7E	CD03	jmp	wares		C229 34	76	dowrit	pshs	u,y,z,b,a	write a track	
		t			C22D 33	E4	leau	,s			
		entry:			C22D 86	0C	lda	080c		max retries	
		y=data address			C22F 34	02	pshs	a			
		x=beginning address of table			C231 1F	12	tfr	x,y			
		d=track/sector			C233 20	02	bra	dowri2			
		exit:			C235 31	23	dowri1	leay	\$03,y		
		yanext data address			C237 A6	C4	dowri2	lda	,u	get track	
		x=same as entry			>C239 17	087C	lbsr	displa		display track	
		b=sector+constant			C23C E6	A4	ldb	,y		get sector	
		a=track			C23E 27	1C	beq	dowri5			
CIA6 34	76	setin0	pshs	u,y,x,b,a	C240 AE	21	idx	081,y		get buffer address	
CIA8 33	E4	leau	,s	set pointer	C242 BD	DE03	jsr	dwrite		write a sector	
CIAA 32	7C	leas	-004,s	make stack room	C245 27	EE	beq	dowri1			
CIA4 31	A9 FF80	leay	\$FF00,y	\$FF00 is -256 (sector size)	C247 8E	C840	lds	0sysfcb		error retry	
C1B0 10AF SC		sty	-004,u	save address - \$0100	C24A F6	C5B8	ldb	trgdrv			
C1B3 E7	SE	stb	-002,u	save start sector	C24D E7	03	stb	103,s			
C1B5 10BE CSAA		ldy	tabptr	point to table	C24F B9	DE09	jsr	drest		restore	
C1B9 EC	A4	ldd	,y		C252 6A	SF	dec	-001,u			
C1B8 AB	44	adda	004,u	get max track	C254 26	E1	bne	dowri2		retry	
C1BD A7	44	sta	004,u		C256 1A	02	dowri3	orcc	0802	force error	
C1BF E7	5F	stb	-001,u	save table constant	C258 32	C4	dowri4	leas	,u		
C1C1 60	C4	tst	,u	check for track zero	C25A 35	F6	puls	pc,u,y,c,b,a			
C1C3 27	02	beq	setin0		C25C 10AE	42	dowri5	ldy	102,u	verify target disk	
C1C5 31	22	leay	002,y	adjust pointer	C25F 20	02	bra	dowri7			
C1C7 10AE 22		setin0	ldy	002,y	get table address	C261 31	23	dowri6	leay	\$03,y	
C1CA A6	9E	setin0	ida	-002,u	get sector	C263 A6	C4	dowri7	lda	,u	get track
C1CC A6	A6	ida	a,y	get physical sector	C265 E6	A4	10b	,y		get sector	
C1CE A7	00	sta	,z+	store it	C267 27	EF	beq	dowri4			
C1D0 SF		cirb			C269 BD	DE19	jsr	dseek		seek track	
C1D1 E3	SC	adda	-004,u	find start value	C26C BD	DE06	jsr	dverfy		verify it	
C1D3 ED	B1	std	,z++	save start address	C26F 27	F0	beq	dowri6			
C1D5 A6	SE	ida	-002,u	get sector	C271 6A	SF	dowri8	dec	-001,u		
C1D7 4C		inca		make next logical number	C273 27	E1	beq	dowri3		retry	
C1DB A1	A4	cpa	,y	check for 0az sector	C275 A6	C4	lda	,u		get track	
C1DA 23	02	bis	setin2		C277 E6	A4	ldb	,y		get sector	
C1DC 86	01	ida	001	reset to first	C279 AE	21	10c	001,y		get buffer address	
C1DE A7	5E	setin2	sta	-002,u	save sector	C27B BD	DE03	jsr	dwrite		rewrite the sector
C1E0 A1	41	cpa	001,u	check for complete track	C27E 27	E3	beq	dowri7			
C1E2 26	E6	bne	setin1		C280 20	EF	bra	dowri8			
C1E4 6F	84	cir	,z	dark end - null			t				
C1E6 A6	41	ida	001,u	get start sector	C282 0E	C840	getsir	lds	0sysfcb	read sir	
C1E8 AB	5F	adda	-001,u	add to constant	C285 E7	03	stb	103,c			
C1EA A1	A4	cpa	,y		C287 CC	0003	lld	000003		track # sector 3	
C1EC 23	02	bis	setin3		C28A ED	08 1E	std	01,e			
C1EE A6	A4	suba	,y		C2BD B6	04	ida	0007		single sector read	
C1F0 A7	41	setin3	sta	001,u	reset rb on null	C2BF A7	84	sta	,z		
C1F2 32	C4	leas	,u	restore stack	C291 7E	D406	jmp	fns			
C1F4 35	F6	puls	pc,u,y,z,b,a				t				
		t			C294 BE	C848	drvsel	lds	0sysfcb	select drive	
C1F6 34	76	doread	pshs	u,y,z,b,a	C297 E7	03	stb	103,c			
C1F8 33	E4	leau	,s		C299 7E	DE0C	jmp			drive	
C1FA 86	0C	ida	080c				t				
C1FC 34	02	pshs	a		C29C A6	06	0avexy	ida	,z+	0ave b bytes from z to y	
C1FE 1F	12	tfr	x,y		C29E A7	A8	sta	,y+			
C200 20	02	bra	dore2		C2A0 5A		decb				
C202 31	23	doreal	leay	103,y	C2A1 26	F9	bne	0avexy			
C204 A6	C4	dore2	ida	,u	C2A3 39		rts				
C206 17	00AF	lbsr	displa				t				
C209 E6	A4	ldb	,y	get sector	C2A4 34	06	lookup	pshs	b,a	find max sector in table	
C20B 27	18	beq	dore3		C2A6 0E	C320	10b	0tabtop		point to table	
C20D AE	21	lda	001,y	get buffer address	C2A9 A6	00	lda	,z+		get size	
C20F 8D	DE08	jsr	dread	read the sector	C2AB E1	84	lookup	capb	,z	check sector count	
C212 27	EE	beq	doreal		C2AD 27	07	beq	looku2			
C214 BE	C840	lda	0sysfcb		C2AF 30	06	leaf	00b,x			
C217 F6	CSBA	ldb	srdrv		C2B1 4A		deca				
C21A E7	03	stb	103,z		C2B2 26	F7	bne	looku1			
C21C BD	DE09	jsr	drest	restore	C2B4 1C	FB	andcc	08fb		force error	
C21F 6A	SF	dec	-001,u		C2B6 35	B6	lookup	puls	pc,b,a		
C221 26	E1	bne	dore2	retry			t				
C223 1A	02	orcc	002	set error	C2B8 01	CSBD	displa	cpa	lastrk	display track number	
C225 32	C4	dore3	leas	,u	C2B8 27	I2	beq	displa			
C227 35	F6	puls	pc,u,y,z,b,a		C2B8 34	76	pshs	d,u,z,y			
		t			C2B8 87	CSBD	sta	lastrk			

C2C2 BE C5BD	idx	blastrk	point to track	C35E C475	fdb	tab5dd	
C2C5 BD CD3C	jsr	outhex	display track	C35E 34 B4	fcb	934,904	
C2C8 66 0D	lda	080d	cr	C360 C37A	fdb	tab8ds	
C2CA BD C00F	jsr	outhch		C362 C3D2	fdb	tab8dd	
C2CD 35 76	puls	d,u,x,y		C364 3A 20	fcb	93a,926	
C2CF 39	displx	rts		C366 C37A	fdb	tab8ds	
				C368 C407	fdb	tab8de	
C2D0 BD CD1E	putget	jsr	pstrng	;			
C2D3 BD CD15	jsr	getchr		;			
C2D6 B1 03	cmpa	0803	check for abort	;			
C2D8 1027 0043	lbeq	pabort		;			
C2DE 84 SF	anda	085f		;			
C2DE 81 59	cpa	0859		;			
C2E0 39		rts		;			
				;			
C2E1 70 C5BE	psourc	tst	single	insert source disk	C36A 0F 01 06 0B	tab8ss fcb	90f,901,906,90b,904,909,90e,902
C2E4 26 14	bne	psours			C372 07 0C 05 0A	fcb	907,90c,905,90a,90f,903,908,90d
C2E6 34 76	pushs	d,u,x,y			C37A 1E 01 06 0B	tab8ds fcb	91e,901,906,90b,904,909,90e,902
C2E8 BE C521	idx	01sourc			C382 07 0C 05 0A	fcb	907,90c,905,90a,90f,903,908,90d
C2EB BC C504	cpa	lasosq			C38A 17 1C 1B 15	fcb	917,91c,910,915,91a,913,910,91d
C2EE 27 05	beq	psosrd			C392 11 16 1B 14	fcb	911,916,91b,914,919,91e,912
C2F0 BF C504	stx	lasosq			C399 1A 01 0E 09	tab8sd fcb	91a,901,90e,909,916,904,911,90c
C2F3 BD DB	bsr	putget			C3A1 19 07 14 02	fcb	919,907,914,902,90f,90a,917,905
C2F5 BD CD24	psoudr	jsr	pcrlf		C3A9 12 0D 1A 08	fcb	912,90d,91a,908,915,903,910,90b
C2F8 35 76	puls	d,u,x,y			C3B1 1B 06 03	fcb	918,906,913
C2FA 39	psourx	rts			C3B4 0D 01 06 0B	tab8se fcb	91d,901,906,90b,910,915,91a,902
					C3B8 07 0C 11 16	fcb	907,90c,911,916,91b,90c,908,90d
C2FB 7D C5BE	ptarge	tst	single	insert target disk	C3C4 12 17 1C 04	fcb	912,917,91c,904,909,90e,913,918
C2FE 26 14	bne	ptargx			C3CC 10 05 0A 0F	fcb	91d,905,90a,90f,914,919
C300 34 76	pushs	d,u,x,y			C3D2 34 01 0E 09	tab8dd fcb	934,901,90e,909,916,904,911,90c
C302 BE C548	idx	01targe			C3DA 19 07 14 02	fcb	919,907,914,902,90f,90a,917,905
C305 BC C504	cpa	lasosq			C3E2 12 0D 1A 08	fcb	912,90d,91a,908,915,903,910,90b
C30B 27 05	beq	ptargd			C3EA 1B 06 13 20	fcb	918,906,913,92d,91b,928,923,930
C30A BF C504	stx	lasosq			C3F2 1E 2B 26 33	fcb	91e,92b,926,933,921,92e,91c,929
C30D BD C1	bsr	putget			C3FA 24 31 1F 2C	fcb	924,931,91f,92c,927,934,922,924
C30F BD CD24	ptargd	jsr	pcrlf		C402 1D 2A 25 32	fcb	91d,92a,925,932,920
C312 35 76	puls	d,u,x,y			C407 3A 01 06 0B	tab8de fcb	93a,901,906,90b,910,915,91a,902
C314 39	ptargx	rts			C40B 07 0C 11 16	fcb	907,90c,911,916,91b,903,908,90d
					C417 2F 34 39 21	fcb	92f,934,939,921,926,92b,930,935
C315 BE C49A	pferrd	idx	01ferrd	message output routines	C41F 3A 22 27 2C	fcb	93a,922,927,92c,93f,936,912,917
C318 20 0D	bra	exits			C427 1C 04 09 0E	fcb	91c,904,909,90e,913,918,91d,905
C31A BE C40F	pferwt	idx	01ferwt		C42F 0A 0F 14 19	fcb	90a,90f,914,919,91e,923,926,92d
C31D 29 08	bra	exits			C437 32 37 1F 24	fcb	932,937,91f,924,929,92e,933,938
C31F BE C56F	pabort	lds	01abort		C43F 20 25 2A	fcb	928,925,92a
C322 20 03	bra	exits			C442 0A 01 04 02	tab5ss fcb	90a,901,904,902,905,903,906,909
C324 BE C4E4	pbdrv0l	idx	01bdv0l		C44A 07 0A 08	fcb	907,90a,908
					C44D 12 01 07 0D	tab5sd fcb	912,901,907,90d,902,908,90e,903
C327 BD CD1E	exits	jsr	pstrng	print error and exit	C455 09 0F 04 0A	fcb	909,90f,904,90a,910,905,90b,911
C32A 2E CD03	jmp	arms			C45D 06 0C 12	fcb	906,90c,912
					C460 14 01 04 02	tab5ds fcb	914,901,904,902,905,903,912,910
C32D 0A	tabtop	fcb	90a	number of entries in table	C468 13 11 14 06	fcb	913,911,914,906,909,907,90a,908
C32E 0A 0B		fcb	90a,90B	sectors, format constant	C470 0B 08 0C 0F	fcb	90b,90e,90c,90f,90d
C330 C442		fdb	tab5ss	track = 0	C475 24 01 07 0D	tab5dd fcb	924,901,907,90d,902,908,90e,903
C332 C442		ldb	tab5ss	track > 0	C47D 09 0F 16 1C	fcb	909,90f,916,91c,922,917,91d,923
C334 0F 03		fcb	90f,903		C485 1B 1E 24 04	fcb	918,91e,924,904,90a,910,905,90b
C336 C36A		fdb	tab8ss		C48D 11 06 0C 12	fcb	911,906,90c,912,919,91f,913,91a
C338 C36A		fdb	tab8ss		C495 20 14 1B 21	fcb	920,914,91b,921,915
C33A 12 02		fcb	912,902				
C33C C442		fdb	tab5ss		C49A 2D 20 20 66	1ferrd fcc	"-- fatal error reading source disk!"
C33E C440		fdb	tab5sd		C4BD 07 04	fcb	907,904
C340 14 07		fcb	914,907		C4BF 20 20 20 66	1ferwt fcc	"-- fatal error writing target disk!"
C342 C46F		fdb	tab5ds		C4E2 07 04	fcb	907,904
C344 C460		fdb	tab5ds		C4E4 20 20 20 75	1bdv0l fcc	"-- unable to restore system id "
C346 1A 04		fcb	91a,904		C503 72 65 63 6F	fcb	"record!"
C348 C36A		fdb	tab8ss		C50A 07 04	fcb	907,904
C34A C399		fdb	tab8sd		C50C 20 20 20 6D	1compl fcc	"-- mirror complete!"
C34C 10 12		fcb	91d,912		C51F 07 04	fcb	907,904
C34E C36A		fdb	tab8ss		C521 20 20 20 69	1sourc fcc	"-- insert source disk and hit a key. "
C350 C384		fdb	tab8se		C546 07 04	fcb	907,904
C352 1E 03		fcb	91e,903		C548 20 20 20 69	1targe fcc	"-- insert target disk and hit a key. "
C354 C37A		fdb	tab8ds		C56D 07 04	fcb	907,904
C356 C37A		fdb	tab8ds		C56F 20 20 20 6D	1aborts fcc	"-- mirror aborted!"
C358 24 02		fcb	924,902		C581 00 0A	fcb	90d,90a
C35A C46B		fdb	tab5ds				

C503 20 20 20 74	fcc	"-- target disk should be reformatted"		
C504 07 04	fcb	\$07,04		
t				
C5AA	tabptr	reb	4	local variables
C5AE	srtctks	reb	2	
C5B0	trqtkts	reb	2	
C5B2	loctab	reb	2	
C5B4	lasosq	reb	2	
C5B6	countr	reb	1	
C5B7	trkfit	reb	1	
C5B8	maxtrk	reb	1	
C5B9	maxsec	reb	1	
C5BA	srcdrv	reb	1	
C5BB	trgdrv	reb	1	
C5BC	verflg	reb	1	
C5BD	lastrk	reb	1	
C5BE	single	reb	1	
C5BF	idbuff	reb	13	sir bold area
t				
0000	buffer	equ	start	beginning addr of buffer
t end start				
t flex system defined entry vectors				
t				
flex	equ	\$c000		
t				
linbuf	equ	\$c000	line buffer	
codadr	equ	\$c100	utility command space (1.5 k)	
cadend	equ	\$c700	utility command space end	
sysfcb	equ	\$cB40	system fcb address	
t				
t global values specified by tlyset and asn				
t				
bspcchr	equ	\$cc00	backspace character	
delchr	equ	\$cc01	delete character	
ealchr	equ	\$cc02	end of line character	
depth	equ	\$cc03	depth count	
width	equ	\$cc04	width count	
nulls	equ	\$cc05	null count	
tabchr	equ	\$cc06	tab character	
bsechr	equ	\$cc07	backspace echo character	
pause	equ	\$cc08	pause control byte	
escchr	equ	\$cc09	escape character	
s_drn	equ	\$cc0a	system drive number	
w_drn	equ	\$cc0b	working drive number	
t				
t flex system global variables				
t				
sysflg	equ	\$cc0d	use system drive flag	
sysdate	equ	\$cc0e	date registers	
lsttra	equ	\$cc11	last terminator character	
cbuftpt	equ	\$cc14	line buffer pointer	
escret	equ	\$cc16	escape return register	
curchr	equ	\$cc18	current natch character	
prevch	equ	\$cc19	previous natch character	
curict	equ	\$cc1a	current line count	
loadao	equ	\$cc1b	loader address offset data	
xfrffq	equ	\$cc1d	transfer address flag	
zfradr	equ	\$cc1e	transfer address of loaded file	
outsnt	equ	\$cc22	output switch	
insnt	equ	\$cc23	input switch	
docodf	equ	\$cc28	docad entry flag	
curcol	equ	\$cc29	current output column	
memend	equ	\$cc2d	end of memory address	
cpultype	equ	\$cc33	cpu type flag	
retadr	equ	\$cc43	doced return address	
ulcfag	equ	\$cc49	upper/lower case map flag	
prompt	equ	\$cc4e	pointer to prompt string	
t				
t cpu type flag bit definitions				
t				
cpu_2hz	equ	\$10000000	t => 2 ohz cpu clock rate	
cpu_slow	equ	\$01000000	t => memory stretch active	
cpu_50hz	equ	\$00100000	t => 50 hz power line frequency	
cpu_rasf	equ	\$00010000	t => cpu ras is available	
cpu_rtck	equ	\$00001000	t => 6819 real time clock avail	
cpu_iobs	equ	\$00000100	t => i/o set up like old box	
cpu_time	equ	\$00000010	t => 6840 timer available	
cpu_zoem	equ	\$00000001	t => extended memory is used	
t				
t printer driver interface addresses				
t				
pt_rap	equ	\$cc35	printer reserved area pointer	
pt_len	equ	\$cc37	printer reserved area length	
pt_dev	equ	\$cc39	printer device address	
pinit	equ	\$cccc0	printer initialize vector	
ptero	equ	\$cccd0	printer close vector	
pbkt	equ	\$cccd8	printer ready check vector	
pout	equ	\$ccce4	printer character output module	
prcflg	equ	\$cccf0	active spooling process flag	
t				
t flex system defined entry vectors				
t				
colds	equ	\$cd00	flex cold start address	
wares	equ	\$cd03	flex ware start address	
renter	equ	\$cd06	re-enter flex processing	
inch	equ	\$cd09	input character (low level)	
outch	equ	\$cd0f	output character (low level)	
getchr	equ	\$cd15	input character routine	
putchr	equ	\$cd18	output character routine	
inbuff	equ	\$cd1b	input line buffer	
pstrng	equ	\$cd1e	print string	
class	equ	\$cd21	classify character	
pcrlf	equ	\$cd24	print cr/lf sequence	
nxtch	equ	\$cd27	get next character from input	
qefil	equ	\$cd2d	scan file spec address	
t				
load	equ	\$cd30	load file entry point	
setext	equ	\$cd33	set up file extension	
outdec	equ	\$cd39	output decimal number	
outhex	equ	\$cd3c	output hexadecimal number	
rplerr	equ	\$cd3f	i/o error abort routine	
qhex	equ	\$cd42	get hexdecimal specification	
outadr	equ	\$cd45	output hexadecimal address	
indec	equ	\$cd48	get decimal number	
docad	equ	\$cd4b	docad entry address	
status	equ	\$cd4e	check terminal input status	
t				
t low level terminal and interrupt control addresses				
t				
intap	equ	\$d3de	vector for input tap routine	
dumay	equ	\$d3e0	dumay rts instruction for re	
setirq	equ	\$d3e1	set irq process vector	
clrirq	equ	\$d3e3	clear irq process vector	
inch	equ	\$d3e5	low-level term input w/o echo	
t_off	equ	\$d3ed	timer off routine address	
t_on	equ	\$d3ef	timer on routine address	
t_init	equ	\$d3f1	timer initialize routine addr	
tint	equ	\$d3f5	low-level terminal initialize	
tcheck	equ	\$d3f7	low-level terminal check addr	
touch	equ	\$d3f9	low-level terminal output addr	
tanche	equ	\$d3fb	low-level terminal input & echo	
t				
t file management system entry points				
t				
fascls	equ	\$d403	close up all tiles entry	
fas	equ	\$d406	tile manager exec call	
fcbase	equ	\$d409	file control block base	
verify	equ	\$d435	fas verify flag	
surtab	equ	\$d436	fas surname table	
t				
fcblen	equ	256+64	file control block length	
t				
t disk driver entry points				
t				

dread	equ	\$de00	read sector routine
dwrite	equ	\$de03	write sector routine
dverfy	equ	\$de06	verify routine
drest	equ	\$de09	drive restore routine
drive	equ	\$de0c	drive select routine
dcheck	equ	\$de0f	check drive ready
dquick	equ	\$de12	quick check drive ready
dseek	equ	\$de1b	drive seek-to-sector routine

## BUILD YOUR OWN FAT MAC

by  
**Mike Wolf**  
 3195 Arizona  
 Los Alamos, NM 87544

I guess the big news is that I'm writing this on my FAT Mac. I just upgraded to 512K. There were no unexpected problems and the software I have tried so far seems to work OK and takes advantage of the added RAM. I can dimension a array of 45,000 double precision numbers in Microsoft Basic. Or, I can type a article of 100 pages or so in MacWrite (this is a calculated number). I haven't had time to write that much). More good news is that the RAMs have just taken a plunge in price, so the cost isn't completely out of reason.

**Now for the bad news.** This is not a job for the novice hardware hacker as it requires unsoldering 16 ICs from a 4-Layer PCB. It also, of course, negates any warranty, and probably will make it difficult to get service through normal channels. However, for those of you who know no fear, here's how to do it.

This procedure is for the Original Board. It is my understanding that Apple has a new board out for the Fat Mac which will be installed in the 128K Macs when present stocks are depleted. I have not seen one, but I understand that you will only have to replace the RAMs in the new board.

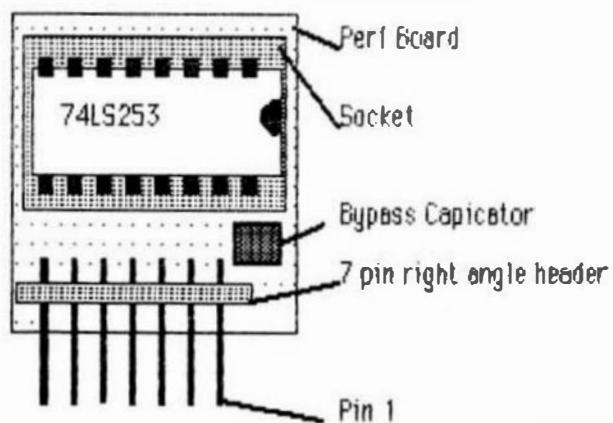
a) You will need the following tools: A #5 torklok screwdriver long enough to reach the screws under the handle. A conventional screwdriver can be modified for this task by grinding the blade to fit in the splines of the screws. A temperature controlled soldering iron with a fine tip suitable for high density printed circuit work. A good quality desoldering tool such as a Solduvac. Don't scrimp on the desoldering setup! A ruined board is a pretty expensive way to learn.

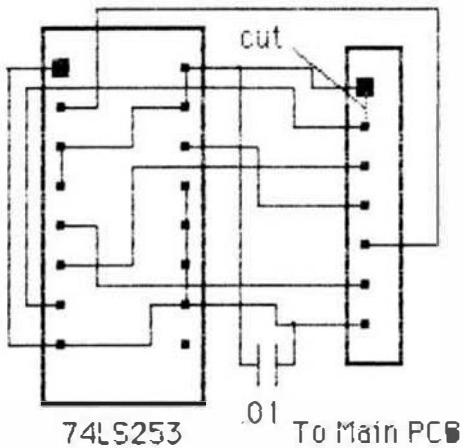
b) You will need the following parts: 16 256K Dynamic RAMs (41256 type), a small piece of perf board large enough to hold 1 16 pin IC, a 7 pin strip of right angle header (more about this later), a bypass capacitor (.01 to .1 micro farad ceramic), 1 74LS253 IC, and 17 Top quality IC sockets. Don't scrimp on these sockets. Use the machined pin type that cost about \$1 apiece. I have used both Hitachi and NEC RAMs for this conversion; the Hitachi part number is MT50256-20, the NEC number is D41256-20. I see no reason that other parts of this type shouldn't work also. 200nS parts are fast enough.

c) Remove the five screws holding the case together (2 under the handle, 2 at the bottom of the back at the ends of the connectors, and one under the battery cover). Place the computer face down on a padded surface and remove the rear cover. It fits tightly and will need some persuading. Careful prying in the crack, jerking the rear cover, etc., to separate it. Once the rear cover is off you will see the board which fits parallel to the bottom of the case; this is the Computer Board. Remove it by disconnecting the two connectors (one to the disc, the other to the vertical board) and slide the board to the rear.

d) Set the computer aside and examine the board. Along the front center of the board is the RAM chips in 2 rows of 8 ICs each. On the left end of the CPU is a row of 7 solder filled holes. Once you have identified these items, get out your desoldering equipment and go to work. Clean the solder out of the 7 holes, and desolder and remove the 16 RAM chips. Be careful!! Clean the board carefully to remove flecks of solder that might cause shorts, and examine it (preferably with a magnifier) to insure that no traces have been broken. If they have, repair them with stripped wire-wrap wire. Then install 16 sockets in the RAM locations, and install the new RAM chips in the sockets.

e) Construct the multiplexer as shown in the illustrations, wiring it as shown. Cut the trace between the square padded hole (pin 1) and the second hole of the row of holes you just cleaned, out and install the multiplexer in place. The board should sit vertical to the computer board. Check your work twice: are the ICs firmly in the sockets with no bent pins? Is Pin #1 to the front of the board? Clean the flux off with a solvent and old toothbrush. Check for shorts. When you are satisfied, reinsert the board into the case and reconnect the cables. Plug it in and turn it on. If you get the usual beep and sign-on, followed by the no-disc icon, all is well. Insert a boot disc and check it out. If you have MicroSoft BASIC, run it and try "?fr(0)" you should get 374000 or so. Run some other stuff to check it out, and then close it up. You've just saved enough to buy a second disc drive!!





## FLEX 2/9 CATALOG UTILITY

### FLEX 2/9 CATALOG UTILITY (CAT.CMD)

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FABENS, TEXAS 79838  
PHONE 915-764-3607

This article describes an enhancement of the old TSC FLEX 2.0 CAT.CMD utility. This new version should be usable for both FLEX 2 and 9 systems. I can only vouch for my FLEX 2 configuration. I bought my SW6800 back in the days before the Earth cooled, and have put several thousand hours into software development for it. The following listing is the result of finally sitting down and doing something about the limitations of the old CAT.CMD.

CAT.CMD is pretty much compatible with the syntax of the old CAT.CMD utility. The main difference being that the input line parameters are ALL tested for syntax first; redundant options and match lists are ignored; and drive numbers, options and match lists can be entered in any order.

New features are listing of catalog protected files, deleted files, and two forms of printout.

Listing of catalog protected files ('P' option) is assessed by using a 3 character password (optional) for those files that you have misplaced.

Listing of deleted files ('D' option) is handy when you have inadvertently (like 2:00 in the A.M.) deleted a file by mistake (but not over written) and you use some sort of disk recovery utility in conjunction with CAT.CMD, such as the DISKSAVE.CMD utility.

The short form of printout (default) is ideal to view all the file names on one page, when your only concern is the presence of the names and their extensions. The listing can be as many as 9 names wide ('C' option) or as few as 1. A special 'CO' option will list in the old FLEX CAT.CMD format.

The expanded form ('E' option) will list the file number, drive number the file was found on, file name, file extension, file protection (if any), file start track-sector, and file end track-sector (again used in conjunction with a disk recovery utility).

The assembled listing is pretty much self documenting and should be fairly easy to make any changes as far as password, option codes, and default conditions.

### CONCLUSION

Believe it or not, this utility started out as an experiment with the FLEX documented subroutines and just snowballed, after a month, into its present form. It sure has been an experience and I enjoyed it immeasurably.

I've been a subscriber to '68 since day one, and I'm glad to finally contribute something.

```

NAM    CAT.CMD
      CAT.CMD UTILITY (ENHANCED VERSION)
      DONALD GOULETTE
      BOX 1153
      FABENS, TEXAS 79838
      (915)-764-3607
      26 MARCH 1984
  
```

#### \*SYNTAX FOR 'CAT.CMD':

- \*1.       ++=CAT
- \*2.       ++=CAT +E 1 0 +0
- \*3.       ++=CAT +EP00G 0 .SYS P.CMD A
- \*4.       ++=CAT 0 .TXT,+C9,1,+PD0G
- \*5.       ++=CAT 0 <CD
- \*6.       ++=AT .CMD +DCSPD0G 0 1 .CMD COPY.CMD

\*LINE 1: CAT WORKING DRIVE (ALL FILES) OR BOTH DRIVES  
\* IF WORKING DRIVE = AUTO. (LISTED IN SHORT FORM)

\*LINE 2: AT, IN EXPANDED FORM, DRIVES 0 AND 1 AND THERE  
\* DELETED FILES.

\*LINE 3: CAT, EXPANDED FORM, DRIVE 0, ALL FILES WITH  
.SYS EXTENSION (ALSO CAT PROTECTED, IF ANY),  
ANY FILE THAT STARTS WITH 'P' AND ALSO A '.CMD'  
EXTENSION (ALSO CAT PROTECTED, IF ANY), AND  
ANY FILE STARTING WITH AN 'A' (ALSO CAT PRO-  
TECTED, IF ANY).

\*LINE 4: CAT, DRIVE 0 AND 1, ALL '.TXT' FILES (ALSO  
AT PROTECTED, IF ANY). IN SHORT FORMAT, 5  
COLUMNS WIDE.  
\* SPACES AND COMMAS ARE INTERCHANGABLE.

\*LINE 5: CAT DRIVE 0 (ALL FILES) IN THE OLD CAT.CMD  
FORMAT.

\*LINE 6: CAT, DRIVES 0 AND 1, ALL FILES WITH A '.CMD'  
EXTENSION AND ALSO ALL '.CMD' CAT PROTECTED  
AND DELETED FILES, IF ANY. (EXPANDED FORM)  
NOTE: THE 2ND .CMD INSTRUCTION IN THE LINE  
AND THE COPY.CMD ARE REDUNDANT AND ARE IGNORED.  
\* ALSO, THE 'E' IN THE OPTION LINE INHIBITS THE  
'C9' OPTION REQUEST (SHORT FORM ONLY).

#### \*EXPLANATION OF OPTIONS:

\*PREFIX ALL OPTIONS WITH "OPTCHR", IN THIS CASE "+".  
\*OPTION MAY BE ENTERED INDIVIDUALLY OR TOGETHER WITH OTHERS.

\*EX: +D+PD0G or +D +E +PD0G

\*OPTION "D": LIST A DELETED FILE IF ALL OTHER DRIVE, MATCH  
CONDITIONS HAVE BEEN MET.

\*EX: ++=CAT +D .CMD  
\*(DELETED FILE XYZ.TXT WOULD NOT BE LISTED BUT DELETED FILE  
XYZ.CMD WOULD BE).

\*NOTE: A DELETED FILE WILL HAVE A '?' INSERTED AT THE START  
OF THE FILE NAME (THE FIRST LETTER OF THE NAME OF A DELETED  
FILE IS UNKNOWN BECAUSE FLEX REPLACES IT WITH A \$FF).

\*OPTION "P": LIST A CAT PROTECTED FILE IF ALL OTHER DRIVE,  
MATCH CONDITIONS HAVE BEEN MET.

\*EX: ++=CAT +P0G .SYS  
\*(CAT PROTECTED FILE ABC.CMD WOULD NOT BE LISTED, BUT  
CAT PROTECTED FILE ABC.SYS WOULD BE).

\*NOTE: FOLLOWING THE 'P' OPTION, A 3 ALPHA-NUMERIC CODE MUST  
BE INSERTED. IF AN INCORRECT 3 PART CODE OR NONE, A

•	SYNTAX ERROR WILL BE ISSUED.	C109	PTRD	RMB	2	STRING PTR DESTINATION
•	CHANGE THE STRING AT LABEL 'KEY' FOR ANOTHER PASSWORD.	C108	CLMONT	RMB	1	CLM CNTR
•	CHANGE THE EQU AT LABEL 'PASS' TO 'N' TO DEFEAT THE	C10C	EXPFLG	RMB	1	EXPAND FORM FLAG
•	PASSWORD FEATURE.	C100	MATFLG	RMB	1	MATCH FLAG
C10E	AUTFG	RMB	1	AUTO FLAG		
C10F	DEFFG	RMB	1	DEFAULT FLAG		
C110	FILCNT	RMB	2	FILE CNT		
C112	DRVFG	RMB	1	DRV ENABLE FLAGS		
C113	DELFG	RMB	1	CAT DEL FILE FLAG		
C114	CATFG	RMB	1	CAT PROT FLAG		
C115	CLMLIM	RMB	1	MAX SHORT FORM CLM CNT		
C116	OLDFG	RMB	1	OLD CAT FORMAT		
C117	IOTMP	RMB	2	ADDR OF PTR		
C119	STGPTR	RMB	40	MATCH PTRS STACK		
C141 7F	C10C	CATO	CLR	EXPFLG	SHORT FORM MODE	
C142 7F	C108	CLR	CLMONT	INIT CLM CNTR		
C147 7F	C100	CLR	MATFLG	STRING MATCH FLAG		
C14A 7F	C10F	CLR	DEFFG	DEFAULT FLAG		
C14D 7F	C113	CLR	DELFG	CAT DEL FILE FLAG		
C150 1F	C112	CLR	DRVFG	CLR ALL DRVS		
C153 7F	C114	CLR	CATFG	CAT PROT FLAG		
C156 7F	C10E	CLR	AUTFG	AUTO FLAG		
C159 7F	C110	CLR	FILCNT	FILE COUNTER		
C19C 7F	C111	CLR	FILCNT=1			
C15F 7F	C116	CLR	OLDFG	OLD CAT FORM OFF		
C162 86	FF	LDAA	#FFF	MATCH STRING EDT		
C164 87	C119	STA	STGPTR	INIT IT		
C167 4F		CLRA		ZERO		
C168 81	00	CMPA	#CLMDFT	ZERO MODE?		
C16A 26	07	BNE	OL01	NOT OLD FORM		
C16C 7C	C116	INC	OLDFG	OLD CAT FORM ON		
C16F 86	01	LDAA	#1	1 CLM W/ OL0		
C171 20	02	BRA	OLDIA			
C173 86	00	OLDI	LDAA	DEFAULT CLM CNT		
C175 87	C115	OLDIA	STA	SAVE IT		
C178 86	CDCG	LDAA	CLMLIM	FETCH DEF DRV		
C179 81	FF	CMPA	#RKDRV	AUTO MODE?		
C17D 26	09	BNE	#FFF	NO AUTO MODE		
C17F 86	02	LDAA	#2	DO BOTH DRVS		
C181 87	C10E	STA	AUTFG			
C184 86	CC11	NAMDE	LSTTRM	LAST CHR		
C187 81	00	CMPA	#SD	CR?		
C189 26	14	BNE	CATO1	NO		
C188 8E	CR40	LDX	#FCB	PTR		
C18E 86	CC0C	LOAA	WRKDRV	DEF WRK DRY		
C191 A7	03	STA	3,X	SET DEF DRV		
C193 81	FF	CMPA	#SF	AUTO MODE?		
C195 26	05	BNE	CAT2Y	NO		
C197 86	02	LDAA	#2	CAT BOTH DRVS		
C199 87	C10E	STA	AUTFG			
C19C 7E	C220	CAT2Y	JMP	NO OPTIONS, GO DO IT		
C19F 8D	CD27	CATO1	JSR	GET DRV		
C1A2 24	20	BCC	ALTCB	ALPHA NUMERIC		
C1A4 81	00	CATO1	#100	ALL DONE?		
C1A6 1027	0083	CATO1B	LBEQ	ALL DONE SCAN		
C1A8 8D	C94A	CATO1	JSR	OPTION TEST FOR OPTIONS		
C1AD 25	12	BCC	ERRYX	SYNTAX ERROR		
C1AF 81	00	CMPA	#SD	CR?		
C1B1 27	7A	BNE	CAT2	ALL DONE SCAN		
C1B3 81	2C	CATO1A	CMPA	COMM?		
C1B5 27	E8	BNE	CATO1	OK, SKIP		
C1B7 81	20	CMPA	#\$20	SPACE?		
C1B9 27	E4	BNE	CATO1	OK, SKIP		
C1BB 81	2E	CMPA	#.	EXT?		
C1B0 26	02	BNE	ERRYX	SYNTAX ERROR		
C1B8 20	1E	BRA	SET FLAG			
C1C1 7E	C542	CATO1B	JMP	SYNTAX ERROR		
C1C4 81	34	CATO2	CMPA	TOO LARGE		
C1C6 24	17	BCC	HATO	SET MATCH STRING		
C1CB 84	03	ANDA	#\$03	STRIN ASCII		
C1CA 4C		INCA		OFFSET		
C1CB 5F		CLR8				
C1CC 1A	01	ROT	SEC	SET DRV BIT		
C1CE 59		ROLB	ROLB	ROLL IN NEW DRV		
C1CF 1C	FE	CLC				
C1D1 4A		DECA				
C1D2 26	FA	BNE	ROT	ROTATE MORE		
C1D4 8D	C112	ORAB	DRVFG	MASK IN DRV #		
C1D7 F7	C112	STAB	DRVFG	UPDATE		
C1DA 7C	C10F	INC	DEFFG	NO DEF DRV		
C1D0 20	00	BRA	CATO1	KEEP SCANNING		
C1D9 8E	CC14	LDX	LBPNTR	LINE BUFFER PTR		
C1E2 30	1F	DEX	PRESENT CHP			
>C1E4 8D	C1FA	JSR	SAVPTR	SAVE START OF STRING ADD		
C1E7 24	03	BCC	MAT1	NO ERR		
C1E9 7E	C542	JMP	CAT68	SYNTAX ERR		
C1EC 7C	C100	MAT1	INC	MATCH FLAG ON		
C1EF 8D	CD27	MAT1A	JSR	FIND END OF STRING		
C1F2 24	FB	BCC	MAT1A	AND LOOKOUT ANY STRING #		
C1F4 81	2E	CMPA	#.	PERIOD /EXT1 ?		
C1F6 27	F7	RED	HAT1A	SKIP EXT		
C1F8 20	AA	BRA	CATO1B	CONTINUE		
*SEARCH FOR PLACE ON STRING STACK TO PUT NEW *MATCH STRING POINTER...						
*CAT.CMD UTILITY STARTS HERE						
C100	ORG	FLEX+\$100 UTILITY AREA		C1FA BF	C117	SAVPTR
C100 20 3F	CAT	BRA	CATO	C1FD BE	C119	STX
C102 02	VN	FCB	GET AROUND TEMPS	C200 A6	84	IOTMP
C103	STORE	RMB	VERSION NUMBER	C202 81	FF	SAVPTR
C105	SIZE	RMB	"	C204 27	0C	STRING VECTORS
C107	PIRS	RMB	2	C206 30	01	LOX
			STRING PTR SOURCE	C208 30	01	#\$FF
				C20A BC	C141	END OF VECTORS?
				C206 26	F1	SAVZ
				C20F 1A	01	SEC
						SEARCH FOR END
						TOO FAR?
						NOT YET!
						EAR, OVERFLOW

C211 39					C308 27	03	BED	CAT4	OK
C212 B6	C117	SAV2	RTS	LDAA IDXMP	C30A 7E	C53C	JMP	CAT9	ERROR
C215 A7	84			STAA 0,X	C300 8E	C840	LDX	#FCB	RESET
C217 30	01			INX	C310 86	07	LDAA	#7	GET INFO REC
C219 86	C118			LDAA 10XMP+1	C314 BD	0406	JSR	FMS	CALL FMS - GET REC
C21C A7	84			STAA 0,X	C317 27	03	BEQ	CAT4A	OK
C21E B6	FF			#\$FF	C319 7E	C533	JMP	CAT6	ERRORS
C220 30	01			EDT	C31F A6	04	LDAA	4,X	START OF INFO
C222 A7	84			SET NEW EOT	C321 B1	FF	CMPA	#\$FF	FETCH 1ST CHR
C224 BE	C119			LDX #STG TR	C323 26	09	BEQ	CAT4C	DELETED?
C227 8F	C117			IST ADDR OF MATCH VECTOR	C325 7D	C113	TST	DELF	NOT DELETED!
C228 1C	FE			STX 10XMP	C328 27	E3	REQ	CAT4	LIST DELETED FILE?
C22C 39				OK	C32A 86	3F	LOAA	#7	NO, SKIP IT
				RTS	C32C A7	04	STAA	4,X	FILL IN DEL CHR
					C32E A6	0F	CAT4C	15,X	SAVE FOR LATER
C22D BD	C024	CAT2	JSR	PCRLF	C30 84	10	LDAA	\$810	ATTRIBUTES
C230 7F	C108		CLR	CLMNT	C32 27	05	ANDA		CAT PROTECTED?
C233 7F	C111		CLR	FILONT+1	C334 7D	C114	BEQ	NOPRT	NDT CAT PROTECTED
C236 70	C112		TST	DRVFG	C337 27	04	TST	CATFG	CAT PROT OPTION?
C238 27	2F		BEQ	CAT2Z	C339 A6	04	BEQ	CAT4	NO, SKIP IT
C239 5F			CLR8		C33B 81	00	NOPRT	LDA4	REFETCH?
C23C 86	C112		LDAA	DRVFG	C342 8E	C63C	LDX #STG9		LAST ENTRY?
C23F 84	01		ANDA	#1	C345 8D	C01E	JSR	PCRLF	SECTORS LEFT
C241 26	18		BNE	FND0	C348 8E	C105	LDX #51ZE		SECTORS LEFT
C243 B6	C112		LDAA	DRVFG	C34C 8D	C039	JSR	OUTDEC	NO SPACES
C246 B4	02		ANDA	#2	C34F 8D	C024	JSR	PCRLF	
C248 2 13			BNE	FND1	C352 7D	C10F	TST	DEFFG	DEFAULT MODE?
C24A B6	C112		LDAA	DRVFG	C355 26	13	BNE	NOPRTI	NO DEFAULT DRV
C24D 84	04		ANDA	#4	C357 7D	C10E	TST	AUTFG	ALREADY DONE?
C24F 26	08		BNE	FND2	C359 27	0E	BEQ	NOPRTI	DONE
C251 B6	C112		LDAA	DRVFG	C362 26	03	OEC	AUTFG	NEXT DRV (AUTO MODE)
C254 84	08		ANDA	#8	C364 7E	C539	TST	AU1FG	DRV DONE?
C256 26	03		BNE	FND3	C367 7E	C220	CAT00	JMP	NOT YET
C258 7E	C542		JMP	CAT6B	C368 7D	C112	JMP	CAT2	FLEX
C259 5C			INC8		C370 8E	C844	CAT01	TST	AGAIN?
C25D 5C			FND2	INC8	C380 8F	C107	NOPRTI	DRYFG	DONE?
C25E 43			FND1	INC8	C383 BE	C111	TST	AUTFG	NO YET!
			FND0	CDMA	C386 A6	84	BNE	CAT00	DONE
C25F B4	C112		ANDA	DRVFG	C388 87	C109	CAT01	JMP	MATCH STRING?
C262 B7	C112		STAA	DRVFG	C388 A6	01	BEQ	MAT3	NO MATCH STRING REQUIRED
C265 BE	C840		LDX	#FCB	C379 27	67	LDX #STGPT	1ST PTR	
C268 E7	03		STAB	3,X	C372 7D	C100	CAT4B	SIX	RESET PTR
C26A 7D	C10F	CAT2Z	TST	DEFGG	C377 BE	C119	LDX #IDKMP		
C26D 26	1C		BNE	CAT2A	C378 BF	C117			
C 6F 8E	C840		LDX	#FCB					
C272 86	CC0C		LDAA	WRKDRV					
C273 A7	03		STAA	3,X					
C277 7D	C10E		TST	AUTFG					
C278 27	0F		BEQ	CAT2A					
C27C B6	C10E		LDAA	AUTFG					
C27F 81	02		CMPA	#2					
C281 26	04		BNE	CAT2X					
C283 86	0D		LDAA	#0					
C285 20	02		BRA	CAT2Y					
C287 86	01	CAT2X	LOAA	#1					
C289 A7	03	CAT2Y	STAA	3,X					
C288 8E	C840	CAT2A	LDX	#FCB					
C288 86	10		LDAA	#\$10					
C2 87	84		STAA	0,X					
C292 BD	0406		JSR	FMS					
C 95 27	06		BEQ	CAT2B					
C297 7E	C542	ERRY	JMP	CAT6B					
C29A 7E	C53C	ERRZ	JMP	CAT9					
C290 8E	C840	CAT2B	LDX	#FCB					
C2A0 86	07		LDAA	#\$7					
C2A2 A7	84		STAA	0,X					
C2A4 BD	0406		JSR	FMS					
C2A7 26	F1		BNE	ERRZ					
C2A9 BE	0605		LDX	#STG1					
C2AC BD	C01E		JSR	PMSG					
C2AF BE	C842		LDX	#FCB+2					
C2B2 6F	84		CLR	0,X					
C2B4 5F			CLR8						
C2B5 BD	C039		JSR	OUTDEC					
C2B8 8E	0655		LDX	#STG3					
C2B8 BD	C01E		JSR	PMSG					
C2B6 BE	C844		LDX	#FCB+4					
C2C1 C6	08		LDAB	#8					
C2C3 A6	84	AGNH	LDAA	0,X					
C2C5 27	08		BEQ	ADOK					
C2C7 BD	C018		JSR	PUTCHR					
C2CA 30	01		INX						
C2CC 5A			DEC8						
C2DD 26	F4		BNE	AGNH					
C2CF 06	02	ADOK	LOAB	#2					
C2D1 BD	C9E		JSR	PSPACE					
C2D4 86	23		LDAA	#\$23					
C2D6 BD	C018		JSR	PUTCHR					
C2D9 8E	C84F		LDX	#FCB+15					
C2DC 5F			CLR8						
C2D0 BD	C039		JSR	OUTDEC					
C2E0 BD	C024		JSR	PCRLF					
C2E3 7D	C10C		TST	EXPFLG					
C2E6 27	06		BEQ	NOOK1					
C2E8 8E	C61E		LDX	#STG2					
C2E9 BD	C01E		JSR	PMSG					
C2EE BD	C024	AGNH	JSR	PCRLF					
C2F1 BE	C855		LDX	#FCB+21					
C2F4 A6	84		LDAA	0,X					
C2F6 87	C109		STAA	S12E					
C2F9 A6	01		LDAA	1,X					
C2FB 87	C106		STAA	S12E+1					
C2FE 8E	C840		LDX	#FCB					
C301 8E	06		LDAA	#6					
C303 A7	84		STAA	0,X					
C305 BD	0406		JSR	FMS					
				CALL FMS - DO OPEN					

\*AT THIS POINT, THE INPUT LINE HAS BEEN TESTED...

C308 27	03	BED	CAT4	OK
C30A 7E	C53C	JMP	CAT9	ERROR
C300 8E	C840	LDX	#FCB	RESET
C310 86	07	LDAA	#7	GET INFO REC
C314 BD	0406	JSR	FMS	CALL FMS - GET REC
C317 27	03	BEQ	CAT4A	OK
C319 7E	C533	JMP	CAT6	ERRORS
C31C 8E	C840	LDX	#FCB	START OF INFO
C321 B1	FF	LDAA	4,X	FETCH 1ST CHR
C323 26	09	CMPA	#\$FF	DELETED?
C325 7D	C113	BEQ	CAT4C	NOT DELETED!
C328 27	E3	TST	DELF	LIST DELETED FILE?
C32A 86	3F	REQ	CAT4	NO, SKIP IT
C32C A7	04	LOAA	#7	FILL IN DEL CHR
C32E A6	0F	STAA	4,X	SAVE FOR LATER
C30 84	10	ANDA	\$810	CAT PROTECTED?
C32 27	05	BEQ	NOPRT	NDT CAT PROTECTED
C34 7D	C114	TST	CATFG	CAT PROT OPTION?
C33 27	04	BEQ	CAT4	NO, SKIP IT
C339 A6	04	NOPRT	LDA4	REFETCH?
C33B 81	00	LOAA	#\$00	LAST ENTRY?
C33F 8D	C024	JSR	PCRLF	SECTORS LEFT
C342 8E	C63C	LDX	#STG9	NO SPACES
C345 8D	C01E	JSR	PMSG	
C348 8E	C105	LDX	#51ZE	
C34C 8D	C039	JSR	OUTDEC	
C34F 8D	C024	JSR	PCRLF	
C352 7D	C10F	TST	DEFFG	DEFAULT MODE?
C355 26	13	BNE	NOPRTI	NO DEFAULT DRV
C357 7D	C10E	TST	AUTFG	ALREADY DONE?
C359 27	0E	BEQ	NOPRTI	DONE
C362 26	03	OEC	AUTFG	NEXT DRV (AUTO MODE)
C364 7E	C539	TST	AU1FG	DRV DONE?
C367 7E	C220	CAT00	JMP	AGAIN?
C368 7D	C112	NOPRTI	CAT2	DRYFG
C369 26	F8	BNE	CAT00	DONE?
C370 8E	C844	CAT01	TST	AUTFG
C380 8F	C107	LDX	PTRS	SAVE
C383 BE	C111	LDX	IDXTMP	ADDR OF PTR
C386 A6	84	LOAA	0,X	MSB
C388 87	C109	STAA	PTRD	
C388 A6	01	LOAA	1,X	LSB
C389 27	2E	NATE4	PTRD+1	DEST
C390 BE	C109	LDX	PTRS	FETCH CHR
C393 A6	84	LOAA	0,X	EXT?
C395 27	35	BEQ	EXT1	EXT TIME?
C395 BE	C111	TST	PTRS	ZERO?
C398 8D	41	BEQ	EXT1	MATCH OK
C399 25	41	BCS	MAT3	SAVE
C399 1F	8940	TAB		SAVE
C3A0 30	01	INX		NEXT CHR
C3A2 BF	C109	STX	PTRD	UPDATE
C3A5 BE	C107	LDX	PTRS	SOURCE
C3A8 A6	84	LOAA	0,X	FETCH COMP CHAR
C3AA 30	01	INX		NEXT CHR
C3AF 34	04 A1ED	CJAF J4	PTRS	SAME?
C3B1 27	2E	BEQ	MAT4	MATCH-CONTINUE
C3B3 27	OB	LDX	PTRD	SET PTR
C3B5 BE	C111	LDX	IDXTMP	NEXT ONE
C3B8 30	01	INX		
C3B8 8E	C111	LDX	PTRD	UPDATE
C3C1 81	FF	LOAA	0,X	FETCH
C3C3 26	BB	BNE	MAT4	EDT?
C3C3 8E	C119	TST	PTRD	NO
C3C8 8E	C117	LDX	PTRD	SAVE
C3C8 7E	C117	STX	PTRD	SAVE
C3C8 27	BB	LOAA	0,X	GO WATCH EXT!
C3C8 8E	C10C	MAT3	TST	EXP MODE?
C3D0 8E	C107	BEQ	SH1	SHORT MODE
C3D4 35	02	INC	FILONT+1	FILE ONTR
C3F6 BE	C11D	LDAA	FILONT+1	FETCH ONTR
C3F6 8E	C018	PULA	CMPA	#\$0A
C3F9 5F	C11D	LDX	PTRD	>97
C3FA BD	C018	LDX	PTRD	NO SPACE
C3FF 8D	C53E	LDX	PTRD	POINT TO FILE /
C402 BE	C843	LDX	PTRD	SUPPRESS SPACES
C402 BE	C843	LDX	PTRD	PRINT FILE /
		LDX	PTRD	2 SPACES

C405 A6 84 LDAA 0,X FETCH DRIVE  
 C407 B8 30 ADDA #350 ASCII OFFSET  
 C409 BD CD18 JSR PUTCHR PRINT DRV  
 C40C B6 2E LDAA #' PERIOD  
 C40E BD CD18 JSR PUTCHR  
 C411 RE CB44 SHT1 LDX #FCB+4 FILE NAME  
 C414 C6 08 LDAB #8 8 CHR  
 C416 A6 R4 LP1 LDAA 0,X FETCH CHR  
 C418 B1 00 CMPA #100 NOP?  
 C41A 26 02 BNE LP1A SKIP  
 C41C 86 20 LDAA #820 MAKE IT A SPACE  
 C41E 80 CD18 LP1A JSR PUTCHR PRINT NAME CHR  
 C421 50 01 INX NEXT CHR  
 C423 5A DECB  
 C424 26 F0 RNE 1.P1 NEXT CHR  
 C426 86 2E LDAB #'  
 C428 80 CD18 JSR PUTCHR  
 C42B 06 D3 LDAB #3 3 CHR  
 C42D A6 84 LP LDAA 0,X FETCH EXT CHR  
 C42F 80 CD18 JSR PUTCHR  
 C432 30 01 INX  
 C434 5A DECB  
 C435 26 F6 BNE LP NEXT CHR  
 C437 70 C10C TST EXPFLG EXP MODE?  
 C43A 26 21 BNE LP3 EXP MODE?  
 C43C 70 C116 TST OLDFG OLD CAT FORMAT?  
 C43F 26 IC BNE LP3 YES!  
 C441 B6 C108 LP2A LDAA CLMONT WHICH Q.M?  
 C444 4C INC CLMONT OFFSET  
 C445 B1 C115 CMPA QML IM TIME FOR CR/LF?  
 C448 26 08 RNE LP4 NO, BUT RELOOP  
 C44A BD CD24 JSR PURLF  
 C44D 7F C108 CLR CLMONT RESET  
 C450 20 0B RRA LP4A SKIP  
 C452 7C C108 LP4 INC CLMONT CLM+1  
 C453 C6 03 LDAB #3 3 SPACES  
 C457 80 C58E JSR PSPACE  
 C45A 7E C300 LP4A JMP CAT4 NEXT FILE NAME  
 C45D 8E C855 LP3 LDX #FCB+21 FILE SIZE  
 C460 C6 FF LOAB #FFF LEADING SPACES  
 C462 BD CD39 JSR OUTDEC SECTOR OUT (DEC)  
 C465 C6 02 LOAB #'  
 C467 B0 C58E JSR PSPACE 2 SPACES  
 C46A 70 C10C TST EXPFLG EXP MODE?  
 C46D 27 5C BEQ MTH1 NO, OLD SHORT FORM  
 C46F 86 C85A LDX #FCB+26 DAT  
 C472 7F C103 CLR STORE CLR STORAGE  
 C473 A6 84 LDAA 0,X FETCH DAY  
 C477 B7 C104 STA STORE+1 SAVE IT  
 C47A B1 0A CMPA #80A >?  
 C47C 24 09 BCC NSP NO SPACE  
 C47E 34 02 PSHA  
 C480 86 20 LDAA #820  
 C482 BD CD18 JSR PUTCHR  
 C485 35 02 PULA  
 C487 BE C103 NSP LOX #STORE POINT TO DAY  
 C488 5F CLR8 SUPPRESS SPACES  
 C489 BD CD39 JSR OUTDEC PRINT DAY  
 C490 86 2D LDAA #'  
 C493 BE C959 JSR PUTCHR  
 C495 A6 84 LDX #FCB+25 MONTH  
 C496 8E C905 LDX #TABLE MONTH TABLE  
 C498 4A DECA DEC MONTH  
 C49C 27 0A BEQ FOUND GOT THE MONTH  
 C49E 30 01 STEP INX  
 C500 60 84 TST D,X END OF ENTRY?  
 C492 26 FA BNE STEP NEXT STEP  
 C4A4 30 01 INX START OF NEXT ENTRY  
 C4A6 20 F3 BRA MTH1 NEXT MONTH  
 C4AB A6 84 FOUND LDAA D,X FETCH CHR  
 C4A4 27 08 BEQ MTH NOP IF '00'  
 C4AC BD CD18 JSR PUTCHR PT IT  
 C4AF 30 01 INX NEXT CHR  
 >C4B1 7E C4AB JMP FOUND  
 C4B4 86 2D MTH LDAA #'  
 C4B6 80 CD18 JSR PUTCHR  
 C4B9 8E C858 LDX #FCB+27 YEAR  
 C4BC A6 84 LDAA 0,X FETCH YEAR  
 C4BE 7F C103 CLR STORE  
 C4C1 B7 C104 STA STORE+1 SAVE IT  
 C4C4 5F CLR8 SUPPRESS ZEROS  
 C4C9 8E C103 LDX #STORE POINT TO YEAR  
 C4C8 BD CD39 JSR OUTDEC  
 C4C8 BE C84F MTH1 LDX #FCB+15  
 C4C8 03 LOAB #'  
 C4D0 BD C58E JSR PSPACE 3 SPACES  
 C4D3 A6 84 LDAA 0,X FETCH ATTRIBUTES  
 C4D5 B4 80 ANDA #80 'WRITE' PROT?  
 C4D7 27 04 BEQ PRT1A NO  
 C4D9 86 57 LDAA #'W MRI E PROT  
 C4D8 20 02 BRA PRT1 SKIP  
 C4D0 66 2A PRT1A LDAA #'  
 C4DF BD CD18 PRT1 JSR PUTCHR PT WRITE PROT  
 C4E2 A6 84 LDAA D,X ATTRIB  
 C4E4 B4 40 ANDA #840 'DELETE' PROT?  
 C4E6 27 04 BEQ PRT A NO  
 C4E8 B6 44 LDAA #'D DELETE PROT  
 C4EA 20 02 BRA PRT2 SKIP  
 C4EC 86 2A PRT2A LDAA #'  
 C4EE 80 CD18 PRT2 JSR PUTCHR PT DELETE PROT  
 C4F1 A6 84 LDAA 0,X ATTRIB  
 C4F3 B4 20 ANDA #820 'READ' PROT?  
 C4F5 27 04 BEQ PRT3A NO  
 C4F7 B6 52 LDAA #'R READ PROT  
 C4F9 20 02 BRA PRT3 SKIP  
 C4FB 86 2A PRT3A LDAA #'  
 C4FD 80 CD18 PRT3 JSR PUTCHR  
 C500 A6 84 LDAA 0,X ATTRIB  
 C502 B4 10 C500 80 CD18 PRT4 JSR PUTCHR  
 C504 27 04 LDAA #'C PRT4 JSR PUTCHR  
 C506 66 43 C506 80 CD18 PRT4 JSR PUTCHR  
 C508 20 02 LDAA #'I PRT4 JSR PUTCHR  
 C50A 86 2A LDAA #'C PRT4 JSR PUTCHR  
 C50C 80 CD18 PRT4 JSR PUTCHR  
 C50F 70 C10C TST EXPFLG EXP MODE?  
 C512 26 03 RNE PRT4D JSR OUTADR  
 C514 7E C441 JMP LP7A NO, OLD SHORT FORM  
 C517 30 01 INX  
 C519 30 01 INX  
 C518 06 02 LOAB #'  
 C51D 80 C58E JSR PSPACE 2 SPACES  
 C520 80 CD45 JSR OUTADR TRACK-SECTOR  
 C523 30 01 INX END ADDR OF FILE  
 C525 06 02 LOAB #'  
 C527 80 C58E JSR PSPACE 2 SPACES  
 C52A 80 CD45 JSR OUTADR TRACK-SECTOR  
 C52D 80 CD24 PRT4B JSR PCRLF  
 C530 7E C300 JMP CAT4 REPEAT  
 C533 A6 01 CAT6 LDAA 1,X GET ERROR STATUS  
 C535 B1 08 CMPA #8 EOF ERROR?  
 C537 26 03 BNE CAT9 WARNS  
 C539 7E C003 CAT6A JMP RPTERR REPORT ERR  
 C53F 7E C003 CAT JSR WARNS  
 C542 8E C66C JMP MSG SYNTAX ERR  
 C545 BD CD1E JSR BRA CAT6A ABORT  
 C548 20 EF  
 \*\*\*\*\* OPTION \*\*\*\*\*  
 \*TEST FOR OPTION CODES AND SET FLAGS ACCORDINGLY\*

C54A B1 2B OPTION CMPA #OPTCHR OPTION LEAD IN CHR?  
 C54C 27 03 BEQ OPT2 YES  
 C54E 1C FE OPT1 CLR NO SYNTAX ERROR  
 C550 39 00 RTS  
 C551 BD CD27 OPT2 JSR NXTON GET OPTION  
 C554 81 2C CMPA #' COMM?  
 C556 27 F6 BEQ OPT1 DONE  
 C558 81 20 CMPA #820 SPACE?  
 C55A 27 F2 BEQ OPT1 DONE  
 C55C 81 00 CMPA #'S CR?  
 C55E 27 EE BEQ OPT1 DONE  
 C560 81 45 CMPA #EXPCHR EXP MODE?  
 C562 26 05 BNE OPT3 NO  
 C564 7C C10C INC EXPFLG EXP MODE?  
 C567 20 E8 RRA OPT2 MORE?  
 C569 81 44 OPT3 CMPA #DELCHR CAT DEL FILES?  
 C568 26 05 BNE OPT4 NO  
 C56D 7C C113 INC DELFG CAT DEL FILES?  
 C570 20 DF BRA OPT2 MORE?  
 C572 81 50 OPT4 CMPA #PRODRH CAT PROT FILES?  
 C574 26 20 BNE OPT4 NO  
 C576 86 59 LDAA #PASS PASSWORD MECHANISM ON?  
 C578 81 59 CMPA #'Y YES?  
 C57A 26 15 BNE OPT4 IGNORE PASSWORD  
 C57C 80 CD27 JSR NXTON  
 C57F 81 44 CMPA #KEY MATCH? (1ST CHR)  
 C581 26 38 BNE OPT6 NO MATCH  
 C583 80 CD27 JSR NXTON  
 C586 81 4F CMPA #KEY1 MATCH? (2ND CHR)  
 C588 26 31 BNE OPT6 NO MA CI  
 C58A 80 CD27 JSR NXTON  
 C58D 81 47 INC CATFG PT CAT PROT FILES  
 C58F 26 A BRA OPT7 MORE?  
 C591 7C C114 OPT4A INC CATFG PT CAT PROT FILES  
 C593 81 3A BRA OPT7 MORE?  
 C595 27 DA BRA OPT2 CLM CNT CHANGE?  
 C597 84 0F BRA OPT6 SYNTAX ERROR  
 C599 81 3A JSR NXTON GET CLM CNT  
 C5A1 24 18 INC RICS NOT ALPHA-NUM, ERROR  
 C5A3 81 30 INC OPT6 TOO LARGE?  
 C5A5 27 DA BRA OPT6 ERROR?  
 C5A7 84 0F BEQ OPT3A ZERO?  
 C5A9 87 C115 BRA OPT6 OLD FORM STRIP ASCII  
 C5A9 7C C116 STA CLM IM SAVE FOR LATER  
 C5B1 86 01 BRA OPT2 OLD FORM OFF  
 C5B3 87 C115 BRA OPT2 MORE?  
 C5B6 7C C116 INC OLDFG I CLM  
 C5B8 1A 01 BRA OPT2 OLD CAT FORMAT  
 C5B8 39 00 RTS SEC MORE?  
 \*\*\*\*\* PSPACE \*\*\*\*\*  
 \*PRINT # OF SPACE SET IN 'B' ACC  
 PSPACE LDAA #820 SPACE  
 JSR PUTCHR PRINT SPACE  
 DECB  
 BNE PSPACE MORE  
 RTS  
 \*\*\*\*\* PMSG1 \*\*\*\*\*  
 \*PRINT STRING W/O A CR-LF  
 PMSG1 LDAA 0,X FETCH CHR  
 CMPA #' EOT?  
 BEQ PMSG2 DONE  
 JSR PUTCHR PT IT  
 BRA PMSG1 NEXT CHR  
 RTS  
 \*\*\*\*\* PMSG2 \*\*\*\*\*  
 PMSG2 RTS

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##### WHIMICAL

Need the Ease of Design and Maintainability of "Structured Programming" AND the Speed and Control of Assembly Language? Then WHIMICAL was designed for you! This Single Pass, Recursive Descent Compiler provides the tool for developing simple Utilities to MAJOR Systems in Assembly Language. Supports 3 "Lex" Levels which allow one level of Procedure nesting, or more within "Modules". It is easy to develop programs written for other machines since you are working at the Assembly Language Level. Features unified, user-defined I/O; produces ROMable, relocatable, recursive, re-entrant Code; Structured style and statements with Procedures and Modules; supports Byte and Double-Byte primitives with 3 types of Integers (up to 32 bit); Char and Boolean, and unlimited sized Arrays (vectors only); Interrupt handling; unlimited length Variable Names; Variable Initialization (defaults to \$00); Include "Source File" directive; Conditional compiling; direct Code insertion; control of the Stack Pointer; etc. To quote Ron Anderson in his comments about WHIMICAL in the Sept. '83 Issue of '88 Micro Journal that, except for the lack of floats, "... I have to give this one VERY high rating, ...". It is a FAST Compiler which produces FAST Code (his "Primes" Benchmark ran at 9 secs. on a 2 MHz System).

F and CCF - \$195.00

\*\* SHIPPING \*\*  
Add 2% U.S.A.  
(min. \$2.50)  
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## 'C' COMPILERS

### Windrush Micro Systems

#### C Compiler

By James McCosh. Full featured C Compiler for the FLEX Operating System (lacking ONLY "bit-fields"), including an Assembler. Requires the TSC Relocating Assembler IF the user wishes to implement his own Libraries.

F and CCF - \$295.00

#### Introl

#### C Compiler

A full-featured C, streamlined for the 6809. Generates very efficient object code. Output "benchmarks" close to 10MHz 68000 in 8 Bit Operations; 1.5 times faster than a 4 MHz Z80 when using a 2MHz 6809 System (Re. p 43, "88 Micro Journal, May '83). Floats, etc.

F, CCF, and O - \$375.00

U - \$425.00

One Year Maint. - \$100.00

## PASCAL COMPILERS

### TSC

#### PASCAL Compiler

Native Code Compiler (UCSD Oriented).

F and CCF - \$200.00

#### Lucida

#### PASCAL Compiler

P-Code Compiler (ISO Standard). Designed especially for Microcomputer Systems; Run-time System checks available resources for each task, allowing operation on even minimal computer systems. Allows linkage to Assembler Code for maximum flexibility.

F and CCF 5" - \$190.00

F 8" - \$205.00

#### OmegaSoft

#### PASCAL Compiler

For the PROFESSIONAL; ISO Based, Native Code Compiler. Primarily for Real-Time and Process Control applications. Use custom I/O devices in place of the Pascal INPUT and OUTPUT; Long Int. (32 Bit); Dynamic length strings; interrupt processing, ROM-able, PIC, Re-entrant Code, etc. POWERFUL Includes Source for the Symbolic Debugger, Runtime, and several Utilities. Requires a "Motorola Compatible" Relocating Assembler and Linking Loader.

F and CCF - \$425.00

One Year Maint. - \$100.00

## DECOMPILE

### Southeast Media

#### DUB (A UniFLEX "basic" De-Compiler)

Re-Create a Source Listing from UniFLEX Compiled basic Programs. Easy to Use; works w/ ALL Versions of UniFLEX basic; Output to Disk or Terminal. Time TESTED and PROVEN; SOLD!

U - \$219.95

#### Availability Legend

F = FLEX, CCP = Color Computer FLEX

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#### **COPYCAT**

Pascal NOT required  
Allows reading TSC Mini-FLEX, SSB DOS68, and Digital Research CP/M Disks while operating under FLEX 1.0, FLEX 2.0, or FLEX 9.0 with 6800 or 6809 Systems. COPYCAT will not perform Miracles, but, between the program and the manual, you stand a good chance of accomplishing a transfer. Includes Utilities to List Directories, Copy Files, and convert Text Files when required. Also includes a Utility for investigating Physical Compatibility problems. Programs supplied in Modular Source Code (Assembly language) to make it easier to solve unusual problems.

F and CCF 5" - \$50.00  
F 8" - \$65.00

### Computer Systems Consultants

#### **FLEX DISK UTILITIES**

Eight (8) different FLEX Utilities that should be a part of every FLEX Users Toolbox; Assembly Language (Source Code):

Copy a File with CRC Errors, so it can possibly be salvaged; Test Disk for errors; Compare two Disks; a fast Disk Backup Program; Edit Disk Sectors; Linearize Free-Chain on the Disk; print Disk Identification; and Sort and Replace the Disk Directory (in sorted order).

PLUS

Ten BASIC Programs to:  
A BASIC Resequencer with EXTRAS over "RENUM"; works with ALL Versions of FLEX BASIC AND the Precompiler, checks for missing label definitions, processes Disk to Disk instead of in Memory.

Compare, Merge, or Generate Updates between two BASIC Programs, check BASIC Sequence Numbers, compare two unsequenced files, and 5 Programs for establishing a Master Directory of several Disks, and sorting, selecting, updating, and printing paginated listings of these files.

A BASIC Cross-Reference Program, written in Assembly Language, which provides an X-Ref Listing of the Variables and Reserved Words in TSC BASIC, XBASIC, and PRECOMPILER BASIC Programs. ALL UTILITIES INCLUDE SOURCE (either BASIC or Source Code). An EXCELLENT Value!

F and CCF - \$50.00

## BUSINESS WORD PROCESSING

### Windrush Micro Systems

#### **SCREDITOR III**

EXTREMELY Powerful Screen-Oriented Editor/Word Processor. Almost 50 different commands; EXCELLENT Documentation (over 300 pages), including a full Tutorial Section to help you learn how to use the system. Features Cursor-based editing, dynamic screen Formatting (what you see is what you get), Multi-Column display and editing, "decimal align" columns (AND add them up automatically, if wanted), define multiple keystroke macros, even and odd page number headers and footers, imbed printer control codes in text, full justification series of commands, full "help" support, store common command series on disk for future use, etc. Easy "Set-Up" (for example, you just hit the key you want to use for a specific function, such as "cursor up", and the System reads an stores that key - no digging into tech manuals for codes, etc.); use supplied "set-ups", or remap the keyboard to what you are used too. Except for proportional printing, this package WILL DO IT ALL!

6800 or 6809 FLEX or SSB DOS, OS-9 - \$175.00

### Southeast Media

#### **SPELLB "Computer Dictionary"**

OVER 120,000 words!

No more "Let your fingers do the walking through the Dictionary" while you are entering Text with your favorite Editor or Word Processor. SPELLB is more than just "another Spelling Checker"; it allows you to look up a word from within your Editor or Word Processor so that you KNOW it is right WHEN YOU TYPE IT IN with the SPN.CMD Utility (which operates in the FLEX Utility Space). Yes, it ALSO allows you to check and update the Text after you are finished, along with allowing you to ADD WORDS to the Dictionary, "flag" questionable words in the Text for evaluation later, "view a word in context" before changing or ignoring, etc. SPELLB first checks a "Common Word Dictionary", then the normal Dictionary, then a "Personal Word List", and finally, any "Special Word List" you may have specified. SPELLB also allows the use of Small Disk Storage Systems.

F and CCF - \$129.95

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68 Micro Journal

III Please Specify Your Operating System & Disk Size III

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### Great Plains Computer Co.

#### **STYLOGRAPH**

A full-screen oriented WORD PROCESSOR -- now runs on the Data-Camp and FHL Color FLEX Systems; uses the 51 x 24 Display Screens). Full screen display and editing (i.e., what you see is what you get); supports the Daisy Wheel proportional printers.

SPECIAL CCF - \$195.00

F and D - \$295.00 U - \$395.00

#### **SPELL**

Fast Computer Dictionary,  
F, CCF, OS-9 - \$125.00

U - \$175.00

#### **MAIL MERGE**

Greatly extends the power and flexibility of STYLOGRAPH.  
F, CCF, D - \$145.00 U - \$195.00

### Southeast Media

#### **JUST**

Text Formatter

JUST, a Text Formatter developed by Ron Anderson, provides numerous features which make it a valuable addition to any FLEX Users Software Library. JUST is designed for formatting Text Output for Dot Matrix Printers and provides many unique features:

- Output the "Formatted" Text to the Display for format analysis and change.
- Output the "Formatted" Text to a Text File for use with the supplied FPRINT.CMD for producing multiple copies of the Text on the Printer INCLUDING IMBEDDED PRINTER COMMANDS (this Utility is very useful at other times also, and worth the price of the program by itself).
- "User Configurable" for adapting to other Printers (comes set up for Epson MX-80 with Graftax); provides for up to ten (10) imbedded "Printer Control Commands", such as italics on and off, boldface on and off, etc.
- Automatic compensation for a "Double Width" printed line.
- Includes the normal line width, margin, indent, paragraph, space, vertical skip lines, page length, page numbering, centering, fill, justification, etc.
- Use with ANY Editor.
- Supplied with "Structured Source" (Windrush PL/9); easy to see the flow of the program.

F and CCF - \$49.95

#### **Availability Legend**

F = FLEX, CCF = Color Computer FLEX

O = OS-9, CDO = Color Computer OS-9

U = UNIFLEX

CDD = Color Computer Disk

CCT = Color Computer Tape

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**SOFTWARE**

## DATA BASE MANAGEMENT SYSTEMS

### Westchester Applied Business Systems

#### XDMS

Possibly one of the most powerful Database Management Systems' available, this machine language program is small enough to operate on a single sided 5" disk, yet provides the speed of H.L. and power limited only by the user's imagination. This DMS supports Relational, Sequential, Hierarchical, and Random Access File Structures, and has Virtual Memory capabilities for those Giant Data Bases. XDMS Level I provides a functional "entry level" System which provides for defining a Data Base, entering and changing the Data, and producing Reports. XDMS Level II adds the POWERFUL "GENERATE" facility which uses an English Language Command Structure in manipulating the Data to create new File Structures, Sort, Select, Calculate, etc. XDMS Level III adds several special "Utilities" which provide additional ease of working with the various structures, changing System Parameters, etc.

XDMS Lvl I - F & CCF - \$129.95  
 XDMS Lvl II - F & CCF - \$199.95  
 XDMS Lvl III - F & CCF - \$269.95  
 XDMS System Manual only - \$24.95

## ACCOUNTING PACKAGES

Great Plains Computer Co. and Universal Data Research, Inc. both have Business Packages written in TSC XBASIC for FLEX, CoCo FLEX, and UniFLEX \*\*\*

\*\*\*\*\*  
 \*\*\* Call 800-338-6800 for more information \*\*\*  
 \*\*\*\*\*

### Computer Systems Consultants

#### FULL SCREEN INVENTORY/MRP

The Full Screen Inventory System provides a means of maintaining small inventories. Using a linked, keyed random file structure based upon the item field, it keeps the file in alphabetical order for easier inquiry. With the FIND command, the user may locate and/or print all records matching on partial or complete item, description, vendor, or attributes. Items in backorder or below minimum stock levels may be located and/or printed thru the same process. Printed output may be produced in item or vendor order. A materials requirement planning (MRP) capability for manufacturing environments is included to allow the maintenance and analysis of Hierarchical assemblies of items in the inventory file. It requires TSC's Extended BASIC.

F and CCF - \$100.00, U - \$150.00

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### The Virginia Company

#### BIZPACK

BIZPACK is used for storing accounting, numeric, and financial data which can then be used for planning, budgeting, forecasting, analyzing, etc. While "Electronic Spreadsheets" are extremely useful in many situations, BIZPACK excels in businesses where there are numerous expense columns, revenue sources, significant business indicators, large numbers, erratic week-to-week and month-to-month fluctuations, etc. BIZPACK helps determine statistical relationships, establish trend lines, "smooths" data via moving averages, analyze seasonal data, adjusts for inflation, logs data in Statistics or Column functions, plots data, etc. BIZPACK is oriented toward time series analysis of businesses. The Program displays information on the screen in Columns of Information with each Row conforming to a defined Period of Time (weeks, months, years, etc.), and is very easy to use (data is easy to enter, change, and modify; commands can be renamed to suit the users requirements; unlimited ability to create specialized commands using common BASIC Statements; etc.). Requires TSC's Extended BASIC.

F and CCF - \$135.00  
 with Source - \$250.00

### Computer Systems Consultants

#### TABULA RASA SPREADSHEET

TABULA RASA is similar to DESKTOP/PLAN and provides for the generation and maintenance of tabular computation schemes often used for analysis of business, sales, and economic scenarios. Its menu-driven user interface provides these capabilities even to those users with no programming experience. Its extensive report-generation capabilities allow the user to generate professional results with minimum effort. It requires TSC's Extended BASIC.

F and CCF - \$100.00, U - \$200.00

### Computer Systems Center

#### DYBACALC

THE Electronic Spread Sheet for 6809 Computer Systems. An extremely POWERFUL Business Tool, this Program will find an unlimited number of "non-business" applications, also if for example, a Full Junior College Electronics Curriculum was set up using DYBACALC. Advanced features like "Table Lookup" make Income Tax work easy; Column or Row Sorting for numerous applications; etc. Completely "Memory Resident". Machine Language, this Program is FAST. Provides STANDARD FLEX Text File output for use with BASIC, Word Processors, Pascal, "C", etc. Also available for Data-Comp and FML FLEX systems using the 50 x 24 Displays.

F and SPECIAL CCF - \$200.00  
 U - \$395.00

## ODDS AND ENDS

### Computer Systems Consultants

#### FULL SCREEN FORMS DISPLAY

This package supports any Serial Terminal with cursor control of Memory-Mapped Video Displays. The package substantially extends the screen Input/Output capabilities of TSC's Extended BASIC programs by providing a simple, table-driven method of describing and using full screen displays. These table entries are easy to set up and maintain, and are normally stored on disk and read as required. A simple, interactive means of generating the forms and the data field definitions is provided.

F and CCF - \$50.00, U - \$75.00

### Computer Systems Consultants

#### FULL SCREEN MAILING LIST

The Full Screen Mailing List System provides a means of maintaining simple mailing lists. Using a random fill structure based on the first character of the name field, it maintains the file in alphabetical order for easier inquiry. With the FIND command, the user may locate all records matching on partial or complete name, city, state, zip, or attributes. Printed listings and output to labels may also be produced on the same selective basis. It requires TSC's Extended BASIC.

F and CCF - \$100.00, U - \$110.00

#### Availability Legend —

F = FLEX, CCP = Color Computer FLEX  
 O = OS-9, CCO = Color Computer OS-9  
 U = UniFLEX  
 CDD = Color Computer Disk  
 CCT = Color Computer Tape

III Please Specify Your Operating System & Disk Size III

Southeast Media

**RAPIER**

CHESS 6809

Requires FLEX and DISPLAYS On Any Type Terminal  
Features:

- \*Four levels of play.
  - \*Swap side. \*Point scoring system.
  - \*Two display boards. \*Change skill level.
  - \*Solve Checkmate problems in 1-2-3-4 moves.
  - \*Make move and swap sides. \*Play white or black.
- This is one of the strongest CHESS programs running on any microcomputer, estimated USCF Rating 1600+ (better than most 'club' players at higher levels).

F and CCF - \$79.95



Southeast Media

**DIET-TRAC Forecaster**

DIET-TRAC Forecaster is an XBASIC program that plans a diet in terms of either calories and percentage of carbohydrates, proteins and fats (C P O%) or grams of Carbohydrate, Protein and Fat food exchanges of each of the six basic food groups (vegetable, bread, meat, skim milk, fruit and fat) for a specific individual.

Sex, Age, Height, Present Weight, Frame Size, Activity Level and Basal Metabolic Rate for normal individual are taken into account. Ideal weight and sustaining calories for any weight of the above individual are calculated. When a weight goal is given (either gain or loss), and a calorie plan is agreed upon between the computer and the individual, the number of days to reach the weight goal is projected. The starting and ending rate of weight loss is calculated, and a daily calendar with each day's weight for a 30-day period is printed.

F - \$59.95  
U - \$89.95

## COLOR COMPUTER SOFTWARE

Stearns Electronics

**FORTH**

Intrigued by FORTH?? Here is a FORTH package tailored to the Color Computer! This package is supplied on Tape, with instructions for transferring it to disk if you wish. Written primarily in machine language, it's speed is unparalleled. A full Semigraphic-8 Editor is provided, along with "goodies" like Graphics and Sound Commands, Printer Commands, Auto-Repeat and Control Keys, etc. If you are interested in learning FORTH, a Trace Feature is provided which is invaluable. If you are a FORTH Pro, this package provides CPU carry Flag accessibility, Fast Task Multiplexing, Clean Interrupt Handling, etc. (Or; you won't "out grow" the basic capabilities of this implementation). Combine this package with Leo Brodie's EXCELLENT Book "Starting FORTH", and you will be a FORTH Expert before you know it (and have a lot of fun doing it!).

Color Computer TAPE - \$58.95

Custom Software Engineering, Inc.

**Color Computer GRAPHIC SCREEN PRINT** Programs

Dumps any "PHODE" Screen to the Printer with the BASIC USR Function. Shift the Printout Left or Right or Reverse Print (Dark for Light Screen and Vice Versa). All Programs on Tape.  
GSPR for R.S. LP-VII/VIII & DMP 100/200/400 \$7.95  
GSPR for Epson w/ Graftrax and Graftrax + \$9.95  
GSPRG for Gemini 10 and 15 \$9.95  
GSPR for the Prowriter Printers \$9.95

Custom Software Engineering, Inc.

**DATE-O-BASE CALENDAR** Program

A Menu Driven EXTENDED BASIC Program which allows the entry of up to 12 Memos per Day, each of which may contain up to 28 Characters, for any day of the Month between the years 1700 and 2099. A Graphic Calendar shows which days contain Memos, and a "Key Word" Search is provided which can be output to the Screen or Printer.

TAPE DATE-O-BASE CALENDAR  
(Each Tape File will hold up to 400 Memos) \$16.95  
DISK DATE-O-BASE CALENDAR  
(4,000 Memos at 300/Month per Disk) \$19.95



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That's INTEREST-ing

Interested in INTEREST (the Honey Knd)? An EXTENDED BASIC program that will help you deal with numerous problems requiring interest calculations. Present Value, Rate of Return, Current Bond Yield and Rate of Return to maturity, Loan Repayment Amortization Schedules, etc.

TAPE - \$29.95

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**DISK DATA HANDLER 64K**

An EXTENDED BASIC Data Management System w/ Mach. Lang. Routines. Allows a max of 246 Chars. and 14 Ffields per Record, and another Record can be linked to the first; B Char. Field Names, up to 99 Chars. per Field. Powerful On-Screen editor for input and update, flexible Output capabilities including output to Disk Files for use by other Programs. Change File Definition without re-entering the Data, Split Files, etc. Allows Multiple Field Sorts, Select on any combination of Fields, etc. An extremely POWERFUL TOOL; Instructions provide examples of Mailing Lists and a Financial Stock Profit and Loss Tracking System.

DISK - \$54.95

Custom Software Engineering, Inc.

**DISK DOUBLE ENTRY**

DISK EXTENDED BASIC Accounting Program w/ Mach. Lang. Routines. A "Traditional" Accounting Package for Small Business, Clubs, Churches, Personal use, etc. Up to four levels of subtotals with Trial Balance, Income Statement, and Balance Sheet Reports. DEE allows up to 300 accounts and a Trial Balance of \$9,999,999.99. Transactions may be up to 14 lines long, and comments and explanations may be freely used. Accounts are traceable to the Journal transaction, which may include comments. Screen reports allow review of past transactions and current balances.

DISK - \$44.95

## This Month's STAR SPECIAL

**'RAPIER' CHESS 6809**

see page 39  
Pkg. \$79.95

Special \$39.95

Offer Ends April 15th, 1985

Availability Legends —

F = FLEX, COC = Color Computer FLEX

O = OS-9, COD = Color Computer OS-9

U = UniFLEX

CD = Color Computer Disk

CCT = Color Computer Tape



```

*STRINGS:
TABLE FCB 'Jan'
      FCB 0
      FCB 'Feb'
      FCB 0
      FCB 'Mar'
      FCB 0
      FCB 'Apr'
      FCB 0
      FCB 'May'
      FCB 0
      FCB 'Jun'
      FCB 0
      FCB 'Jul'
      FCB 0
      FCB 'Aug'
      FCB 0
      FCB 'Sep'
      FCB 0
      FCB 'Oct'
      FCB 0
      FCB 'Nov'
      FCB 0
      FCB 'Dec'
      FCB 0

C605 43 61 74 61 STG1 FCB 'Catalog of Drive Number '
C609 6C 6F 67 20
C60D 6F 66 20 44
C611 72 69 76 65
C615 20 4E 75 60
C619 62 65 72 20
C610 04
C61E 20 23 20 20 STG2 FCB 4 * # Name TYPE SIZE *
C622 20 20 4E 61
C626 60 65 20 20
C62A 20 20 54 79
C62E 70 65 20 20
C632 93 69 7A 65
C636 20 20
C638 20 20 4E 61
C63C 74 65 20 20
C640 20 20 20 50
C644 72 6F 74 20
C648 20 53 74 72
C64C 74 20 20 45
C650 6E 64
C652 00 0A 04
C655 44 69 73 68 STG3 FCB $0,3A,4
C659 3A 20           FCB 'Disk: '

C65B 04
C65C 53 65 63 74 STG5 FCB 4 * Sectors Left =
C660 0F 72 73 20
C664 4C 65 66 74
C668 20 30 20
C66B 04
C66C 53 59 4E 54 STG6 FCB 4 *SYNTAX ERROR, try again !!

C670 41 58 20 45
C674 52 52 4F 52
C678 2C 20 74 72
C67 79 20 61 67
C680 61 69 6E 20
C684 21
C6 5 07 04          F B 7.4

C687 ZEND EOU * END OF RAM FLAG
END CAT

```

0 ERROR(S) DETECTED

#### SYMBOL TABLE:

ADGK	C2CF	ADGK1	C2EE	AGNH	C2C3	AUTFG	C10E	CAT	C100
CATO	C141	CATO1	C19F	CATO1A	C1B3	CATO1B	C1A4	CATO2	C1C4
CAT0D	C367	CAT2	C2D0	CAT2A	C2	CAT2B	C290	CAT2Y	C19C
CAT2X	C287	CAT2Y	C289	CAT2Z	C26A	CAT4	C30D	CAT4A	C31C
CAT4B	C372	CAT4C	C32E	CAT6	C533	CAT6A	C539	CAT6B	C542
CAT9	C53C	CATFG	C114	CLMDNG	0043	CLMONT	C108	CLMDFT	0000
CLMLIN	C115	DEFFG	C10F	DELCHR	0044	DEFLFG	C113	DRVFG	C112
ERRY	C297	ERRYX	C1C1	ERRR2	C29A	EXPCHR	0045	EXPFLG	C10C
EXTI	C3CE	FCB	C840	FILONT	C110	FLEX	C000	FMS	D406
FMSCLS	D403	FNDO	C25E	FRD1	C250	FNFD2	C25C	FNFD3	C258
FOUND	C448	IDXTMP	C1F7	KEY	0044	KFY1	004F	KEY2	0047
LBNTR	CC14	LP1	C416	LP1A	C41E	LP2	C42D	LP2A	C443
LP3	C45D	LP4	C452	LP4A	C45A	LSTTRN	CC11	MATO	C10F
MAT1	C1EC	MAT1A	C1E5	MAT3	C30E	MAT4	C390	MAT4A	C370
MATFLG	C100	MTH	C4B4	MTH1	C4C8	MNDE	C184	MNPT	C339
MCPRTR	C36A	NNSP	C3F6	NSP	C487	NOTCH	C027	NXTMT	C498
OL01	C173	OLD1A	C175	OLDFG	C116	OPT1	C54E	OPT2	C551
OPT3	C569	OPT4	C572	OPT4A	C591	OPT5	C596	OPT5A	C581
OPT6	C58B	OPTCHR	002B	OPT10H	C54A	OUTADR	C045	OUTDEC	C039
PASS	0059	PCRLF	C024	PMSG	C01E	PMSG1	C5C7	PMSG2	C504
PROGR	0050	PRT1	C4DF	PRT1A	C4DD	PRT2	C4EE	PRT2A	C4BC
PRT3	C4FD	PRTJA	C4FB	PRT4	C50C	PRT4A	C50A	PRT4B	C52D
PRT4D	C517	PSPACE	C5BE	PTRD	C109	PTRS	C107	PUTCHR	C018
ROT	C1CE	PTRTR	C03F	SAVI	C200	SAV2	C212	SAPTR	C1FA
SHTI	C411	SIZE	C105	STEP	C49E	STG1	C809	STG2	C01E
STG3	0855	STG5	069C	STO6	C65C	STO7P	C119	STORE	C103
TABLE	C505	VN	C102	WARM5	C003	WRDRV	C00C	ZEMD	C687

# ADDRESS

Here is a program that I call ADDRESS it is an address book program. The basic program is Robert Lund's RADCAT modified and changed to fit this application, so much of the credit should go to him. As R. Lund suggested in the July 83' issue of the Journal that the program might be useful as is or in another program, it has. Anyone using this program should refer to the July 83' Small DIMS articles for reference. My version uses two menu's, a main menu and one with the search command that allows you to change the Name, Address, City State & Zip, or Phone number. It allows you to delete an entry. The main allows you to print it on terminal or printer, exit to FLFX or insert a new entry. I hope this program is useful to someone. Again RON thanks for a GREAT Magazine.

Joseph M. Aulicino  
2014-59th Street  
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* ADDRESS - J.M. Aulicino 10-Oct-83
*
* A MC6809 Address Book program
* Menu Driven program
*
* Credit to R. Lund for his RADCAT program
* from which I used many routines...
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3FFF	MENTOP	EQU	\$3FFF	- Top of table
C003	WARM5	EQU	C003	- Flex ware start
C01E	PSTRNG	EQU	C01E	- Flex print string
C024	PCRFLF	EQU	C024	- Flex print CrLf
C015	GETCHR	EQU	C015	- Flex Get character
C018	PUTCHR	EQU	C018	- Flex Put character
C840	FCB	EQU	C840	- Flex File control block
C03F	RPTERR	EQU	C03F	- Flex Report error
D403	FMSCLS	EQU	D403	- FMS Close
C020	GETFIL	EQU	C020	- Flex Parse file specs
C014	BLCPPNT	EQU	C014	- Flex Line buffer pointer
D406	FMS	EQU	D406	- Flex Call FMS
E070	PRACIA	EQU	E070	- Printer port
E004	TMACIA	EQU	E004	- Terminal port
D400	PORT	EQU	D400	- Flex I/O port in use
C009	PAUSE	EQU	C009	- Flex Pause flag
* Equates				
0100		ORG	0100	
* Temp storage				
0100				- next entry
0102				
0104				
0106				
0108				
010A				
010C				
22				- match string

* SET-UP TABLE															
* zeros the table area															
0122 BE	0778	TABCLR	LOI	BTABLE	- get table addr.	0105 BE	0100	LDX	MEXTOP						
0125 AF			CLRA			0108 C6	1E	LDI	030						
0126 A7	80	TACLR	STA	0,1+	- clear entry table	010A BD	13	BSR	NAME						
0128 8C	3FFF		EMPI	MENTOP		010C BD	C024	JSR	PCRLF						
0129 24	F9		BNE	TACLR		010F BE	05F2	LOI	0MS6						
* Initialize printer port															
0130 96	03		LDA	0803	- reset ACIA	011C BD	C015	NAME	GETCHR						
0135 B7	E070		STA	PRACIA		0102 81	00	CMPI	0800						
0132 86	11		LDA	0811	- set ACIA to 8 bits 2 stop	0104 27	10	BEQ	NAME2						
0134 B7	E070		STA	PRACIA		0106 81	08	CMPI	0808						
* read file into memory															
* starting at BTABLE															
0137 80	04A7		JSR	READ	- get address file	010C BD	C015	NAM1	GETCHR						
* FIND FIRST EMPTY ENTRY															
013A BE	0778	FIND	LOI	BTABLE	- point to table	0102 81	00	CMPI	0800						
0133 A6	84		LDA	0,1	- get 1st entry	0104 27	10	BEQ	NAME2						
013F 27	1A		DEB	FOUND	- if entry =0 found	0106 81	08	CMPI	0808						
0161 C6	SE	F1	LDB	094	- entry size	010A 30	1F	DEI	NAME						
0143 30	01	F2	INI		- step thru table	010C SC	INC0	BNE	NAME						
0145 8C	3FFF	CMPI	MENTOP			010E 20	F0	BRA	NAME1						
0148 26	08	BNE	MIT	- if not MEXTOP goto MIT	010F A7	80	NAM2	STA	0,1+						
0144 8E	0SAF		LDB	0MS62	- Table Full	01E1 5A	DEC0	DEC0	- decrement char. count						
014D BD	C01E		JSR	PSTRNG		01E2 26	E8	BNE	NAME1						
0150 20	09		BRA	FOUND		01E4 20	07	BRA	NAME3						
0152 5A		MIT	DEC0		- dec & check if at next entry	01E6 B6	20	NAM2	LDA	0820					
0153 26	EE		BNE	F2		01E8 A7	80	STA	0,1+	- add spaces till char. count =0					
0155 A6	84		LDA	0,1		01E9 5A	DEC0	BNE	NAME2						
0157 27	02		DEB	FOUND		01EB 26	F8	BNE	NAME2						
0159 20	E8		BRA	F1		01ED SF	0100	XAM3	STS	MEXTOP					
015B BF	0100	FOUND	STI	MEXTOP	- save when found	01F0 39	RTS								
* DISPLAY OPTIONS & GET INPUT															
015E BD	C024	DISPLAY	JSR	PCRLF		01F1 BE	0778	LIST	LDI	BTABLE					
0161 BE	04FA		LOI	0MS61	- display menu	01F4 BF	0100	STI	MEXTOP	- point to beginning of table					
0164 BD	C01E		JSR	PSTRNG		01F7 BD	0382	LSTJ3	JSR	OUT					
0167 BD	C015		JSR	GETCHR	- get char.	01FA BE	0100	LDI	MEXTOP	- output entry					
016A B1	31		CMPI	0831		0200 DF	0100	STI		- if MEXTOP is zero end					
016C 26	02		BNE	DIS1	- is it a "1"	0210 86	FF	LDA	08FF						
016E 26	1C		BRA	ENTER		0212 B7	CC09	STA	PAUSE	- disable pause					
0170 B1	32	DIS1	CMPI	0832		0215 B9	082	PRINT1	JSR	OUT					
0172 26	03		BNE	DIS2	- is it a "2"	0218 BE	0100	LOI	MEXTOP	- print entry					
0174 7E	022C		JMP	SEARCH		021B 27	02	REQ	PRINT2	- if MEXTOP is zero end					
0177 B1	33	DIS2	CMPI	0833		021D 20	F6	BRA	PRINT1						
0179 26	03		BNE	DIS3	- is it a "3"	021F BE	E004	PRINT2	LDI	0MACIA					
017B 7E	0377		JMP	E11T		0222 0F	0F0	STI	PORT	- restore terminal port					
017E B1	34	DIS3	CMPI	0834		0223 4F	CLRA								
0180 26	02		BNE	DIS4	- is it a "4"	0226 B7	CC09	STA	PAUSE	- restore pause					
0182 20	60		BRA	LIST		0229 7E	013A	JMP	FIND						
0184 81	35	DIS4	CMPI	0835		* SEARCH FOR NAME									
0186 26	02		BNE	DIS5	- is it a "5"	022C BD	C024	SEARCH	JSR	PCRLF					
0188 20	7A		BRA	PRINT		022F BE	063B	LOI	0MS611	- Search for					
018A 20	D2	DIS5	BRA	DISPLAY	- if none of above do again	0232 BD	C01E	JSR	PSTRNG						
* ENTER INFO CATALOG															
018C BD	C024	ENTER	JSR	PCRLF		0235 BE	010C	LOI	0STRNG	- point to test string					
018F BE	05CA		LOI	0MS61	- Name	0238 BD	C015	SEA1	GETCHR						
0192 80	C01E		JSR	PSTRNG		023B B1	00	CMPI	0808	- if Cr end string					
0195 BE	0100		LOI	MEXTOP	- pointer to next entry	023B 27	18	BEQ	SEA2						
0198 C6	16		LDB	B22	- character count	023F B1	00	CMPI	0808						
019A BD	33		BSR	NAME	- get name	0241 26	04	BNE	SEA10	- if not BS store char.					
019C BD	C024		JSR	PCRLF		0243 30	1F	DEI	SEA10	- decrement pointer					
019F BE	05B1		LOI	0MS61	- Address	0245 20	F1	BRA	SEA1	- get next char.					
01A2 BD	C01E		JSR	PSTRNG		0247 A7	80	SEA10	STA	0,1+					
01A5 BE	0100		LOI	MEXTOP	- pointer to next entry	0249 8C	0122	CMPI	0STRNG+22	- store char. in test string					
01A8 C6	1E		LDB	080	- character count	024E 27	02	BEQ	SEA2						
01AA BD	23		BSR	NAME	- get address	024E 20	E8	BRA	SEA1	- get next char.					
01AC BD	C024		JSR	PCRLF		0250 4F	SEA2	CLRA	SEA2	- end string					
01AF BE	0500		LOI	0MS65	- City, State	0251 A7	B4	STA	0,1						
01B2 BD	C01E		JSR	PSTRNG		0253 BD	C024	JSR	PCRLF						
* LIST ON PRINTER															
0204 BE	E070	PRINT	LOI	0MACIA		0256 BE	071A	LOI	BTABLE-94						
0207 BF	0F0		STI	PORT	- change I/O port to printer/										
020A BE	077B		LDI	BTABLE	- point to top of table										
020B DF	0100		STI	MEXTOP											
0210 86	FF		LDA	08FF											
0212 B7	CC09		STA	PAUSE											
0215 B9	082		PRINT1	JSR	OUT										
0218 BE	0100		LOI	MEXTOP											
021B 27	02		REQ	PRINT2											
021D 20	F6		BRA	PRINT1											
021F BE	E004		PRINT2	LDI	0MACIA										
0222 0F	0F0		STI	PORT	- restore terminal port										
0223 4F	CLRA														
0226 B7	CC09		STA	PAUSE	- restore pause										
0229 7E	013A		JMP	FIND											
* SEARCH FOR NAME															
022C BD	C024	SEARCH	JSR	PCRLF		022F BE	063B	LOI	0MS611	- Search for					
022F BE	063B		LOI	0MS611		0232 BD	C01E	JSR	PSTRNG						
0232 BD	C01E		JSR	PSTRNG		0235 BE	010C	LOI	0STRNG	- point to test string					
0235 BE	010C		LOI	0STRNG		0238 BD	C015	SEA1	GETCHR						
0238 BD	C015		SEA1	GETCHR		023B B1	00	CMPI	0808	- if Cr end string					
023B 27	18		BEQ	SEA2		023F B1	00	CMPI	0808						
0241 26	04		BNE	SEA10	- if not BS store char.	0243 30	1F	DEI	SEA10	- decrement pointer					
0245 20	F1		BRA	SEA1	- get next char.	0245 20	F1	BRA	SEA1	- get next char.					
0247 A7	80		SEA10	STA	0,1+	0247 A7	80	SEA10	STA	0,1+					
0249 8C	0122		CMPI	0STRNG+22		0249 8C	0122	CMPI	0STRNG+22						
024E 27	02		BEQ	SEA2		024E 27	02	BEQ	SEA2						
024E 20	E8		BRA	SEA1		024E 20	E8	BRA	SEA1						
0250 4F	SEA2		SEA2	CLRA		0250 4F	SEA2	CLRA	SEA2						
0251 A7	B4		STA	0,1		0251 A7	B4	STA	0,1						
0253 BD	C024		JSR	PCRLF		0253 BD	C024	JSR	PCRLF						
0256 BE	071A		LOI	BTABLE-94											

0259 BF	0108		STI	TEMP4	- save pointer	0307 27	53		BED	PHONE	- if '4' goto phone
025C 86	16	SEA3	LDA	022		0309 91	55	CMPA	0035		
025E 07	0106		STA	TEMP3	- store char. count	0308 27	C5	BED	DELETE	- if '5' goto delete	
0261 DE	0108		LDI	TEMP4		0308 81	36	CMPA	0036		
0264 C6	SE		LDB	004	- entry size	030F 1027	FE27	LBDQ	FIND	- if '6' goto other menu	
0264 30	J1	SEA4	INT			0313 20	05	BRA	CHANGE		
0266 8C	3FFF		CMPX	0MENTOP		0315 80	ED24	NAME	JSA	PERLF	
0268 26	02	SME	SEAS		- if not MEANTOP	0318 8E	0SCA	LDI	0MSG1	- Name	
0268 20	68		BRA	FIN		0318 8D	CD1E	JSR	PSTRING		
026F 5A		SEA5	DEC8		- decrement entry count	031E 8E	010A	LDI	TEMPS	- get pointer	
0270 26	F4		BNE	SEA4		0321 C6	16	LDB	022	- set char. count	
0272 BF	0108		STI	TEMP4	- save current entry	0323 80	01CF	JSR	MANI	- get entry	
0275 BF	0104		STI	TEMP2	- save table pointer	0326 20	C2	BRA	CHANGE	- Goto change menu	
0278 8E	010C	SEA9	LDI	013:RNG	- point to test string	0328 8D	CD24	ADD8	JSR	PERLF	
027B BF	0102		STI	TEMP1		0328 8E	0501	LDI	0NS54	- Address	
					* Set 'B' from TSTRING	032E 8D	C91E	JSR	PSTRING		
027E AE	0102	SEA6	LDI	TEMP1	- get pointer	0331 8E	010A	LDI	TEMPS	- get pointer	
0281 E6	B4		LDB	0,1	- get char.	0334 C6	16	LDB	022	- get name count	
0283 24	02	SME	SEA7		- Done ?	0336 30	01	ADD8	INI	- skip over name entry	
0285 20	1F		BRA	MATCH	- all matched	0338 8A	DEC8				
0287 30	01	SEA7	INI		- bump pointer	0339 26	FB	BNE	ADRI		
0289 BF	0102		STI	TEMP1	- save it	033B 86	030	LDB	030	- char. count	
					* get 'A' from table/	0340 8D	01CF	JSR	MANI	- get entry	
028C BE	0104	SEA8	LDI	TEMP2	- get table pointer	0340 20	A8	BRA	CHANGE	- goto change menu	
028F 86	80		LDA	0,1	- get char.	0342 8D	CD24	CITY	JSR	PERLF	
0291 8C	3FFF		CMPX	0MENTOP	- done ?	0345 8E	0500	LDI	0MSG5	- City, State & zip	
0294 27	44		BED	FIN	- if yes exit	0348 8E	010A	LDI	TEAPS	- get pointer	
0296 7A	0106		DEC	TEMP3	- decrement char. count	034E C6	34	LDB	052	- get name + address count	
0299 27	C1		BED	SEA3	- if count =0 go next entry	0350 30	01	CITY8	INI	- skip over name + address entry	
029B BF	0104		STI	TEMP2		0352 8A	DEC8				
029E 34	04 A1E0		CBA		- compare char.	0353 26	FB	BNE	CITY1		
02A2 27	B8		BED	SEA6	- If match get next char.	0355 C6	1E	LDB	030	- set char. count	
02A4 20	B2		BRA	SEA9	- reposition pointer	0357 8D	01CF	JSR	MANI	- get entry	
02A6 DE	0108	MATCH	LDI	TEMP4	- point to entry	0358 20	BE	BRA	CHANGE	- goto change menu	
02A9 BF	0100		STI	NEXTOP		035C 8D	CD24	PHONE	JSR	PERLF	
02AC BF	010A		STI	TEMPS		035F 8E	0502	LDI	0N968	- Phone#	
02AF 46	B4		LDA	0,1		0362 8D	CD1E	JSR	PSTRING		
02B1 27	A9		BED	SEA3	- if open entry find next entry	0365 BE	0104	LDI	TEMPS	- get pointer	
02B3 BD	039C		JSR	OUT3	- print it	0368 C6	52	LDB	082	- get name + address + city count	
02B6 8E	015F		LDI	0MSG13	- Change this entry ?	036A 30	01	PHONE8	INI	- skip name, addr. & city entry	
02B9 80	CD1E		JSR	PSTRING		036C 8A	DEC8	BNE	PHONE1		
02BC 80	CD15		JSR	GETCHR		036D 26	FB				
02B6 81	59		CMPA	0059		036F E6	0C	LDB	012	- set char. count	
02C1 26	99		BNE	SEA3	- if no get next entry	0371 8D	01CF	JSR	MANI	- get entry	
02C3 8E	09FC		LOI	0MSG7	- Are you sure ?	0374 16	FF73	LORA	CHANGE		
					* EXIT ROUTINE						
02C6 80	CD1E		JSR	PSTRING		0377 8D	007C	E8T	JSR	DELFILE	- delete old .BAK file
02C9 80	CD15		JSR	GETCHR		037A 8D	7E	BSR	RENAME	- renames .DAT to .BAK	
02CC 81	59		CMPA	0059		037C 8D	042C	JSR	WRITE	- writes new .DAT	
02CE 26	8C		SME	SEA3	- if no get next entry	037F 7E	C003	JMP	HARMS	- exit to FLEI	
02D0 20	18		BRA	CHANGE	- goto change routine						
02D2 8E	010A	DELETE	LDI	TEMPS	- get entry pointer						
02D5 4F			CLR8	*							
02D6 A7	B4		STA	0,1	- clear first byte of entry	0382 8E	0100	OUT	LDI	NEXTOP	- point to entry
02D8 20	82		BRA	SEA7		0385 A6	84	LDA	0,1	- get 1st char.	
02D8 8D	CD24	FIN	JSR	PERLF		0387 26	13	BNE	OUT3	- if not 0 print it	
02D8 BE	0448		LDI	0MSG12	- ICR1 to continue	0389 C6	SE	LDB	074	- get next entry	
02E0 86	CD1E		JSR	PSTRING		038B 30	01	OUT1	INI		
02E3 8D	CD15		JSR	GETCHR	- wait for keystroke	038B 8C	EFF	CMPX	0MENTOP		
02E6 4F			CLR8			0390 26	02	SME	OUT2		
02E7 7E	01JA		JMP	FIND		0392 20	SF	BRA	OUT8	- exit	
					* CHANGE ENTRY	0394 5A	DEC8	OUT2			
02E8 BE	0474		CHANGE	LDI	0MSG14	0395 26	F4	BNE	OUT1		
02EB BB	CD1E		JSR	PSTRING		0397 8F	0100	STI	NEXTOP		
02F0 8E	0705		LDI	0MSG13	- print menu	039A 20	E8	BRA	OUT		
02F3 BB	CD1E		JSR	PSTRING		039C 8D	CD24	OUT3	JSR	PERLF	- out CrLf
02F6 86	CD15		JSR	GETCHR		039F C6	16	LDB	022	- set char. count	
02F9 81	51		CMPA	0031		03A1 A6	84	OUT4	LDA	0,1	- get char.
02FB 27	18		BED	NAME	- if '1' goto name	03A3 8D	CD18	JSR	PUTCHR	- output char.	
02FD 81	32		CMPA	0032		03A4 30	01	INI			
02FF 27	27		BED	ADDR	- if '2' goto addr	03A6 8C	FFF	CMPX	0MENTOP		
0301 81	33		CMPA	0033		03A8 27	46	BED	OUT8	- if at MENTOP exit	
0303 27	50		BED	CITY	- if '3' goto city	03AB 8A	DECB	DEC8		- decrement char. count	
0305 81	34		CMPA	0034		03AE 26	F1	BNE	OUT4	- if not 0 get next char.	
						03B0 8D	CD24	JSR	PERLF		
						03B3 C6	1E	LDB	030	- set char. count	

0303 A6 B4	OUT5	LDA	0,I	- get char.	0462 26	0F	SNE	WRITE4
0387 2B C018		JSA	PUTCHR	- output char.	0464 20	F1	SRA	WRITE4
035A 30 01		INI						+ CLOSE FILE & EXIT
038C 2C 3FFF		CMP1	MENTOP					
038F 27 32		BED	DUT8	- if at MENTOP exit	0466 BE CB40	WRITE2	LDI	0FCB
03C1 5A		DEC8		- decrement char. count	0469 B6 04		LDA	0,I
03C2 26 F1		BNE	DUT5	- if not 0 get next char.	046B A7 84		STA	0,I
03C4 BD C024		JSA	PCRFL		046D B0 D406		JSA	FMS
03C7 C6 1E		LDB	0J0	- set char. count	0470 26 01		BNE	WRITE3
03C9 A6 B4	OUT6	LDA	0,I	- get char.	0472 39			RTS
03C9 BD C018		JSA	PUTCHR	- output char.				
03CE 30 01		INI						+ ERROR HANDLER
03D0 8C 3FFF		CMP1	MENTOP					
0383 27 1E		BED	DUT8	- if at MENTOP exit	0473 B8 C02F	WRITE3	JSR	RPTERR
0305 5A		DEC8		- decrement char. count	0476 BD D403		JSA	FMSCLS
0306 26 F1		BNE	DUT6	- if not 0 get next char.	0479 7E C003		JNF	WARNS
0308 BB C024		JSR	PCRFL					- report error
0308 C6 0C		LDB	0J2	- set char. count				- close file
03D0 46 B4	OUT7	LDA	0,I	- output char.				- exit to file
0305 80 C018		JSA	PUTCHR					+ DELETE FILE
03E2 30 01		INI			047C BE 052E	DELFILE	LDE	0MSG10
03E4 9C 3FFF		CMP1	MENTOP		047F BF CC14		STI	BUFPNT
03E7 27 0A		BED	DUT8	- if at MENTOP exit	0482 BE CB80		LDE	0FCB
03E9 5A		DEC8		- decrement char. count	0485 B8 C020	JSR	GETFIL	- point to FCB
03EA 26 F1		BNE	DUT7	- if not 0 get next char.	0488 25 0A		BCS	DEL1
03EC BD C024		JSR	PCRFL		048A B6 0C		LDA	0J2
03EF BF 0100		STI	MENTOP	- store current pointer	048C A7 84		STA	0,I
03F2 39		RTS			048E BD D406		JSR	FMS
03F3 7F 0100	OUT8	CLR	MENTOP		0491 26 01		BNE	DEL1
03F5 7F 0101		CLI	MENTOP+1		0493 39			RTS
03F9 39		RTS			0494 BE CB40		DEL1	LDE
					0497 A6 01		LDA	1,I
					0499 B1 04		CMPA	0,I
					049B 26 01		BNE	DEL2
					049D 39			RTS
					049E B0 E03F		DEL2	JSR
					04A1 BD D403		JSA	FMSCLS
					04A4 7E C003		JNP	WARNS
								- report error
								- close file
								- exit to file
								+ READ FILE
03FA BE C075	RENAME	LDE	0FCB+53	- put .BAK name into FCB	04A7 BE 0615	READ	LDE	0MSG8
03FB 10BE 0A22		LDV	0RS69		04A8 BF CC14		STI	BUFPNT
0401 A6 A0	REN1	LDA	0,Y+	- get name char.	04AD BE CB80		LDE	0FCB
0403 81 04		CMPA	0,I		04B0 B0 C028	JSR	GETFIL	- point to FCB
0405 27 04		BED	REN2	- check for end of name	04B3 29 39		BCS	READ3
0407 A7 80		STA	0,I+	- store char. in FCB				- parse file spec
0409 20 F6		BRA	REWI	- get next char.				
040B BE 0615	REN2	LDI	0MSG8					+ READ FILE
040E BF CC14		STX	BUFPNT	- set pointer to .DAT name	04B5 86 01		LDA	0,I
0411 BE C040		LDE	0FCB		04B7 A7 84		STA	0,I
0414 BD C028		JSR	GETFIL	- parse file spec	04B9 BD D406		JSR	FMS
0417 23 0A		BCS	ERROR4		04BC 26 30		BNE	READ3
0419 B6 00		LDA	0J3	- rename function code	04BE 6F 84		CLR	0,I
041B A7 84		STA	0,I		04C0 B6 FF		LDA	0FFF
041D B0 D406		JSR	FMS	- do rename function	04C2 A7 88 3B		STA	59,I
0420 26 01		BNE	ERROR4					- set for no space compression
0422 39		RTS						
0423 B0 C03F	ERROR4	JSR	RPTERR	- report error				+ READ MEMORY IMAGE
0426 B0 D403		JSR	FMSCLS	- close file	04C5 BE CB40	READ1	LDI	0FCB
0429 7E C003		JNP	WARNS	- exit to file	04C8 10BE 0778		LDY	0TABLE
					04CC B8 0406		READS	JSR
					04CF 26 0A		FMS	
					04D1 A7 A0		CMPA	0,Y+
					04D3 10BE 3FFF		BNE	MENTOP
					04D7 27 0B		BED	READ4
					04D9 20 F1		BRA	READ3
								- if at MENTOP go close file
								+ END OF FILE ERROR
042C BE 0778	WRITE	LDE	0TABLE		04D3 BE C040	READ2	LDE	0FCB
042F BF 0102		STI	TEMP1	- save pointer	04D8 10BE 0778		LDY	0TABLE
0432 BE 0415		LDE	0MSG8		04DC B8 0406		READS	JSR
0433 BF CC14		STI	BUFPNT	- set pointer to file name	04E0 26 0A		FMS	
0438 BE C040		LDE	0FCB		04E1 A7 A0		CMPA	0,Y+
0438 BB C028		JSR	GETFIL	- parse file spec	04E3 10BE 3FFF		BNE	MENTOP
043E 25 33		BCS	WRITE3		04E7 27 0B		BED	READ4
0440 B6 02		LDA	0J2	- write function code	04E9 20 F1		BRA	READ3
0442 A7 B4		STA	0,I					- if at MENTOP go close file
0444 B8 D406		JSR	FMS	- do open for write				+ READ MEMORY IMAGE
0447 26 2A		BNE	WRITE3		04E5 BE CB40	READ1	LDI	0FCB
0449 8F B4		CLR	0,I		04E8 A6 01		LDY	0TABLE
044B B6 FF		LDA	0FFF		04EB B1 0B		READS	JSR
044D A7 88 3B		STA	59,I	- no space compression	04EC 26 0A		FMS	
					04ED 39		CMPA	0,Y+
							BNE	READ3
								- if not EOF error go other err
								+ CLOSE FILE & EXIT
0450 BE C040	WRITE1	LDI	0FCB	- FCB pointer	04E4 B6 04	READ4	LDA	0,I
0453 10BE 0102		LDV	TEMP1	- table pointer	04E6 A7 84		STA	0,I
0457 A6 A0	WRITE4	LDA	0,Y+	- get char. from table	04E8 B8 D406		JSR	FMS
0459 10BE 3FFF		CMPY	MENTOP	- if at MENTOP close file	04EB 26 01		BNE	READ3
045D 27 07		BED	WRITE2	- write char. to file	04ED 39			RTS
045F B0 D406		JSR	FMS					

+ NO FILE ERROR

04EE BE CB40 READ3 LDI 0FED  
 04F1 A6 01 LDA 1,1 - get error code  
 04F3 B1 04 CMPA #0  
 04F5 26 EB JNE READ4 - if not no file error go close  
 04F7 7E 042C JNP WRITE

0600 79 6F 75 20  
 0604 73 75 72 65  
 0608 3F 20 28 59  
 060C 20 6F 72 20  
 0610 0E 29 3A 20  
 0614 04  
 0615 41 04 44 32 MS68 FCC "ADDRBOOK.BAT",SD  
 0619 42 4F 4F 4B  
 061D 2E 04 41 54  
 0621 0B  
 0622 41 44 44 52 MS69 FCC "ADDRBOOK.BAK",4  
 0626 42 4F 4F 4B  
 062A 42 01 0B 04  
 062E 41 04 44 52 MS610 FCC "ADDRBOOK.BAK",SD  
 0632 42 4F 4F 4B  
 0634 2E 02 41 4B  
 063A 0B  
 063B 53 65 61 72 MS611 FCC "Search for: ",4  
 063F 63 68 20 66  
 0643 6F 72 3A 20  
 0647 04  
 064B 05 6E 74 65 MS612 FCC "Enter <CR> to continue",4  
 064C 72 20 3C 43  
 0650 52 3E 20 74  
 0654 6F 20 63 6F  
 0658 6E 74 69 6E  
 065C 75 65 04  
 065F 43 68 61 6E MS813 FCC "Change this entry?",0  
 0663 67 65 20 74  
 0667 68 69 73 20  
 066B 65 6E 74 72  
 066F 79 20 3F 20  
 0673 04  
 0674 4E 61 60 65 MS614 FCC "Name.....(1)",SD,SA  
 0678 2E 2E 2E  
 067C 2E 2E 2E  
 0680 2E 2E 2E  
 0684 2E 2E 2B  
 0688 31 29 00 04  
 068E 41 64 64 72  
 0690 65 73 73 2E  
 0694 2E 2E 2E  
 0698 2E 2E 2E  
 069C 2E 2E 2B  
 06A0 32 29 00 04  
 06A4 45 69 78 79  
 06AB 2C 20 53 74  
 06AC 61 74 65 2E  
 06B0 2E 2E 2E  
 06B4 2E 2E 2B  
 06B8 33 29 00 04  
 06BE 50 6B 6F 6E  
 06C0 65 20 23 2E  
 06C4 2E 2E 2E  
 06CB 2E 2E 2E  
 06CC 2E 2E 2B  
 06D0 34 29 00 04  
 06D4 4E 65 6C 63  
 06D8 74 63 20 65  
 06DC 6E 74 72 79  
 06E0 2E 2E 2E  
 06E4 2E 2E 2B  
 06E8 35 29 00 04  
 06EC 4E 6F 20 63  
 06F0 6B 61 6E 67  
 06F4 65 2E 2E  
 06FB 2E 2E 2E  
 06FC 2E 2E 2B  
 0700 36 29 00 04  
 0704 04  
 0705 43 6B 61 6E MS815 FCC "Change which?",4  
 0709 67 65 20 77  
 070D 6B 69 63 6B  
 0711 3A 20 04  
 077B TABLE EDU ++100  
 END TABCLR

0 ERRORIS DETECTED

+ NO FILE ERROR

04FA 20 2A 2A AS61 FCC ".....Phone Book.....",SD,SA  
 04FE 2A 2A 2A 0B  
 0502 50 68 6F 6E  
 0506 65 20 42 6F  
 050A 6F 68 20 2A  
 050E 2A 2A 2A 2A  
 0512 0B 0A  
 0514 0E 65 77 20  
 0518 45 6E 74 72  
 051C 79 2E 2E  
 0520 2E 2E 2E  
 0524 2E 2E 2E  
 0528 2E 2E 2B 31  
 052C 29 0B 0A  
 052F 53 65 61 72 MS611 FCC "Search for name.....(1)",SD,SA  
 0533 63 68 20 66  
 0537 6F 72 20 6E  
 053B 61 6B 65 2E  
 053F 2E 2E 2E  
 0543 2E 2B 2B 32  
 0547 29 0B 0A  
 054A 45 78 69 74  
 054E 20 74 6F 20  
 0552 46 6C 65 78  
 0554 2E 2E 2E  
 055A 2E 2E 2E  
 055E 2E 2B 33  
 0562 29 0B 0A  
 0563 4C 69 73 74  
 0569 20 2A 67 6F  
 056B 6F 6B 2E 2E  
 0571 2E 2E 2E  
 0575 2E 2E 2E  
 0579 2E 2E 2B 34  
 057D 29 0B 0A  
 0580 50 72 69 6E  
 0584 74 20 42 6F  
 0588 6F 6B 2E 2E  
 059C 2E 2E 2E  
 059D 2E 2E 2E  
 059E 2E 2B 33  
 0598 29 0B 0A 0A  
 059C 0A  
 0599 45 6E 74 65  
 05A1 72 20 53 65  
 05A2 6C 65 63 74  
 05A9 69 6F 6E 3A  
 05AB 20 04  
 05AF 2A 2A 2A 20 MS62 FCC "\*\*\* ALL ENTRIES FULL \*\*\*",SD,SA,4  
 05B3 41 4C 4C 20  
 05B7 45 4E 34 52  
 05B8 49 45 53 20  
 05BF 46 53 4C 4C  
 05C3 20 2A 2A 2A  
 05C7 0B 04 04  
 05CA 4E 63 6B 65 MS63 FCC "Name: ",4  
 05CE 3A 20 04  
 05B1 41 64 64 72 MS64 FCC "Address: ",4  
 05B5 65 73 73 3A  
 05B9 29 04  
 05B0 43 69 74 79 MS65 FCC "City, State Zip codes",4  
 05B6 2C 20 53 74  
 05E3 61 74 65 20  
 05E7 3A 69 70 20  
 05E3 63 6F 64 63  
 05EF 3A 20 04  
 05F2 50 6B 6F 6E MS66 FCC "Phone #: ",4  
 05F6 65 20 23 3A  
 05FA 20 04  
 05F4 41 72 65 20 MS67 FCC "Are you sure? (Y or N) ",4

#### SYMBOL TABLE:

ADDR	0320	ADDR1	0336	BUPINT	CC16	CHANGE	02EA	CITY	0342
CITY1	0350	DELI	0494	DEL2	049E	DELETE	02B2	DELFIL	047C
DISI	0170	DIS2	0177	DIS3	017E	DIS4	0184	DIS5	018A
DISPLA	015E	ENTER	018C	ERROR4	0423	E11T	0377	F1	0141
F2	0143	FCB	EB40	FIN	020A	FIND	013A	FMS	0406
FPSCLS	0403	FOUND	015B	SETCHR	CD15	GETFIL	C929	LIST	01F1
L1ST3	01F7	L1ST4	0201	ATCH	0246	MENTOP	3FFF	MSG1	04FA
MSG10	062E	MSG11	062B	MSG12	0648	MSG13	065F	MSG14	0674
MSG13	0705	MSG2	054F	MSG3	05CA	MSG4	05B1	MSG5	0508
MSG6	05F2	MSG7	05FC	MSG8	0615	MSG9	0622	NAME	01CF
NAME2	01E6	NAME3	01ED	NAME4	010F	NAME5	0315	NEITOP	0100
NIT	0152	OUT	0382	OUT1	03B8	OUT2	0394	OUT3	039C
OUT4	02A1	OUT5	03B5	OUT6	03C9	OUT7	03D0	OUT8	03F3
PAUSE	CC09	PCRLS	CD24	PHONE	035C	PHONE1	036A	PORT	BFE0
PRACTA	E070	PRINT	0204	PRINT1	0215	PRINT2	021F	PS1RNG	CD1E
PUTCHR	CD18	READ	0447	READ1	04C5	READ2	0408	READ3	04E6
READ4	04E4	READ5	04EE	REN1	0401	REN2	0408	RENAME	03FA
RPTERR	C03F	SEAL1	023B	SEAL2	0247	SEAZ	0250	SEAD	025C
SEM	0266	SEAS	0246	SEAZ	027E	SEAB	0287	SEAO	028C
SEAR	027B	SEARCH	022C	TABCLR	0122	TABLE	0178	TACLR	0128
TEMP1	0102	TEMP2	0104	TEMP3	0106	TEMP4	0108	TEMP5	010A
TRACIA	E004	TRING	010C	WARM5	C003	WRITE	042C	WRITE1	0450
WRITE2	0466	WRITE3	0473	WRITE4	0457				

## ANALOG TO DIGITAL, DIGITAL TO ANALOG

### 12 BIT ANALOG to DIGITAL, DIGITAL to ANALOG CONVERTER

Ron Anderson has designed a 12 bit 16 channel Analog to Digital converter (to which I have made some modifications) using a National Semiconductor AOC1210HCD; and I have assembled a 12 bit Digital to Analog converter using an Analog Devices AD567KD to be constructed on a Thomas Instrumentation SP-1 board. Tom Gluyes of Thomas Instrumentation has agreed to sell the SP-1 without the 4 6850's for \$175 assembled and tested. If the analog amplifiers are built on the board, there will not be sufficient room to be able to utilize the 6850's unless no other parts are needed. (I assembled the bare board and had spent about \$125 to get the board ready for wire wrapping. I wish that I had asked about buying the board partially populated--the extra \$50 would have been well worth the cost.) The parts cost for the basic AD and DA circuits is about \$175 with each 0-20 mv amplifier being an additional \$30.

### D/A CONVERTER

The D/A converter is designed to give a +/- 5 volt output range for a digital input of 4095 and 0000 respectively (FFF to 000 hexdecimal). Actually the output is adjusted to +4.9976 and -5.0000 for these inputs as most calibration procedures recommend adjusting the output to 1 LSB less than the nominal range. (The device may be readily reconfigured to give 0 to +10, +/- 10 or +/- 2.5 volt output. A different output amplifier might be necessary in order to implement the 10 volt versions as the 12 volt supplies do not usually have sufficient drive to make a bipolar transistor output stage go to 10 volts.) The circuitry is based on that in the Analog Devices data sheet for the AD567, but the 6821 allows the device to be wired as if the computer had a 12 line data bus with a 2 line control bus. The diagram for the converter and the 5.000 volt reference is given on Figure 1.

Through use of the 6821 PIA, the computer may be made to appear as if it had a 12 line data bus. The 6821 is set up to simultaneously output a 12 bit word to the AD567 with the 8 LSB's stored in the side A registers and the 4 MSB's stored in the side B ones. When the MSB's are read into the 6821, bit 7 must also be forced to 0 as it is used to control the NOT(WRITE) input of the D/A. The CB2 output of the 6821 is programmed to momentarily go low on the next E clock transition after the MSB's have been strobed into the AD567. The chip latches the data and then makes the conversion which may take 500 ns during which time the 6821 may be readaddressed. The procedure in the sample software should be followed in order to properly set up the 6821 and to strobe the data into the AD567.

To calibrate the AD567, first send \$0000 to the converter and adjust trimmer R1 to give exactly -5.000 volts. Then send \$0FFF to the chip and adjust R2 to give +4.9976 volts. Recheck both adjustments and the calibration is complete.

If you want to have some fun comparing the speed of assembler, PL9 and TSC BASIC programs, then try the following ones which generate a sawtooth. They also allow you to see if the hardware is functioning properly. Be prepared, however, the assembler and PL9 programs will need an oscilloscope to monitor the output giving values of about 43 and 75 msec respectively, whereas the BASIC program will require a strip chart recorder or a very slow sweep on the scope as it takes 13.5 seconds!

```
*
* SAWTOOTH WAVEFORM GENERATOR PROGRAM FOR AD567K ON
A
* THOMAS INSTRUMENTATION SP-1 BOARD
* BOARD BASE ADDRESS AT $E100 AS PER GIMIX FLEX
* PIA 11 ON BOARD USED FOR 0/A
*****
* by J. A. McDaniel
* University of Maine at Farmington
* 39 High Street
* Farmington, Maine 04938
* August 15, 1984
*****
```

```

START EQU $0000
LOA #$0
STA SE111  BASE ADDRESS OF SIDE A CONTROL REGISTER
*          PREPARE TO ADDRESS DATA DIRECTION A
* STA SE113  BASE ADDRESS OF SIDE B CONTROL REGISTER
*          PREPARE TO ADDRESS DATA DIRECTION B
LOA #$FF
STA SE110  SIDE A DATA DIRECTION, SET FOR OUTPUT
STA SE112  SIDE B DATA DIRECTION, SET FOR OUTPUT
LOA #$04
STA SE111  SELECT SIDE A OUTPUT REGISTER, NO
INTERRUPTS
LOA #$2C
STA SE113  SELECT SIDE B OUTPUT REGISTER, AND
SET CB2 AS OUTPUT NO INTERRUPTS
CLRA
CLRB
AGAIN LDD #$0000
UP ADDD #$0001
STB SE110  LOAD LOW ORDER BITS
STA SE112  LOAD HIGH ORDER BITS AND FORCE
NOT(WRITE) LOW
OPO #$0FFF
BNE UP
BRA AGAIN
END START
```

```
/* D/A CONVERTER TEST: SEPT 14, 1984  GIVES SAWTOOTH */
/* THOMAS INSTRUMENTATION SP-1 BOARD ADDRESSED AT $E100
AS PER
GIMIX FLEX, D/A USES PIA 11 AT SE110 TO SE113 */
```

```
/* BY J. A. MCDANIEL
UNIVERSITY OF MAINE AT FARMINGTON
39 HIGH STREET
FARMINGTON, MAINE 04938 */
```

```
ORIGIN = $0000
GLOBAL BYTE LB1T, HB1T;
STACK *;
```

```
AT SE110: BYTE DOL(0), DATAL, CONL;
AT SE112: BYTE DDH(0), DATAH, CONH;
```

```
PROCEDURE PIA SETUP:
CONL = $00; /* PREPARE TO ADDRESS DATA DIR */
CONH = $00;
DDI = $FF; /* SET FOR OUTPUT */
DDH = $FF;
CONL = $04; /* SELECT SIDE A AS OUTPUT NO INTERRUPTS */
*/
CONH = $2C; /* SELECT SIDE B AS OUT WITH CB22 AD
OUTPUT */
ENDPROC;
```

```
PROCEDURE UP;
LB1T = $00;
HB1T = $00;
REPEAT
    DATAL = LB1T;
    DATAH = HB1T;
    LB1T = LB1T + $01;
UNTIL LB1T = $FF;
```

```

DATAL = LBIT;
DATMH = HBIT;
HBIT = HBIT + $01;
LBIT = $00;
UNTIL HBIT = $10;
ENDPROC;

```

```

PROCEDURE RUN;
PIA SETUP;
REPEAT
    UP;
    FOREVER;

```

```

10 REM D/A CONV TEST PROGRAM 9/14/84 GIVES SAWTOOTH
OUT
20 REM FILE NAME DA-SAW3.BAS
22 REM USES THOMAS INSTRUMENTATION SP-1 BOARD AT $E100
24 REM BY J. A. MC DANIEL, UNIV. MAINE AT FARMINGTON
30 REM USES PIA 11 ADDRESSEO AT DATA A $E110; CONTROL A
$E111
40 REM DATA B $E112; CONTROL B $E113
45 POKE HEX("E111"),0:POKE HEX("E113"),0: REM PREPARE TO
SET
50 REM DATA DIRECTION REGISTERS
60 POKE HEX("E110"),HEX("F"):POKE HEX("E112"),HEX("FF")
65 REM SET BOTH SIDES AS OUTPUT
70 POKE HEX("E111"),4: REM SET SIDE A AS OUTPUT NO
INTERRUPTS
90 POKE HEX("E113"),HEX("2C"): REM ENABLE C822 AS OUTPUT
TO
100 REM CONTROL 0/A NOT SELECT- MSB OF B USED AS NOT
WRITE
130 FOR N$ = 0 TO 15
140 FOR I$ = 0 TO 255
150 POKE HEX("E110"),1%
160 POKE HEX("E112"),N$
180 NEXT I$
190 NEXT N$
200 GOTO 130
220 END

```

#### A/D CONVERTER

The design for the A/D converter was taken from the literature of National Semiconductor with the analog switching, +/- 10 volt amplifiers and interfacing to the 6821 designed by Ron. The reference and 0 to 20 millivolt amplifiers are of my design. The cost of the amplifiers may be significantly reduced by using cheaper amplifiers than the AD647KH specified, but I prefer the Analog Devices ones, for they always perform as expected and often require no trimming. The reference voltage for the A/D, the switches and the channel selectors is derived from the excellent 10.00 reference of the A0567K0. Figure 2 gives the diagram for the 0 to 20 millivolt amplifier; Figure 3 gives the diagram for the A/D converter and the +/- 10 volt amplifiers.

The basic A/D converter is designed for a 0 to 5 volt input, and thus has a resolution of 1.221 millivolts. The analog amplifier-scalars to scale a +/- 10 volt, and 0 to 20 or +/- 10 millivolts (easily changed to other values) to the requisite 0 to 5 volt range are described. BASIC programs are described which: (1) makes an input of +/- 10 amplifier yield +2047 and -2048 respectively (RWA), and (2) makes an input of 0 or 20 millivolts yield 0000 or 4095 respectively.

The basic calibration procedure is to apply a voltage of 5.0000 to pins 18 and 19 of the ADC1210HCD and then adjust the 5 volt reference supply until the LSB flickers equally between 0 and 1 with all other bits being off. Then a voltage of 0.00061 volts is applied to pins 18 and 19 and the zero adjust (R1 on Figure 3) is adjusted until the LSB flickers between 0 and 1 with all other bits being on. (The converter is operating in the complementary binary output mode, and a short PL9 program is given below which outputs a hex word equal to the converter output, the inverse of the hex word, and the actual voltage input on a 0 to 5 basis.)

After the A/D has been calibrated, the sample and hold should be connected and zeroed. With the AD647K no zeroing was really necessary as the amplifiers can be offset slightly to compensate it. (If it is necessary to zero the sample and hold, a zeroing circuit like the one on the +/- 10 volt amplifier may be added. But notice that the sample and hold then has a gain of 1.02 as does the +/- 10 volt amplifier.)

The +/- 10 volt amplifiers can be zeroed by inputting 0.0000 and setting the output to 0 using Ron's program. The actual output will be 2.500 volts at the amplifier.

The 0 to 20 millivolt amplifiers may be set up as follows: with 0.0000 volts in, adjust RB until the output of the first amplifier is 0.0000 as indicated by the voltmeter. (If exactly 0 can not be attained, then set the output so that it is slightly negative.) Then adjust R9 until the output of the second amplifier is 0.0000. If the output of the first amplifier is not 0, then that is permissible, but the output of the final amplifier should be set to -10 times the output attained by the first by adjustment of R9. After these adjustments are finished, R10 should be connected to the -15 volt supply and adjusted until the output is 2.500 volts. The amplifier is calibrated for a 0 to 20 or +/- 10 millivolt input depending on whether R10 is connected or not. If the amplifier is only needed for + voltages, then omit R10 and the associated resistors.

```

10 REM DRIVER FOR 12 BIT A/D CONVERTER IN BASIC
20 REM
25 REM ** BY R. W. ANDERSON **
30 REM ** ANN ARBOR, MICH **
35 REM ****
40 REM PORT ADDRESS IS $E100 ON SAMPLE PROGRAM,
50 REM BUT MAY BE CHANGED TO ANY CONVENIENT
60 REM I/O ADDRESS-- CV IS Variable FOR CHANNEL
65 REM NUMBER OF A/D
70 GOTO 310: REM SKIP SUBROUTINES
80 REM
90 REM FIRST THE PORT INITIALIZE SECTION
100 POKE HEX("E110"),0 : POKE HEX("E11F"),0
110 POKE HEX("E11C"),0 : POKE HEX("E11E"),HEX ("F0")
120 POKE HEX("E11D"),4 : POKE HEX("E11F"),HEX ("3C")
130 RETURN : REM THIS IS A SUBROUTINE
140 REM
150 REM THE CONVERT SUBROUTINE
160 CV$ = CV$ * 16 : REM LEFT SHIFT FOUR
PLACES
170 POKE HEX ("E11E"),CV$ : REM SET UP CHANNEL
NUMBER
180 OAS = PEEK(HEX("E11C")) : REM CLEARS CONVERSION
185 REM COMPLETE FLAG
190 POKE HEX ("E11F"),HEX("34") : REM TURN ON CONVERT
PULSE
200 POKE HEX ("E11F"),HEX("3C") : REM TURN CONVERT PULSE
OFF
210 ST$ = PEEK(HEX("E110")) : REM READ STATUS
220 IF ST$ < 128 THEN 210 : REM WAIT UNTIL HI BIT IS
ON
230 OAS=(PEEK(HEX("E11E")) AND 15) * 256 +
PEEK(HEX("E11C"))
240 IF OAS > 2047 THEN DAS = DAS - 4096
245 REM ADJUST FOR NEGATIVE VALUES
250 RETURN
260 REM NOW YOU MAY ADD THE VALUE OBTAINED TO A
270 REM REAL VARIABLE FOR SUMMING AND AVERAGING
280 REM PROGRAMS.
290 REM
300 REM TEST PROGRAM
310 GOSUB 100
340 CV$ = 0 : REM SET CHANNEL TO ZERO
350 GOSUB 160
360 PRINT OAS
365 GOTO 340
370 END

```

```

10 REM DRIVER FOR 12 BIT A/D CONVERTER IN BASIC
16 REM D-5 V INPUT 0=0000, 5=4095
20 REM PORT ADDRESS IS $E100 ON SAMPLE PROGRAM, BUT
30 REM MAY BE CHANGED TO ANY CONVENIENT I/O ADDRESS
40 REM CV$ IS Variable FOR CHANNEL NUMBER OF A/D
100 REM TEST PROGRAM ** BY J. A. MC DANIEL **
110 GOSUB 210: REM INITIALIZE PORT
120 INPUT "THE CHANNEL NUMBER IS ", CV$
125 CV$ = CV$ * 16: REM CALCULATE CHANNEL NUMBER
130 GOSUB 320: REM PERFORM CONVERSION
140 PRINT DAS
150 GOTO 130
160 END
170 REM

```

#### BYTE MS BITS:

```

AT $E11C: BYTE PIA ADD(0), PIA AD, PIA AC;
AT $E11E: BYTE PIA BD(0), PIA BD, PIA BC;
AT $E110: BYTE PIADOL(0), PIADATL, PIACNL;
AT $E112: BYTE PIADOH(0), PIADATAH, PIACNH;

```

```

CONSTANT TRUE=-1, FALSE=0, MEM=$0000;
INCLUDE 0.10SUBS;
INCLUDE 0.HEXIO.LIB;
INCLUDE REALCON;

```

```

PROCEDURE ESCAPE: BYTE FLAG;
  IF GETKEY = $1B /*ESCAPE KEY ON KEYBOARD */
    THEN FLAG = 1;
    ELSE FLAG = 0;
  ENDPROC FLAG;

PROCEDURE SETUP AD; /* PROCEDURE TO SET UP A/D PIA */
  PIA AC = $00; /* PREPARE TO ADDRESS */
  PIA BC = $00; /* DATA DIRECTION REGISTERS */
  PIA AD = $00; /* SET SIDE A AS INPUT */
  PIA BD = $F0; /* SET 1/2 B AS INPUT 1/2 AS OUTPUT */
  PIA AC = $04;
  PIA BC = $3C;
ENDPROC;

PROCEDURE AD CONVERT(BYTE CHAN NU);
  — BYTE COM B, TEMP—TEST B:
  INTEGER INT B, INT A, DUMMY;
  CHAN NU = SHIFT(CHAN NU, 4); /* LEFT SHIFT FOUR
PLACES */
  PIA BD = CHAN NU; /* SET UP CHANNEL NUMBER OF PIA */
  DATA A = PIA AD; /* CLEAR CONVERSION COMPLETE FLAG */
  PIA BC = $34; /* TURN ON AD CONVERT PULSE */
  PIA BC = $3C; /* TURN OFF AD CONVERT PULSE */

REPEAT
  IF ESCAPE <> 0 THEN JUMP $0000;
  TEST B = PIA AC;
  UNTIL TEST B >= $80;

  DATA A = PIA AD
  DATA B = PIA BD AND SOF; /* REMOVE CHANNEL NU AND GET
4 MSBS */
  COM B = NOT(DATA B) AND $0B; /*COMPLEMENT
UNCOMPLEMENTED BIT (MSB) */
  TEMP = DATA B AND $07; /* GET COMPLEMENTED 3 BITS OF
MSB'S */
  MS BITS = COM B OR TEMP; /* PUT 4 MSB'S TOGETHER */
  INT B = SHIFT(INTEGER(MS BITS), B);
  INT A = INTEGER(DATA A);
  AD OUT = INT B OR INT A;
ENDPROC;

PROCEDURE PRNUM(REAL NUM):BYTE BUF(20);
  PRINT(ASCII(NUM), BUF);
ENDPROC;

PROCEDURE STALL (REAL INDEX2): REAL COUNT;
REPEAT
  COUNT = COUNT + 1;
  IF ESCAPE THEN JUMP $0000;
  UNTIL COUNT = INDEX2;
ENDPROC;

PROCEDURE TEST CONVERT(BYTE CHAN NU): BYTE INDEX;
  REAL A1, INDEX2, AVG, A AVG;
  SETUP AD;
  CRLF;
  PRINT("INPUT THE CHANNEL NUMBER "); CRLF;
  CHAN NU = GET HEX BYTE;
  CRLF;
  PU HEX BYTE(CHAN NU);
  CRLF;
  REPEAT
    INDEX = 0;
    A AVG = 0.0;
    REPEAT
      AD CONVERT(CHAN NU);
      PUT HEX ADDRESS(AD OUT);
      SPACE(9);
      PUT HEX ADDRESS(NOT(AD OUT) AND $0FFF);
      A1 = FLOAT(AD OUT);
      A1 = A1 / 4096^5;
      SPACE(5);
      PRNUM(A1);
      CRLF;
      A AVG = A AVG + A1;
      INDEX = INDEX + $01;
    UNTIL INDEX = $15;
    AVG = A AVG/21;
    SPACE(10); PRNUM(AVG);
    CRLF;
    INDEX2 = 3000;
    STALL(INDEX2);
    CRLF;
  FOREVER;

```

#### CAUTION:

Both the +/- 10 volt amplifier and particularly the 0 to 20 millivolt amplifier can under overload conditions place +/- 10 to 12 volts on the multiplexer input. Under these conditions no damage will result; however, the switches no longer properly work and channels which are supposedly off will feed into the one which is on, causing

erroneous results.

#### CONSTRUCTION HINTS:

A few construction hints might be helpful at this point: all the resistors should be 1% metal film, and the feedback components in the 0-20 millivolt and the +/- 10 volt amplifiers should be matched to 0.1% or better. The 0.1 ufd integrating capacitor should be polystyrene or polyester definitely not ceramic. The bypass capacitors should be ceramic and miniature tantalum for the 0.1 and 10 ufd., respectively.

I found it very helpful to separate the analog grounds (as much as possible) from the SP-1 board. These grounds are symbolized by the triangles on the diagrams and were all tied to a piece of 1/8 inch shield braid run along the top of the board by the use of # 22 or #20 wire with most of them having their own wire. The shield braid was covered by plastic insulation and run to the interconnection board where the three main filter capacitors of my Gilmix tie together. The braid was attached by a solder lug placed under a capacitor ground screw. The digital grounds were all run together on the SP1 board.

It is also helpful to keep the clock divider somewhat removed from the analog circuitry particularly the trimmer potentiometers in order to avoid injecting digital noise into the reference lines. It might also be helpful to shield the analog reference, zeroing, and analog input lines to the A/D as I have experienced digital feed into some of them. The coupled voltages are small but enough to affect 12 bit accuracy and are practically impossible to pinpoint by a scope. Under unfavorable conditions merely connecting the ground lead to the scope can make the error worse by a factor of 10 from injected hum. If you use a scope to detect millivolt fluctuations you will want to float the scope from the AC power line and run a ground to the end of the shield braid that connects to the filter capacitors in the computer. (The scope probe doesn't make a good enough ground for my scope and a wire should be directly connected.)

Finally, to help conserve space on the board, the channel input resistors can be placed so that the leads are only 0.1 inches apart if every other one is first inserted 0.2 inches apart and then the remainder inserted so that they rest on top of the first row. I would also recommend that you place the A/D converter on the right hand side of the SP-1 board since there is a larger square area on the right as opposed to the long thin more or less rectangular area on the left. This would mean that the A/D PIA should be IC 11 on the SP-1 board.

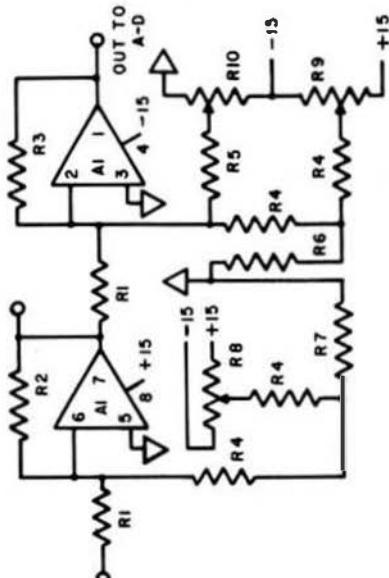


FIGURE 2. 0-20 mV AMPLIFIER  
 R1 10K, R2 200K, R3 100K, R4 634K, R5 511K, R6 2K,  
 R7 1.5K, R8, R9, and R10 20K 15 turn Trimmers  
 AI Analog Devices AD 647KH

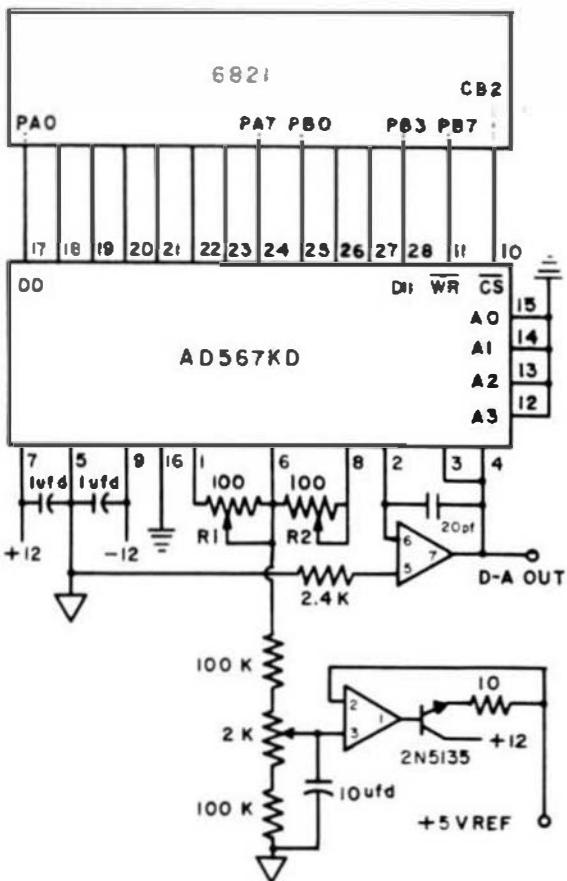


FIGURE 1. DIGITAL TO ANALOG CONVERTER  
OP AMP Analog Devices AD647KH  
RI, R2 100 ohm 15 Turn Trimmers (SEE TEXT)

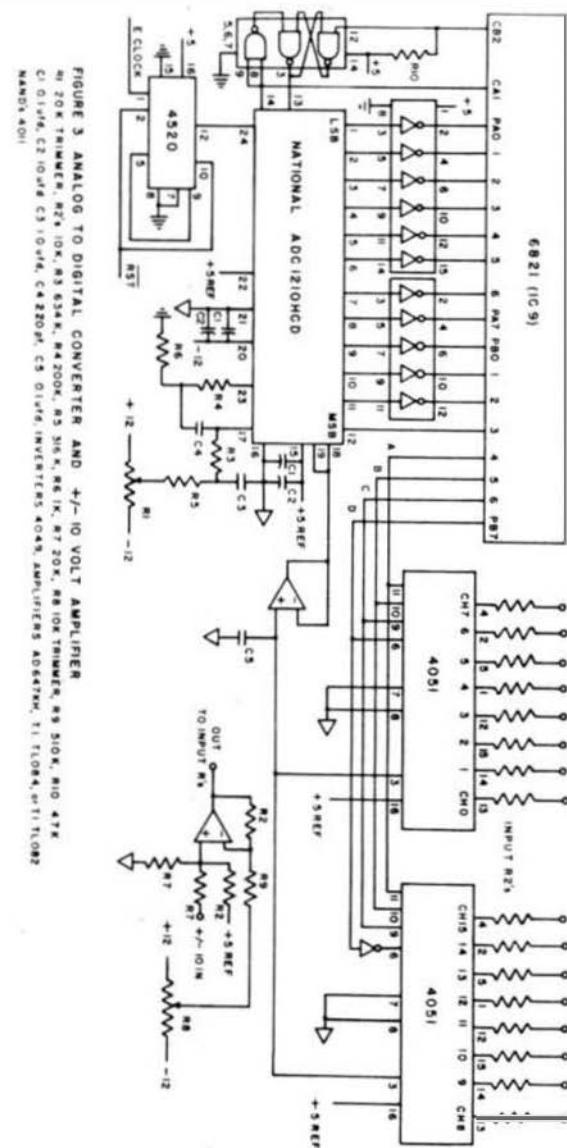


FIGURE 3. ANALOG TO DIGITAL CONVERTER AND +/- 10 VOLT AMPLIFIER  
H1 20K TRIMMER, R1 10K, R2 634K, R3 200K, R4 50K, R5 10K, R6 1K, R7 20K, R8 10K TRIMMER, R9 510K, R10 47K  
C1 0.1uF, C2 1uF, C3 1uF, C4 22pF, C5 0.1uF, INVERTERS 4049HN, 74LUD44, 74TTL082  
NAND1 40101

## BIT BUCKET

**MICRONICS**  
RESEARCH CORP.

Microcomputers, Hardware and Software  
GLIMIX - Sales, Service and Support

21 December 1984

68 Micro Journal,  
5900 Cassandra Smith,  
Eison, TN 37343

Dear Don,  
Some time ago - I've forgotten in which issue of 68 Micro - one of your readers submitted an enhancement to Lao Taylor's COPY utility to output Carriage-Returns to his printer. He also suggested it would be nice if it could handle missing file numbers when the 'F' option was in use so that if, say, the range 2-15 were being copied and File #7 were missing from the sequence, the COPY procedure would not be aborted. Well, I accepted the challenge and am enclosing the necessary patch to implement his suggestion. Thanks for the idea!

1. Locate the label SRPPFIL in the assembly-listing. Six lines above this, change BNI BADNU to BNI MOFILE.
  2. Append the following code to SRPPFIL, immediately after the instruction BRA HALOOP :
- ```
MOFILE INC LORANG+1 Adjust File #  
BNI MOFILE  
INC LORANG  
MOFILE LDX $MOFILE Missing File message
```

```
JSR PSTRNG  
CLRB  
LDX $LORANG Display File #  
JSR OUTDBC  
LDX $SKPDIR+22 Continue?  
JSR ASRMSG  
BEQ HALOOP Yes  
CLR PNTDRV No  
JMP EXIT2
```

3. Insert the following in the messages section, immediately above WAITMS :  
MOFILE FCC "Non-existent File #"  
PCB 4

This letter has set me to thinking that if it were not for seeing our friend's suggestion, this idea might not have occurred to me. Perhaps there are other readers out there with thoughts of "Wouldn't it be nice if a certain utility had such and such an option, or even if there were a utility to do whatever." but perhaps they don't have the expertise to carry out their ideas. I hereby invite you folks out there to contact me with your ideas, and maybe between us we can come up with something we can all use to our advantage.

Over the next few months I hope to submit upgraded versions of other utilities I've been working on. Have a merry Xmas and a VERY happy New Year.

1333 LYNN AVENUE.  
ABBOTSFORD,  
BRITISH COLUMBIA,  
CANADA V3E 1E2

Sincerely,  
*Bj Jones*  
R. Jones  
President

**MOTOROLA INC.**  
MOS Integrated Circuits Group  
7500 E. BLUESTEIN, BLDG.  
AUSTIN, TEXAS 78721  
For further information EC-102

# PRESS INFORMATION

EDITORIAL CONTACT: Ed Prestwood  
(602) 994-6959

Editorial Contact: Val Bauer  
512 923-6804  
Reader Contact: Beverly Sill  
201 592-2348

MOTOROLA PUBLISHES NEW 16/32-BIT MICROCOMPUTER BOARD DATA BOOK

## INTRODUCTION TO INTEGRATED CIRCUIT LAYOUT

By Brian Spinks

Austin, Texas, December 1, 1984 — A definitive college-level textbook on integrated circuit layout is finally available. Introduction to Integrated Circuit Layout, by Brian Spinks, offers the basic theory and method of integrated circuit design to engineering and drafting students as well as other technologists, as a preparation for integrated circuit (IC) mask design.

Introduction to Integrated Circuit Layout is intended to provide the student with a working vocabulary of the trade, the basic theory necessary for the layout of metal-oxide-semiconductor integrated circuits (MOS ICs), and a method for translating a logic diagram to a schematic design for use in designing an integrated circuit. The reader is also shown techniques for the design of a composite drawing of masks for use in the fabrication of ICs, and the requirements for noncircuit elements of ICs, such as logos, alignment keys, and etch marks.

-more-

The material for Introduction to Integrated Circuit Layout was developed from a second-year college-level drafting course. In 1979, Brian Spinks began compiling notes for a technical course designed to alleviate the necessity for in-house training in integrated circuit design. The lack of available teaching materials led to the eventual publication of Introduction to Integrated Circuit Layout. The book is addressed to students possessing some familiarity with printed-circuit layout and design, electronic schematic drafting, and basic electronics.

Introduction to Integrated Circuit Layout contains a very useful glossary of the trade vocabulary, as well as easy-to-read drawings, diagrams, examples, and student exercises where appropriate. The material is compiled solely from sources provided by present and former members of the Microprocessor Products Division Design Staff, Motorola, Inc., Austin, Texas.

Brian Spinks received his BA in Mathematics in 1962 from the University of Texas at Austin, where he also worked at the Defense Research Laboratory. In 1977, he was awarded a BSEE from the University of Houston. Mr. Spinks has worked at Lockheed Electronics on a NASA Program, at Texas Instruments, and at American Micro Systems, Inc., where he worked for two years before joining the Applications Engineering Staff at Motorola in 1975.

Introduction to Integrated Circuit Layout, by Brian Spinks, 1983, is \$19.95 in soft cover, \$24.95 in hard cover. For further information regarding Introduction to Integrated Circuit Layout, please contact Beverly Sill, College Publicity, Prentice-Hall, Inc., Englewood Cliffs, New Jersey, 07632, or call collect, (201) 592-2348.

FOR IMMEDIATE RELEASE ... A new data book covering the broad range of Motorola 16/32 Bit Microcomputer Board-Level products is now available.

Chapters cover VMEmodules, VERSImodules, I/Omodules, operating systems, development systems, system bus technical summaries, and customer support. Technical specifications, photos, charts, and graphs are used to describe a variety of board, system, and software products. The board-level products are based on the MC68000 Microprocessor Family, and include a onboard microcomputer utilizing the MC68020—Motorola's new, full 32-bit MPU.

A COPY may be ordered by requesting DL127 from Motorola Literature Distribution Center, 616 W. 24th Street, Tempe, Arizona 85282, (602) 994-6561. Price is \$2.05.

\*\*\*

Note to Editors: Please do not publish without including price information.



**MOTOROLA**  
Semiconductor Products Inc.  
P.O. BOX 20912 PHOENIX, ARIZONA 85026

Innovative systems  
through silicon.

## News Release

Plexus  
Computers,  
Inc.  
3833 North First Street  
San Jose, CA 95134  
408 • 943 9433  
TWX/TELEX 910 338 2223

Contact: Jeff Stover  
Marketing Communications  
408 • 943 2247

FOR RELEASE ON OR AFTER  
14 NOVEMBER 1984

### PLEXUS ENTERS 1-8 USER MARKET WITH MC68010-BASED \$10,000 UNIX SUPERMICRO COMPUTER

SAN JOSE, CA., NOV. 14, 1984 — Plexus Computers, Inc. today announced its new entry into the 1-8 user marketplace with a high performance MC68010-based supermicro computer specifically designed to run the UNIX® operating system.

Designated the Plexus P/15, the new 32-bit supermicro offers full compatibility with the rest of the Plexus UNIX-based line of products and is targeted at the growing VAR/OEM market, as well as toward large end-users.

As with the other Plexus computers, the P/15 features a multiprocessor architecture and utilizes two MC68010 microprocessors, a memory capacity of up to 2 Mbytes and eight full duplex serial ports for terminals or other peripherals.

"The P/15 is aimed directly at the small user who needs not only 8 user capacity, but high performance as well," said Edward J. Hayes, Plexus' VP of Marketing.

"There is a very large segment of the OEM/VAR market that has indicated a need for the P/15," said Hayes. "In most cases, these VAR's have been selling a system allowing from one to four users and they have been running out of steam...out of power as their application needs grew. The P/15, with its superior multiprocessor architecture utilizing two of the more powerful MC68010 microprocessors, is offered as a solution to this VAR dilemma."

Hayes pointed out that the P/15 is an entirely new product, designed by Plexus engineers especially for this market.

"As the Plexus product line continues to expand, new products like the P/15 will appear; not as a replacement for existing systems, but as viable new alternatives for VAR's and end-users who want UNIX® and want powerful, multi-tasking supermicros," Hayes said.

The P/15 is packaged to fit in a very limited space often found in the typical business office. With its attractive low profile design, a height of under 25 inches and a weight of under 75 pounds, it easily fits along side other office equipment and furniture. The P/15 is totally self-contained, allowing for up to 54 Mbytes of disk storage in 2 Winchester-type disks, plus a single 5.25 inch double sided, double density floppy disk and uses standard 115VAC power.

P/15 users should expect significantly faster response time in the multiuser environment than they are accustomed to seeing in other similarly priced machines, Hayes pointed out. He indicated that these benefits are a factor of both the multiprocessor design and the separate 32-bit processors for I/O functions, such as data communications, disk subsystem control and functions such as job processing and operating system execution.

The I/O processor is a 32-bit device controlling an SCSI subsystem interface offering full error-correction facilities as well as full SCSI interface functions serving the hard and floppy disks. In addition, the I/O processor handles all character I/O to and from the eight serial ports removing this overhead from the job processor.

The Plexus P/15 CPU job processor, using the 32-bit MC68010 with a 10MHz clock, operates with no wait states, through a shared high-speed map, with 8 Mbytes of address space. It supports the IEEE proposed standards for floating point arithmetic.

Utilizing current 256-bit RAM devices, the P/15 memory is available with up to 1 million 16-bit words.

The P/15 is available within 120 days A.R.O. and is priced at \$10,950 US, quantity one, with .5 Mbyte of RAM and 12 Mbytes of Winchester disk storage, plus a 1 Mbyte floppy disk drive.

Plexus Computers, Inc. manufactures a fully compatible line of high performance 32-bit supermicrocomputers designed for the VAR/OEM and large end-user, utilizing the UNIX® operating system and a unique multiprocessor architecture which provides users with exceptional power and performance. Plexus markets its products through sales offices in the U.S. and via its distributors and subsidiaries throughout the world.

For additional information, please contact Jeff Stives, Plexus' Marketing Communications Manager, 3833 North First Street, San Jose, CA. 95134, (408)943-2247.

- 30 -

1984

UNIX is a trademark of Bell Laboratories.



'88 MICRO JOURNAL  
ATTN: DON WILLIAMS  
5900 CASSANDRA SMITH  
P O BOX 849  
MURKIN, TN 37363  
UNITED STATES OF AMERICA

Your Ref:

Our Ref: WCD/HB

Date: 14/12/84

#### SCREDITOR III WORD PROCESSOR

Dear Don,

Windrush is proud to announce the acquisition of the world-wide rights to the PLX and DS-9 versions of SCREDITOR III from Alford and Associates.

Windrush will continue to market these versions at \$175.00 and provide upgrade disks to existing users for \$25.00 and upgrade disks and manuals to existing users for \$45.00.

To obtain an upgrade, the ORIGINAL disk must be returned with the payment (cheque/MO/VISA/ACCESS).

The current version number of SCREDITOR III is 1.200.

Please note that the "Tutorial Cassette" referred to in some of Alford and Associates advertisements will not be supplied by Windrush.

Dealer and license enquiries are invited.

Regards,

WILLIAM C. DICKINSON  
DIRECTOR  
WINDRUSH MICRO SYSTEMS LIMITED



'88 MICRO JOURNAL  
ATTN: DON WILLIAMS  
5900 CASSANDRA SMITH  
P O BOX 849  
MURKIN, TN 37363  
UNITED STATES OF AMERICA

Your Ref:

Our Ref: WCD/HB

Date: 13/12/84

#### CHANGE IN UPGRADE POLICY

Dear Don,

Well we've held back doing this as long as we could but with the steady increase in international postal charges for the DS-9 over two years we can refrain no longer.

Effective January 1, 1985 our upgrade charges will be as follows:

- |                                                                                       |         |
|---------------------------------------------------------------------------------------|---------|
| 1. Upgrade disk <u>only</u> for any Windrush product                                  | \$25.00 |
| 2. Upgrade disk and manual for any Windrush product other than PL/9 or SCREDITOR III. | \$35.00 |
| 3. Upgrade disk and manual for PL/9 or SCREDITOR III                                  | \$45.00 |

The above prices include air mail postage.

Current version numbers are:

|                     |        |
|---------------------|--------|
| RACE .....          | 2.61   |
| XFACE .....         | 2.31   |
| ASMOS .....         | 2.30   |
| D-BUG .....         | 9.6.80 |
| ACCOSS 'C' .....    | 23.2.8 |
| PL/9 .....          | 4.23   |
| SCREDITOR III ..... | 1.200  |

To take advantage of this service the user must:

1. Return the ORIGINAL Master Disk (not a copy).
2. Enclose a cheque, Money Order or credit card authorisation.

The disk should be sent fully insured for the full price of the product as per our advertisement. We will not be held responsible for disks that fail to reach us. The customs declaration (green sticker) should read "Goods Of UK Origin".

Any application for an upgrade that is not accompanied by the original master disk will be returned without action.

We would appreciate it if you would give this letter the widest possible dissemination.

Regards,

  
Bill Dickinson  
DIRECTOR  
WINDRUSH MICRO SYSTEMS LIMITED

## MICRONICS

RESEARCH CORP.

Microcomputer Hardware & Software  
GIMIX® Sales Service & Support

16 December 1984

Mr Don Williams,  
68 Micro Journal,  
5900 Cassandra Smith,  
Hixson, TN 37343

Dear Don,  
Re the program PC.CMD and the amendments recently submitted by the author, Don Korte, he omitted one very important insertion of a Line-Feed to prevent overprinting of lines. This requires the addition of

LDA #5A  
JSR POUT

immediately after the outputting of NULINE in the section of code labelled PPG2.

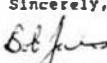
I would suggest a further enhancement, which would allow the use of multiple spaces in the optional title. NXTCH unfortunately compresses multiple spaces to a single space. The listing should be changed as follows:

1. Add to equates BUFPNT EQU SCC14  
2. Change the code at P121 to  
LDB #79 Max length  
LDY BUFPNT Point to Line-Buffer  
P121 LDA 0,Y+ Get char  
CMPA #", End-of-Title?  
BEQ P122 Yes  
STA 0,X+ No. Store char  
DEC B  
BNE P121 Keep loading  
P122 STY BUFPNT Update Buffer-Pointer  
P13 LDA # ..... etc

It might also be worth mentioning that for those whose Printer requires a multiple-code sequence to switch from NORMAL to NARROW printing, a quick fix is to set both NORMAL and NARROW to 00 at the beginning of the program. Then under the label HEADER (at the end of the program) replace the word NORMAL with the corresponding code sequence, e.g. \$1B, \$8, \$1, or whatever, and similarly with NARROW under the label HEADR1.

In closing, I would like to reach out to owners of the GIMIX 80x24 Video-Board with a view to exchanging programs. I have lots of good stuff specially written to take advantage of this board's graphics and sound features.

3530 LYNN AVENUE  
ABBOTSFORD  
BRITISH COLUMBIA  
CANADA V2S 1E7

Sincerely,  
  
R. Jones  
President

The BASIC programs DATA16, DATA8, and DATA16 work with an AD-16 JPC analog to digital conversion board. USER subroutines are called by the BASIC programs to insure that as soon as the conversion has been completed the next step in the data taking process can be initiated. The machine language routines disable the IRQ and allow a complete handshake between the ADC0817 A/D chip and the 6821 PIA by checking for the status of the eighth bit of the 6821's status register. The manner of handshaking provided in the machine language program can be used to form the nucleus of a more complex program.

Maximum sampling rate for one channel is 9000 samples per second for a 1 Megahertz clock 6800 based system where the 7474 chip has been removed from the AD-16 board and pins 3 and 5 of the 7474 socket are

tied together in order to clock the ADC0817 at 1 Megahertz. A 1000 Hertz 5.0 volt peak to peak sine wave with a DC offset of +2.5 volts was found to be reproducible at 9000 samples per second.

It is worth noting that in all the USER machine language routines, where more than one channel is selected, there is a delay of about 24 clock cycles. This serves to insure that the correct channel is accessed by keeping the ALE and Start of Conversion pins of the ADC0817 high for the duration of the delay. BASIC has been used to display the data and engineering units and other formats in BASIC are possible. The programs provided are essentially test programs to tell the user that his A/D board is functioning or not. DATA16 samples 16 channels in numerical order one channel after another for a total of 16 samples per channel. DATA8 allows the user to select eight channels in any sampling order. Each channel is sampled one channel after another for a total of thirty-two samples per channel. DATA16 allows the user to select any two of sixteen channels with 256 samples being taken for the channel chosen. The gain for all channels is one. The AD-16 is on port 4.

2 Jeffrey M. Craig Apt. 912 - 3001 S. King Dr. Chicago, IL 60616 4/6/82

```
10 REM THE NAME OF THIS PROGRAM IS DATA16
20 POKE HEX("26"),0
30 POKE HEX("27"),0
40 LET S=0
50 EXEC,"GET,USER1.BIN"
60 PRINT "DO YOU WANT HARDCOPY - 'Y' FOR YES OR 'N' FOR NO."
70 INPUT P8
80 IF P8<>"Y" AND P8<"N" THEN GOTO 60
90 PRINT "ENTER THE CHANNEL YOU WANT SAMPLED"
100 INPUT C
110 IF C<1 THEN GOTO 90
120 IF C>16 THEN GOTO 90
130 IF C=1 THEN Y=16
140 IF C=2 THEN Y=17
150 IF C=3 THEN Y=18
160 IF C=4 THEN Y=19
170 IF C=5 THEN Y=20
180 IF C=6 THEN Y=21
190 IF C=7 THEN Y=22
200 IF C=8 THEN Y=23
210 IF C=9 THEN Y=24
220 IF C=10 THEN Y=25
230 IF C=11 THEN Y=26
240 IF C=12 THEN Y=27
250 IF C=13 THEN Y=28
260 IF C=14 THEN Y=29
270 IF C=15 THEN Y=30
280 IF C=16 THEN Y=31
290 POKE HEX("5999"),Y
300 POKE HEX("14"),HEX("60")
310 POKE HEX("25"),HEX("00")
320 LET A=USA(0)
330 IF P8<"Y" THEN OPEN "D,PRINT" AS 0
340 IF P8<"Y" THEN PRINT #0,""
350 FOR I=1 TO 256
360 LET Y=PEEK(HEX("7800") + I)
370 LET Y0=STR0(Y)
380 LET T=LEN(Y)
390 IF P8<"Y" THEN GOTO 420
400 PRINT SPC(5-T);Y;
410 GOTO 460
420 PRINT #0,SPC(5-1);Y;
430 LET S=S+1
440 IF S>8 THEN PRINT #0
450 IF S>8 THEN S=0
460 NEXT I
470 IF P8<"Y" THEN CLOSE 0
480 END
```

```

10 REM THE NAME OF THIS PROGRAM IS DATA2
20 POKE HEI("26"),0
30 POKE HEI("27"),0
40 LET N=0
50 LET S=0
50 EXEC,"GET,USER2.BIN"
70 PRINT "DO YOU WANT HARDCOPY - 'Y' FOR YES OR 'N' FOR NO."
80 INPUT P6
90 IF P6>"Y" AND P6<"N" THEN GOTO 10
100 PRINT "ENTER THE EIGHT CHANNELS YOU WANT SAMPLED - "
110 FOR I=1 TO 8
120 INPUT Z
130 IF Z<1 THEN GOTO 100
140 IF Z>16 THEN GOTO 100
150 IF I=1 THEN Y=16
160 IF I=2 THEN Y=17
170 IF I=3 THEN Y=18
180 IF I=4 THEN Y=19
190 IF I=5 THEN Y=20
200 IF I=6 THEN Y=21
210 IF I=7 THEN Y=22
220 IF I=8 THEN Y=23
230 IF I=9 THEN Y=24
240 IF I=10 THEN Y=25
250 IF I=11 THEN Y=26
260 IF I=12 THEN Y=27
270 IF I=13 THEN Y=28
280 IF I=14 THEN Y=29
290 IF I=15 THEN Y=30
300 IF I=16 THEN Y=31
310 POKE HEI("5FF7")=I,0
320 NEIT I
330 POKE HEI("24"),HEI("60")
340 POKE HEI("25"),HEI("60")
350 LET A=USR(0)
360 IF P6="Y" THEN OPEN "0.PRINT" AS 0
370 IF P6="N" THEN PRINT 10.
380 FOR I=1 TO 256 STEP 8
390 GOSUB 720
400 NEIT I
410 PRINT
420 FOR I=2 TO 256 STEP 8
430 GOSUB 720
440 NEIT I
450 PRINT
460 FOR I=3 TO 256 STEP 8
470 GOSUB 720
480 NEIT I
490 PRINT
500 FOR I=4 TO 256 STEP 8
510 GOSUB 720
520 NEIT I
530 PRINT
540 FOR I=5 TO 256 STEP 8
550 GOSUB 720
560 NEIT I
570 PRINT
580 FOR I=6 TO 256 STEP 8
590 GOSUB 720
600 NEIT I
610 PRINT
620 FOR I=7 TO 256 STEP 8
630 GOSUB 720
640 NEIT I
650 PRINT
660 FOR I=8 TO 256 STEP 8
670 GOSUB 720
680 NEIT I
690 PRINT
700 IF P6="Y" THEN CLOSE 0
710 END
720 LET T=PEEK(HEI("6FFF"))=I
730 LET A=STR$(Y)
740 LET T=LEN(Y)
750 IF P6="Y" THEN GOTO 780
760 PRINT SPC(5-T);T;
770 GOTO 850
780 PRINT 0;SPC(5-T);Y
790 S=S+1
800 LET N=N+1
810 IF S=8 THEN PRINT 0
820 IF N=2 THEN PRINT 0
830 IF S=0 THEN S=0
840 IF N=2 THEN N=0
850 RETURN

10 REM THE NAME OF THIS PROGRAM IS DATA3
20 POKE HEI("16"),0
30 POKE HEI("27"),0
40 LET S=0
50 LET N=0
50 EXEC,"GET,USER3.BIN"
70 PRINT "DO YOU WANT HARDCOPY? - 'Y' FOR YES OR 'N' FOR NO."
80 INPUT P6
90 IF P6>"Y" AND P6<"N" THEN GOTO 10
100 IF P6="Y" THEN OPEN "0.PRINT" AS 0
110 IF P6="N" THEN PRINT 10.
120 FOR I=1 TO 256 STEP 16
130 GOSUB 810
140 NEIT I
150 PRINT
160 FOR I=2 TO 256 STEP 16
170 GOSUB 810
180 NEIT I
190 PRINT
200 FOR I=3 TO 256 STEP 16
210 GOSUB 810
220 NEIT I
230 PRINT
240 FOR I=4 TO 256 STEP 16
250 GOSUB 810
260 NEIT I
270 PRINT
280 FOR I=5 TO 256 STEP 16
290 GOSUB 810
300 NEIT I
310 PRINT
320 FOR I=6 TO 256 STEP 16
330 GOSUB 810
340 NEIT I
350 PRINT
360 FOR I=7 TO 256 STEP 16
370 GOSUB 810
380 NEIT I
390 PRINT
400 FOR I=8 TO 256 STEP 16
410 GOSUB 810
420 NEIT I
430 PRINT
440 FOR I=9 TO 256 STEP 16
450 GOSUB 810
460 NEIT I
470 PRINT
480 FOR I=10 TO 256 STEP 16
490 GOSUB 810
500 NEIT I
510 PRINT
520 FOR I=11 TO 256 STEP 16
530 GOSUB 810
540 NEIT I
550 PRINT
560 FOR I=12 TO 256 STEP 16
570 GOSUB 810
580 NEIT I
590 PRINT
600 FOR I=13 TO 256 STEP 16
610 GOSUB 810
620 NEIT I
630 PRINT
640 FOR I=14 TO 256 STEP 16
650 GOSUB 810
660 NEIT I
670 PRINT
680 FOR I=15 TO 256 STEP 16
690 GOSUB 810
700 NEIT I
710 PRINT
720 GOSUB 810
730 NEIT I
740 PRINT

```

```

750 FOR I=16 TO 236 STEP 16
760 GOSUB 810
770 NEXT I
780 PRINT
790 IF Pg="Y" THEN CLOSE 0
800 END
810 LET Y=PEEK(ME1("6FFF")*1)
820 LET Y$=STR$(Y)
830 LET T=LEN(Y$)
840 IF Pg="Y" THEN GOTO 870
850 PRINT SPC(9-T)!!Y
860 GOTO 810
870 PRINT 0,,SPC(3-T),Y;
880 LET S=S+1
890 LET N=N+1
900 IF S=0 THEN PRINT 00
910 IF N=16 THEN PRINT 00
920 IF S=0 THEN S=0
930 IF N=16 THEN N=0
940 RETURN

```

NAME  
CPT FAB  
DRC 12400

04756 EQU 18018  
574196 EQU 39011  
4223AEG EQU 18012  
C74136 EQU 18013  
044CH EQU 15999

```

LDA A D80
STA A STATRS
LDA A D80
STA A DATREG
LDA A D80
STA A STATRS
LDA A D80
STA A CONTRS
LDA B D8FF
STA A ADDRREG
LDA A D834
STA A CONTRS

```

NAM USER2  
OPT PAG  
ORS 10000

```
DATREC EQU 10010  
STATREC EQU 100011  
ADOREC EQU 10012  
CONTREC EQU 10013  
GAINREC EQU 15FFF  
GAINC07 EQU 15FFE  
GAINC06 EQU 15FFB  
GAINC05 EQU 15FFC  
GAINC04 EQU 15FFB  
GAINC03 EQU 15FFA
```

6A1NC2 EQU 15FF9  
6A1NC1 EQU 15FFB

```

LDA A #10
STA A STATRG
LDA A #80
STA A DATREG
LDA A #96
STA A STATRG
LDA A #10
STA A CONTRG
LDA A #1FF
STA A ADDREG
LDA A #834
STA A CONTRG
LDI $1000
CHAN1 LDA A GAINC1
STA A ADDREG
JSR HANDSK
CHAN2 LDA A GAINC2
STA A ADDREG
JSR HANDSK
CHAN3 LDA A GAINC3
STA A ADDREG
JSR HANDSK
CHAN4 LDA A GAINC4
STA A ADDREG
JSR HANDSK
CHAN5 LDA A GAINC5
STA A ADDREG
JSR HANDSK
CHAN6 LDA A GAINC6
STA A ADDREG
JSR HANDSK
CHAN7 LDA A GAINC7
STA A ADDREG
JSR HANDSK
CHAN8 LDA A GAINC8
STA A ADDREG
JSR HANDSK
CPI #7100
BNE RETURN
RTS
RETURN JMP CHAN1
HANDSK LDA A #83C
STA A CONTRG
LDA A #83
LOOP2 DEC A
CMP A #10
BNE LOOP2
LDA A #834
STA A CONTRG
LOOP3 LDA A STATRG
AND A #1000000000
CMP A #1000000000
BNE LOOP3
LDA A DATREG
STA A #1
INI
RTS
END

```

MAP 115ER3  
OPT PAB  
085 16000

```

DATREG EQU $0010
STATRS EQU $0011
ADDREG EQU $0012
CONTRRS EQU $0013

LDA A $00
STA A STATRS
LDA A $00
STA A DATREG
LDA A $00
STA A STATRS
LDA A $00
STA A CONTRRS

```

```

LDA A #0FF
STA A ADREG
LDA A #034
STA A COMTRG

LDI #02000
LS0P1 CPI #007100
B60 EXIT
LDA B #16
LOOP2 STA B ADDR
JSR NM808K
INC B
CMP B #32
B62 LS0P1
JMP LOOP2
EXIT RTS
HANS3 LDA A #03C
STA A COMTRG
LDA A #03
LS0P3 DEC A
CMP A #00
BNE LS0P3
LDA A #034
STA A COMTRG
LOOP4 LDA A STATRG
AND A #217000000
CMP A #214000000
BNE LOOP4
LDA A SATREG
STA A #1
TNE
RTS

END

```

### FLEX Setime Utility

The following SETIME routine is almost an exact copy of SSR's DOS setime routines, except that it has been modified to operate under FLEX (since FLEX does not provide a routine to set the time for those of you who have the MM58167 on-board clock).

In addition to updating the real time clock, SETIME also modifies the FLEX date register appropriately, so that the DATE command need not be used separately to set the month, day and year. One nice feature added to SETIME is the ability to set the year, and because it is a parameter on the command, it can be put in your startup file. By utilizing the FLEX date register to store the year (as the DATE command does), there is no need to have it hard-coded in the SETIME command. The format of the command is:

SETIME

The program will prompt you to enter the current date and/or time.

SETIME D

This will give you the current time and date

SETIME B3

Change the year in FLEX's date register to "83"

SETIME B3 D

Set the year to "83" and display the date/time while you're at it

The real time clock for my 6809 system is at \$F700 (label CLOCK as defined in the FLEX equates). Change this value appropriate!

For a small fee of \$12.00 (U.S.), I will send you the source to the SETIME and EXTEND commands, as well as the FLEX equates, on a 5" floppy disk. Please specify whether you want it on 40 tracks or 80 tracks. Price includes cost of disk. Make check or money order payable to:

Scott Fraser  
547 Sharron Bay  
Winnipeg, Manitoba, Canada  
R2G 0M8

\* Setime - The SET TIME/DATE Transient allows the user of SSB's Real Time Clock (RTC) to set the time, date and year of the hardware device.

\* It is called as:

\* SETIME (set time and date)

\* SETIME D (display time & date)

\* SETIME YY (set year given by "YY" digits)

\* SETIME YY D (set year and display time & date)

\* NOTE: "YY" should be between 00 and 99

\* This program does not use interrupts to update the time and date string on the display once a second instead uses a software loop to determine a 1 cycle/sec interval

\* This is a slight modification of DOS' SETIME cmd to work under FLEX (Sept, 1982)

C387 ZROTIM EQU RDTIME "read time" rtn  
F80C PDATA EQU SF80C addr of PDATA rtn (SSB MOW)

\* MM58167 register offsets

|      |      |     |   |         |
|------|------|-----|---|---------|
| 0002 | SEC  | EQU | 2 | seconds |
| 0003 | MIN  | EQU | 3 | minutes |
| 0004 | HOUR | EQU | 4 | hours   |

|      |     |     |   |               |
|------|-----|-----|---|---------------|
| 0005 | DOW | EQU | 5 | day of week   |
| 0006 | DOM | EQU | 6 | day of month  |
| 0007 | MON | EQU | 7 | month of year |

|         |    |       |                       |
|---------|----|-------|-----------------------|
| C100    |    | ORG   | UCA                   |
| C100 20 | 23 | BRA   | START                 |
|         |    | C002  | VN R0U 2 version 2    |
| C102    |    | YDATE | RMB 16 holds the date |
| C112    |    | YTME  | RMB 16 holds the time |
| C122    |    | XTEMP | RMB 3                 |

|      |      |      |      |                           |
|------|------|------|------|---------------------------|
| C125 | 108E | F700 | LDY  | UCLOCK                    |
| C129 | BD   | C027 | JSR  | NITCH get a character     |
| C12C | 25   | 34   | BCS  | STARTI branch if nothing  |
| C12E | 81   | 44   | CMPA | #7'D just want date/time? |
| C130 | 27   | 22   | BED  | DISID branch if so        |

\* assume a year has been specified

|      |    |      |      |                             |
|------|----|------|------|-----------------------------|
| C132 | BD | C027 | JSR  | NITCH get 1's digit in year |
| C135 | 25 | 38   | BCS  | STARTI branch if nothing    |
| C137 | 1F | 89   | TFR  | A,B A -> B                  |
| C139 | B6 | CC19 | LDA  | PREVC get 10's digit        |
| C13C | 17 | 02C9 | LBSR | RSCBIN convert to binary    |
| C13F | 25 | 4C   | BCS  | ERRI leave if error         |
| C141 | 87 | CC10 | STA  | SYD+2 save year             |
| C144 | BD | C027 | JSR  | NITCH anything else?        |
| C147 | 61 | 20   | CMPA | RSF this a space?           |
| C149 | 26 | 48   | BNE  | XIT now, then leave         |
| C148 | BD | C027 | JSR  | NITCH go past spaces        |
| C14E | 25 | 43   | BCS  | XIT if nothing, leave       |
| C150 | 81 | 44   | CMPA | #7'D want date/time?        |
| C152 | 26 | 3F   | BNE  | XIT now, then leave         |

|      |    |       |      |                                   |
|------|----|-------|------|-----------------------------------|
| C154 | BD | DISID | EQU  | *                                 |
| C154 | BD | C024  | JSR  | PCRF                              |
| C157 | BD | C024  | JSR  | PCRF                              |
| C15A | 17 | 010F  | LBSR | DISPLAY yes, print date/time once |
| C15D | BD | C024  | JSR  | PCRF                              |
| C160 | 20 | 31    | BRA  | XIT return to FLEX                |

|         |      |             |      |          |                              |  |             |         |      |             |                             |
|---------|------|-------------|------|----------|------------------------------|--|-------------|---------|------|-------------|-----------------------------|
| C162 8E | C2EE | C162 START1 | EDU  | *        |                              |  | C1CC 35     | 02      | PULS | A           | restore EOL char            |
| C165 B8 | CD1E |             | LDX  | #0FLVMSG |                              |  | C1CE B7     | CC02    | STA  | EOL.        |                             |
|         |      |             | JSR  | PSTRING  | Print intro                  |  |             |         |      |             |                             |
| Ct68 17 | 0101 | C168 START2 | EDU  | *        |                              |  | C1D1 20     | 8F      | PBA  | START1      |                             |
|         |      |             | LBSR | DISPLY   | Print time/date              |  |             |         |      |             |                             |
| C16B B8 | C04E | C168 START3 | EDU  | *        |                              |  | C1D3 SETIM1 | EDU     | *    |             |                             |
| C16E 26 | 0A   |             | JSR  | STAT     | check keyboard status        |  | C1D6 BD     | C370    | LDX  | #0FLVMSG    |                             |
|         |      |             | BNE  | START4   | branch if char waiting       |  | C1D9 20     | CD1E    | JSR  | PSTRING     |                             |
| C170 17 | 0125 |             | LBSR | READ     |                              |  |             | 88      | BRA  | SETIME      | start again                 |
| C173 F1 | C122 |             | CMPB | RTIMP    | check for 1 second roll over |  |             |         |      |             |                             |
| C176 27 | F3   |             | BEQ  | START3   | if the time didn't change    |  |             |         |      |             |                             |
| C178 20 | EE   |             | BRA  | START2   |                              |  |             |         |      |             |                             |
| C17A BD | C009 | C17A START4 | EDU  | *        |                              |  |             |         |      |             |                             |
|         |      |             | JSR  | INCH     | get waiting character        |  |             |         |      |             |                             |
| C17D 84 | 5F   |             | ANDA | #0SF     | fold lower case to upper     |  |             |         |      |             |                             |
| C17F B1 | 54   |             | CMPA | #1T      | set time?                    |  |             |         |      |             |                             |
| C181 27 | 13   |             | BED  | SETIME   |                              |  |             |         |      |             |                             |
| C183 B1 | 44   |             | CMPA | #1D      | set date?                    |  | C1DB        | SEDATE  | EDU  | *           |                             |
| C185 27 | 54   |             | BEQ  | SEDATE   |                              |  | C1D8 BE     | C34F    | LDX  | #0DATST     |                             |
|         |      |             |      |          |                              |  | C1DE BD     | CD1E    | JSR  | PSTRING     |                             |
| C187 B1 | 52   |             | CMPA | #1R      | return to FLEX?              |  | C1E1 BD     | CD1B    | JSR  | INBUF       |                             |
| C189 26 | D7   |             | BNE  | START1   |                              |  |             |         |      |             |                             |
| C18B 20 | 06   | C18D ERR1   | BRA  | X11      | return to FLEX               |  | C1E4 30     | 80 00CB | LEAX | MONTBL.PCR  |                             |
| C18D 0E | C298 |             | EDU  |          |                              |  | C1EB 80     | 44      | BSR  | SEDAT2      | convert day of week string  |
| C190 BD | CD1E |             | LDX  | #BADYA   | get bad year msg             |  | C1EA 25     | 39      | BCS  | SEDAT1      | if input error              |
|         |      |             | JSR  | PSTRING  | print it                     |  | C1EC E7     | 25      | STB  | DOW,Y       |                             |
| C193 7E | C003 | C193 X17    | EDU  | *        |                              |  |             |         |      |             |                             |
|         |      |             | JMP  | WARM     | return to FLEX               |  | C1EE 30     | 80 00D7 | LEAX | MONTBL.PCR  |                             |
|         |      | *           |      |          |                              |  | C1F2 80     | 3A      | BSR  | SEDAT2      | convert month string to bin |
|         |      | *           |      |          |                              |  | C1F4 25     | 2F      | BCS  | SEDAT1      | if illegal value            |
|         |      | *           |      |          |                              |  | C1F6 F7     | 0COE    | STB  | SYDR        | save in FLEX date reg       |
|         |      | *           |      |          |                              |  | C1F9 C1     | 09      | CMPB | #9          | convert to BCD              |
|         |      | *           |      |          |                              |  | C1FB 23     | 02      | BLA  | +4          |                             |
|         |      | *           |      |          |                              |  | C1FD CB     | 06      | ADD0 | #6          |                             |
|         |      | *           |      |          |                              |  | C1FF E7     | 27      | STB  | MON,Y       | save it                     |
|         |      | *           |      |          |                              |  |             |         |      |             |                             |
|         |      | *           |      |          |                              |  | C201 20     | CD42    | JSR  | GETHEX      | set day of month digit      |
|         |      | *           |      |          |                              |  | C204 25     | 1F      | BCS  | SEDAT1      | if illegal value            |
|         |      | *           |      |          |                              |  |             |         |      |             |                             |
| C196 B6 | CC02 | C196 SETIME | EDU  | *        |                              |  | C206 1F     | 10      | TFR  | X,D         |                             |
| C199 34 | 02   |             | LDA  | EOL      | get TTY end of line char     |  | C208 C1     | 31      | CMPB | #031        |                             |
| C198 7F | CC02 |             | PSHS | A        | and save it                  |  | C20A 22     | 19      | BHI  | SEDAT1      |                             |
| C19E 8E | C321 |             | CLR  | EOL      | set EOL char to null         |  | C20C E7     | 26      | STB  | DOM,Y       |                             |
| C1A1 80 | CD1E |             | LDX  | #TIMEST  |                              |  |             |         |      |             |                             |
| C1A4 BD | CD1B |             | JSR  | PSTRING  |                              |  |             |         |      |             |                             |
|         |      |             | JSR  | INBUF    | get time string              |  |             |         |      |             |                             |
|         |      |             |      |          |                              |  |             |         |      |             |                             |
| C1A7 BD | CD42 |             | JSR  | GETHEX   | get hours digits             |  | C20E 34     | 04      | PSHS | B           | save hex month              |
|         |      |             |      |          |                              |  | C210 54     |         | LSRB |             | set ten's digit             |
| C1AA 1F | 10   | C1AA IF     | TFR  | X,D      |                              |  | C211 54     |         | LSRB |             |                             |
| C1AC C1 | 23   |             | CMPB | #023     |                              |  | C212 54     |         | LSRB |             |                             |
| C1AE 22 | 23   |             | BHI  | SETIM1   |                              |  | C213 54     |         | LSRB |             |                             |
| C1B0 E7 | 24   |             | STB  | HOUR,Y   |                              |  | C214 86     | 0A      | LDA  | #10         | multiply by 10              |
|         |      |             |      |          |                              |  | C216 30     |         | MUL  |             |                             |
|         |      |             |      |          |                              |  | C217 35     | 02      | PUS  | A           | set back hex month          |
|         |      |             |      |          |                              |  | C219 84     | 0F      | ANDA | #0200001111 | keep 1's digit              |
| C1B2 BD | CD42 | C1B2 00     | JSR  | GETHEX   | get minute digits            |  | C21B 34     | 02      | PSHS | A           | save back                   |
| C1B6 25 | 1C   |             | BCS  | SETIM1   | if illegal value             |  | C21D EB     | E0      | ADD0 | 0,S+        | add on 10's digit           |
|         |      |             |      |          |                              |  | C21F F7     | CC0F    | STB  | SYDR+1      | save in FLEX date reg       |
| C1B7 1F | 10   | C1B7 1F     | TFR  | X,D      |                              |  |             |         |      |             |                             |
| C1B9 C1 | 59   |             | CMPB | #059     |                              |  | C222 16     | FF30    | LBRA | START1      |                             |
| C1B8 22 | 16   |             | BHI  | SETIM1   |                              |  |             |         |      |             |                             |
| C1BD E7 | 23   |             | STB  | MIN,Y    |                              |  | C225 SEDAT1 | EDU     | *    |             |                             |
|         |      |             |      |          |                              |  | C226 BE     | C370    | LDX  | #0FLVMSG    |                             |
| C1BF BD | CD42 | C1BF BD     | JSR  | GETHEX   | get second digits            |  | C228 BD     | CD1E    | JSR  | PSTRING     |                             |
| C1C2 25 | 0F   |             | BCS  | SETIM1   | if illegal value             |  | C22B 16     | FF34    | LBRA | START1      |                             |
|         |      |             |      |          |                              |  |             |         |      |             |                             |
| C1C4 1F | 10   | C1C4 1F     | TFR  | X,D      |                              |  | C22E C6     | 03      | LDB  | #3          |                             |
| C1C6 C1 | 59   |             | CMPB | #059     |                              |  | C230 CE     | C122    | LDB  | #XTEMP      |                             |
| C1C8 22 | 09   |             | BHI  | SETIM1   |                              |  |             |         |      |             |                             |
| C1CA E7 | 22   |             | STB  | SEC,Y    |                              |  |             |         |      |             |                             |

|                  |         |              |                            |                              |                  |        |                                      |
|------------------|---------|--------------|----------------------------|------------------------------|------------------|--------|--------------------------------------|
| C233 BD          | C027    | JSR          | NITCH                      | get a character              | C283 53 55 4E    | FCC    | /SUN/                                |
| C236 24          | 08      | BCC          | SEDAT4                     | if alpha-numeric             | C284 40 4F 4E    | FCC    | /MON/                                |
| C238 81          | 20      | CMPA         | #SP                        |                              | C285 54 55 45    | FCC    | /TUE/                                |
| C23A 27          | F7      | BED          | SEDAT3                     | eat leading blanks           | C286 54 45 44    | FCC    | /WED/                                |
| C23C 81          | 00      | CMPA         | OCR                        |                              | C287 54 48 55    | FCC    | /THU/                                |
| C23E 27          | 29      | BED          | SEDAT7                     |                              | C288 46 52 49    | FCC    | /FRI/                                |
|                  |         |              |                            |                              | C289 53 41 54    | FCC    | /SAT/                                |
|                  |         |              |                            |                              | C28A 00          | FCB    | 0 end of table                       |
| C240 84          | 5F      | ANDA         | #\$SF                      | fold lower case to upper     | C2C9 4A 41 4E    | FCC    | /JAN/                                |
| C242 81          | 41      | CMPPA        | 0'A                        | don't use numeric chars      | C2D0 46 45 42    | FCC    | /FEB/                                |
| C244 20          | 23      | BLT          | SEDAT7                     |                              | C2D1 4D 41 52    | FCC    | /MAR/                                |
| C246 A7          | 00      | STA          | 0,U+                       |                              | C2D2 41 50 52    | FCC    | /APR/                                |
| C248 5A          |         | DEC8         |                            |                              | C2D3 4D 41 59    | FCC    | /MAY/                                |
| C249 26          | E8      | BNE          | SEDAT3                     |                              | C2D8 4A 53 4E    | FCC    | /JUN/                                |
| C24B BD          | C027    | JSR          | NITCH                      | eat terminator character     | C2D9 4A 55 4C    | FCC    | /JUL/                                |
| C24E FE          | C122    | LDU          | XTEMP+0                    | -> get first two chars       | C2DDE 41 55 47   | FCC    | /AUG/                                |
| C251 B6          | C124    | LDA          | XTEMP+2                    |                              | C2E1 53 45 50    | FCC    | /SEP/                                |
| C254 C6          | 01      | LDB          | 01                         | preset counter               | C2E4 4F 43 54    | FCC    | /OCT/                                |
|                  |         |              |                            |                              | C2E7 4E 4F 56    | FCC    | /NOV/                                |
|                  |         |              |                            |                              | C2EA 44 45 43    | FCC    | /DEC/                                |
|                  |         |              |                            |                              | C2D0 00          | FCB    | 0 end of table                       |
| C256 11A3        | 84      | CMPU         | 0,X                        |                              | C2EE 000A        | OPMSG  | FDB CRLF                             |
| C259 26          | 07      | BNE          | SEDAT6                     | try next string              | C2F0 53 65 74 20 | FCC    | 'Set time (T), set date (D),         |
| C25B A1          | 02      | CMPPA        | 2,X                        |                              | C30C 72 65 74 75 | FCC    | 'return to FLEX IR'                  |
| C25D 26          | 03      | BNE          | SEDAT6                     | try next string              | C31E 000A        | FDB    | CRLF                                 |
| C25F 1C          | FE      | CLC          |                            | indicate success             | C320 04          | FCB    | EDT                                  |
| C261 39          |         | RTS          |                            |                              |                  |        |                                      |
| C262 5C          |         | INC8         |                            |                              | C321 000A        | TIMST  | FDB CRLF                             |
| C263 30          | 03      | LEAK         | 3,X                        | try next string              | C323 49 6E 70 75 | FCC    | 'Input time in 24 hour '             |
| C265 60          | 84      | TST          | 0,X                        | end of table?                | C339 66 6F 72 60 | FCC    | 'format '23:59:59''                  |
| C267 26          | E8      | BNE          | SEDAT5                     |                              | C34A 00 0A 3E 20 | FCB    | CR,LF,">,SP                          |
|                  |         |              |                            |                              | C34E 04          | FCB    | EDT                                  |
| C269 1A          | 01      | C269  SEDAT7 | EDU                        | *                            | C34F 000A        | DATST  | FDB CRLF                             |
| C268 39          |         | SEC          |                            | indicate failure             | C351 49 6E 70 75 | FCC    | 'Input date in "DAY,MON,DD"          |
|                  |         | RTS          |                            |                              | C368 00 0A 3E 20 | FCB    | CR,LF,">,SP                          |
|                  |         |              |                            |                              | C36F 04          | FCB    | EDT                                  |
|                  |         | *            | *                          | UPDATE DISPLAY ONCE A SECOND | C370 49 6C 6C 65 | ILVMSG | FCC 'Illegal input value!!!'         |
|                  |         | *            | *                          |                              | C386 04          | FCB    | EDT                                  |
| C26C BD          | C387    | DISPLAY      | EDU                        | *                            |                  |        |                                      |
|                  |         | JSR          | ZROTIM                     | update time/date string      |                  |        |                                      |
| C26F 86          | 0D      | LDA          | OCR                        |                              |                  |        |                                      |
| C271 BD          | C00F    | JSR          | OUTCH                      |                              |                  |        |                                      |
| C274 8E          | C102    | LDX          | #YDATE                     |                              | 0014 STATUS      | EQU    | 20 status register                   |
| C277 AD          | 9F F80C | JSR          | [PDATA]                    | print date string            | C387 34 76       | PSHS   | A,B,X,Y,U save the important stuff   |
| C278 86          | 20      | LDA          | #SP                        |                              | C389 108E F700   | LDY    | BCLOCK -> start of the M680167 reg   |
| C27D 9D          | C00F    | JSR          | OUTCH                      |                              |                  |        |                                      |
| C280 BD          | C00F    | JSR          | OUTCH                      |                              |                  |        |                                      |
| C283 8E          | C112    | LDX          | #YTIME                     |                              | C38D CE C112     | LDU    | #YTIME -> where to put time          |
| C286 AD          | 9F F80C | JSR          | [PDATA]                    | print time string            | C390 86 04       | LDA    | #HOUR                                |
|                  |         |              |                            |                              | C392 80 16       | BSR    | RDTIMI convert & store hours         |
| C28A 86          | 20      | LDA          | #SP                        |                              | C394 86 03       | LDA    | #MIN                                 |
| C28C BD          | C00F    | JSR          | OUTCH                      |                              | C396 80 12       | BSR    | RDTIMI convert & store minutes       |
| C28F 80          | C00F    | JSR          | OUTCH                      |                              | C398 86 02       | LDA    | #SEC                                 |
| C292 80          | 04      | BSR          | READ                       |                              | C39A 80 0E       | BSR    | RDTIMI convert & store seconds       |
| C294 F7          | C122    | STB          | XTEMP                      |                              | C39C 86 04       | LDA    | #EOT                                 |
| C297 39          |         | RTS          |                            |                              | C39E A7 5F       | STA    | -1,U                                 |
|                  |         |              |                            |                              | C3A0 20 38       | BRA    | RDTIMI set end of string             |
|                  |         | *            | *                          | READ THE SECONDS REGISTER    |                  |        | now read the data                    |
|                  |         | *            | *                          |                              |                  |        |                                      |
| C298 E6          | 22      | READ         | LDB                        | SEC.Y                        |                  |        |                                      |
| C29A 39          |         | RTS          |                            |                              |                  |        |                                      |
|                  |         | *            | *                          | Data Area                    |                  |        |                                      |
|                  |         | *            | *                          |                              |                  |        |                                      |
| C29B 49 6E 76 61 | BADYA   | FCC          | /Invalid YEAR specified! / |                              | C3A2 E6 A6       | IREAD  | BCU *                                |
| C292 04          |         | FCB          | EDT                        |                              | C3A4 6D A8 14    | LDB    | A,Y                                  |
|                  |         |              |                            |                              | C3A7 26 F9       | TST    | STATUS,Y test for register roll over |
|                  |         |              |                            |                              | C3A9 39          | BNE    | XREAD                                |
|                  |         |              |                            |                              |                  |        | RTS                                  |

\* Convert BCD digits to ASCII BCD  
 \*  
 C3AA RDTIM1 EQU \*  
 C3AB 80 F6 BSR XREAD read the specified register  
 C3AC 1F 98 TFR B,A  
 \*  
 \* Convert MSB  
 \*  
 C3AE 44 L98A  
 C3AF 44 L98A  
 C380 44 L98A  
 C381 44 L98A  
 \*  
 \* Convert LSB  
 \*  
 C3AA 00 0A 3E 20 FCB DR,LF,">,SP  
 C3AE 04 FCB EOT  
 C34F 000A DATST FDB DRLF  
 C351 49 6E 70 75 FCC 'Input date in "DAY,MON,DD"  
 C368 00 0A 3E 20 FCB DR,LF,">,SP  
 C36F 04 FCB EOT  
 C370 49 6C 6C 65 ILVMSG FCC 'Illegal input value!!!'  
 C386 04 FCB EOT  
 \*  
 \* This routine reads the time and date and stores  
 \* the values in VTIME and VDATE respectively  
 \*  
 0014 STATUS EQU 20 status register  
 C387 RDTIME EQU \*  
 C387 34 76 PSNS A,B,I,Y,U save the important stuff  
 C389 108E F700 LDY UCLOCK -> start of the MSB167 reg  
 \*  
 \* Start by reading the time  
 \*  
 C38D CE C112 LDU VTIME -> where to put time  
 C390 86 04 LDA #HOUR  
 C392 80 16 BSR RDTIM1 convert & store hours  
 C394 86 03 LDA #MIN  
 C396 80 12 BSR RDTIM1 convert & store minutes  
 C398 86 02 LDA #SEC  
 C39A 80 0E BSR RDTIM1 convert & store seconds  
 C39C 86 04 LDA #EOT  
 C39E A7 SF STA -1,U set end of string  
 C3A0 20 38 BRA RDTIM4 now read the data  
 \*  
 \* Read the specified register  
 \*  
 C3A2 XREAD EQU \*  
 C3A2 E6 A6 LDB A,Y  
 C3A4 60 A8 14 TST STATUS.Y test for register roll over  
 C3A7 26 F9 BNE XREAD  
 00A9 39 RTS  
 \*  
 \* Convert BCD digits to ASCII BCD  
 \*  
 C3AA RDTIM1 EQU \*  
 C3AB 80 F6 BSR XREAD read the specified register  
 C3AC 1F 98 TFR B,A  
 \*  
 \* Convert MSB  
 \*  
 C3AE 44 LSRA  
 C3AF 44 LSRA  
 C380 44 LSRA  
 C381 44 L98A  
 \*  
 \* Convert LSB  
 \*  
 C382 C4 0F ANDB DIF  
 \*  
 e Convert MSB & LSB to ASCII

|                                                     |                                            |
|-----------------------------------------------------|--------------------------------------------|
| C384 C3 3030                                        | ADD 0'0+256+0                              |
| C387 ED C1                                          | STD 0,U++ store characters                 |
| C389 86 3A                                          | LDA 0':                                    |
| C388 A7 C0                                          | STA 0,U+                                   |
| C380 39                                             | RTS                                        |
| * Convert binary number to corresponding string     |                                            |
| C38E RDTIM2 D0U *                                   |                                            |
| C38E 80 0E                                          | BSR XREAD read the specified register      |
| C300 C1 09                                          | CMPB #9 check BCD range (> 9?)             |
| C3C2 23 02                                          | BLS #4                                     |
| C3C4 C0 06                                          | SUBB #6                                    |
| C3C6 34 04                                          | PSHS B                                     |
| C3C8 58                                             | ASLB                                       |
| C3C9 EB E0                                          | ADD 0,S+                                   |
| C3CB 3A                                             | ABX -> PICKUP STRING                       |
| C3CC C6 03                                          | LDB #3 move 3 characters                   |
| * C3CE RDTIM3 EOU *                                 |                                            |
| C3CE A6 80                                          | LDA 0,X+ move table char to ...            |
| C3D0 A7 C0                                          | STA 0,U+ parameter string area             |
| C3D2 5A                                             | DEC8                                       |
| C3D8 26 F9                                          | BNE RDTIM8                                 |
| C3D5 86 20                                          | LDA #SP                                    |
| C3D7 A7 C0                                          | STA 0,U+ install separator                 |
| C3D9 39                                             | RTS                                        |
| * Read the data                                     |                                            |
| * C3DA RDTIM4 EOU *                                 |                                            |
| C3DA CE C102                                        | LDU VDATE -> where to put date             |
| C3D0 30 80 F0CF                                     | LEAX #DWTBL-3,PCR -> day of week table     |
| C3E1 86 05                                          | LDA #DOW read day of week                  |
| C3E8 80 D9                                          | BSR RDTIM2 convert to ascii string         |
| C3E5 30 80 FE00                                     | LEAX #MONTBL-3,PCR -> month table          |
| C3E9 86 07                                          | LDA #MON read month                        |
| C3EB 80 01                                          | BSR RDTIM2 convert to ascii string         |
| C3ED 86 06                                          | LDA #DOM read day of month                 |
| C3EF BD B9                                          | BSR RDTIM1 convert to ascii BCD            |
| C3F1 86 20                                          | LDA #SP                                    |
| C3F3 A7 SF                                          | STA -1,U                                   |
| * The following code stores the year portion of     |                                            |
| * the date in the VDATE string. The last 2          |                                            |
| * digits in the year are gotten from the            |                                            |
| * FLEX date register and converted to a 2           |                                            |
| * digit ascii value.                                |                                            |
| * ASCBIN - this routine converts 2 ascii chars      |                                            |
| * to binary                                         |                                            |
| C3F5 CC 3139.                                       | LDB 0'1+256*9 "19"                         |
| C3F8 ED C1                                          | STD 0,U++                                  |
| C3FA 86 CC10                                        | LDA SYDR+2 get binary year                 |
| C3FD 80 2C                                          | BSR BINASC convert to ascii                |
| C3FF ED C1                                          | STD 0,U++                                  |
| C401 86 04                                          | LDA #EDT                                   |
| C403 A7 C4                                          | STA 0,U set end of string                  |
| C405 35 76                                          | PULS A,B,I,Y,U restore the important stuff |
| C407 39                                             | RTS                                        |
| * ASCBIN - this routine converts 2 ascii characters |                                            |
| * to binary                                         |                                            |
| * entry: ACC D contains 2 ascii characters          |                                            |
| * exit: ACC A contains binary equivalent            |                                            |
| * Carry is clear if digits are                      |                                            |

\* valid decimal digits (0-9).  
 \* otherwise carry is set  
 \*  
 \* accumulators A and B are used and not restored.  
 \*  
 C408 ASCBIN EQU \*  
 C40A 30 CMPA #10 make sure first ascii  
 C40C 1C BLO BADIG char is between  
 C40E 39 CMPA #9 0 and ?  
 C410 22 18 BHI BADIG  
 C410 C1 30 CMPB #10 make sure second ascii  
 C412 14 BLO BADIG char is between  
 C414 E1 39 CMPB #9 0 and ?  
 C416 22 10 BHI BADIG  
 C418 84 OF ANDA #2000001111 keep low 4 bits  
 C41A C4 0F ANDB #1000001111  
 C41C 34 04 PSMS B save 2nd digit  
 C41E C6 04 LDB #10  
 C420 3D MUL multiply first by 10  
 C421 1F 98 IFR B,A B -> A  
 C423 AB E0 ADDA 0,S+ add in I's digit  
 C425 1C FE CLC set good RC  
 C427 39 RTS return  
 C428 1A 01 BADIG SEC set bad RC  
 C42A 39 RTS return  
 \*  
 \* BINASC - this routine converts a 1 byte binary number (<= 99 base 10) to ascii.  
 \*  
 \* entry: ACC A contains binary number  
 \* exit: ACC D contains 2 digit ascii rep  
 \*  
 \* Accumulators A and B are used and not restored.  
 \*  
 C42B BINW EQU \*  
 C42B 02 PSMS A save binary #  
 C42D C6 08 LDB #8 8 bits to shift out  
 C42F 4F CLRA hold BCD value here  
 C430 DOBLE EQU \*  
 \*  
 \* Double current BCD result before shifting out a bit from the binary number.  
 \*  
 C430 34 02 PSMS A double BCD value  
 C432 AB E0 ADDA 0,S+  
 C434 19 DAA (in BCD)  
 C435 68 E4 LSL 0,S shift out a bit  
 C437 24 07 BCC CHX branch if bit=0  
 C439 34 02 PSMS A add 1 to current  
 C43D 86 01 LDA #1 BCD value  
 C43D AB E0 ADDA 0,S+ (in BCD of course)  
 C440 5A C0 DECB done yet?  
 C441 26 ED BNE DOBLE no, then continue  
 C443 32 61 LEAS 1,S clean up stack  
 \* Convert BCD # in A to ascii  
 \*  
 C445 34 02 PSMS A save BCD value  
 C447 44 LSRA cvt 10's dig to ascii  
 C448 44 LSRA  
 C449 44 LSRA  
 C44A 44 LSRA  
 C44B 8A 30 DRA #10  
 C44D 35 04 PULS B cvt 1's dig to ascii  
 C44F C4 0F ANDB #1000001111  
 C451 CA 30 ORB #10

(453 39 RTS END START return  
 0 ERROR(S) DETECTED

#### SYMBOL TABLE:

|        |      |         |      |        |      |         |      |         |      |
|--------|------|---------|------|--------|------|---------|------|---------|------|
| ADDX   | C006 | ASCBIN  | C408 | ASREAD | 0001 | ASWRIT  | 0002 | BAC     | 0008 |
| BADIG  | C428 | BADYR   | C29B | BAK    | 0005 | BAS     | 0003 | BELL    | 0007 |
| BIN    | 0000 | BINASC  | C42B | BS     | 0000 | BSE     | C007 | BUFFINT | C114 |
| CHK    | C440 | CLASS   | C021 | CLN    | CC1A | CLOCK   | F700 | CID     | 0002 |
| CMDFLG | C228 | CDC     | CC29 | COLDS  | C000 | CR      | 0008 | ORLF    | 0004 |
| CURC   | CC18 | DAT     | 0007 | DATST  | C34F | DBRV    | 0E00 | DEL     | 0001 |
| DEP7H  | C003 | DIA     | 0009 | DIRTS  | 0005 | DISPLAY | C24C | DISTD   | C154 |
| DOBLE  | C430 | DOUBLND | C048 | DOM    | 0006 | DOS     | CC00 | DOM     | 0005 |
| DIMTR  | C2B3 | DPLMSG  | C2EE | EJECT  | C008 | ENV     | CC20 | EOL     | 0002 |
| EOT    | 0004 | ERR1    | C180 | ESC    | CC0A | ESCRB   | CC16 | FACP    | 0010 |
| FADP   | 0040 | FAMP    | 0020 | FAMP   | 0080 | FCBAS   | 0002 | FCBASE  | 0409 |
| FCBCDA | 002F | FCBCP   | 001E | FCBCRN | 0020 | FCBCUR  | D40B | FCBDI   | 0022 |
| FCBNR  | 0043 | FCBEDA  | 0013 | FCBES9 | 0001 | FCBFA   | 000F | FCBFIC  | 0000 |
| FCBFCD | 0019 | FCBFDD  | 0032 | FCBFS  | 0015 | FCBFM   | 0017 | FCBLEN  | 0140 |
| FCBIP  | 001C | FCBNAM  | 0004 | FCBMB  | 0024 | FCBRI   | 0023 | FCBRSI  | 0010 |
| FCBRSZ | 0018 | FCBSE   | 0040 | FCBSR  | 0038 | FCBSZR  | 0035 | FCB60A  | 0011 |
| FCBVNR | D435 | FCDDAY  | 001A | FCDNTH | 0019 | FCDYR   | 001B | FIA     | CC26 |
| FIEF   | C2C2 | FLEX    | C000 | FMS    | D400 | FMSCAL  | D406 | FMSCLS  | D403 |
| FASERR | CC20 | FNSINT  | D400 | FOA    | CC24 | FSMRAN  | 0002 | FSMSEQ  | 0000 |
| GETCHR | C015 | GETFIL  | C020 | GETHEX | C042 | HOUR    | 0004 | ILVMSC  | C370 |
| INBUF  | C018 | INCH    | C009 | INCH2  | C00C | INDEC   | C048 | IOLFLG  | D0E1 |
| ISWTC  | CC23 | LAO     | CC1B | LF     | 000A | LNEBUF  | C080 | LOAD    | C030 |
| LSTRM  | CC11 | MAP     | CC00 | MENEND | CC28 | MIN     | 0003 | MON     | 0007 |
| MONTR  | C229 | MULL    | C005 | NXTBH  | C027 | NSWITCH | C222 | OUT     | 0008 |
| OUTADR | C145 | OUTCH   | C00F | OUTCM2 | C012 | OUTZEE  | C039 | OUTHER  | C03C |
| PAU    | CC09 | PCRLF   | C024 | PDATA  | F80C | POUT    | C0E4 | PRCK    | C008 |
| PREVC  | C019 | PRINIT  | CC00 | PRT    | 000A | PSTRNG  | C01E | PUTDIR  | C018 |
| RDITIM | C38A | RDTIM2  | C38E | RDTIM3 | C3CE | RDTIME  | C30A | RDTIME  | C387 |
| READ   | C298 | RENTER  | C006 | RPTERR | C00F | RSTRIO  | C00A | SDATA   | 0044 |
| SBLINR | 0040 | SBRSI   | 0042 | SCFSC  | 00F  | SCFSC   | 0000 | SCR     | 0006 |
| SEC    | 0002 | SEDAT1  | C225 | SEDAT2 | C22E | SEDAT3  | C233 | SEDAT4  | C240 |
| SEDAT5 | C256 | SEDAT6  | 0062 | SEDAT7 | C269 | SEDATE  | C108 | SETEXT  | C033 |
| SETIM1 | C103 | SETIME  | C196 | SFA    | C980 | SIRORE  | 0023 | SIRDAY  | 0024 |
| SIRFSB | 001D | SIRFSE  | 001F | SIRFSS | 0021 | SIRLEN  | 0028 | SIRMTW  | 0023 |
| SIRMTS | 0026 | SIRRMH  | 0010 | SIRTS  | 0003 | SIRVNL  | 001B | SIRVR   | 0025 |
| SP     | 0020 | SPS     | C700 | START  | C125 | START1  | C162 | START2  | C168 |
| START3 | C168 | START4  | C17A | STAT   | C04E | STATUS  | 0014 | STKA    | C000 |
| SYDR   | 000E | SYDRV   | C00B | SYS    | 0004 | SYSCON  | C4E  | SYSCR1  | C000 |
| SYSCR2 | C22A | SYSCR3  | C330 | SYSCR4 | C0FB | SYSCFB  | B840 | TAB     | C006 |
| TIMST  | C321 | TRADM   | CC1E | TRFLG  | B01D | TXT     | 0001 | UCA     | C100 |
| UCTA   | CC12 | URAM    | 0000 | VN     | 0002 | WARMS   | C003 | WIDTH   | C004 |
| WKDRV  | CC0C | XBOR    | 0016 | XCLOSE | 0004 | XDELETE | 000C | XFN0    | 0014 |
| XGIR   | 0007 | XGRB    | 0011 | XIT    | C193 | XNSS    | >00F | XODIR   | 0006 |
| XREAD  | 0001 | XOSIR   | 0010 | XOPDT  | 0003 | XOWRIT  | 0002 | XPIR    | 0008 |
| XPSH   | 0015 | XPRB    | 0012 | XREAD  | C0A2 | XRENAM  | 0009 | XRES1   | 0008 |
| XRES0  | 000E | XRES3   | 0013 | XRENAM | 0005 | XRSS    | 0009 | XRNWB   | 0000 |
| XTEMP  | C122 | XWS6    | 000A | YDATE  | C102 | YTIME   | C112 | ZDOTH   | C387 |

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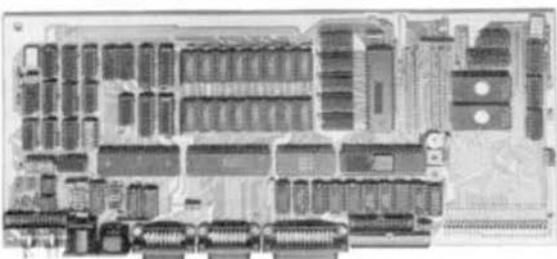
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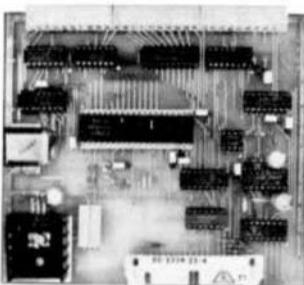
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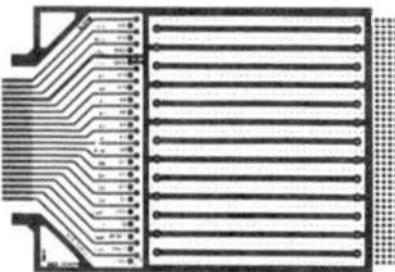
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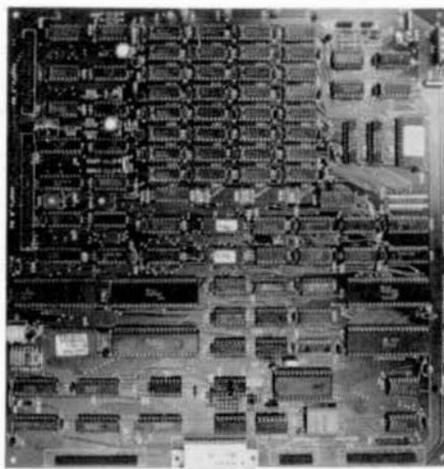
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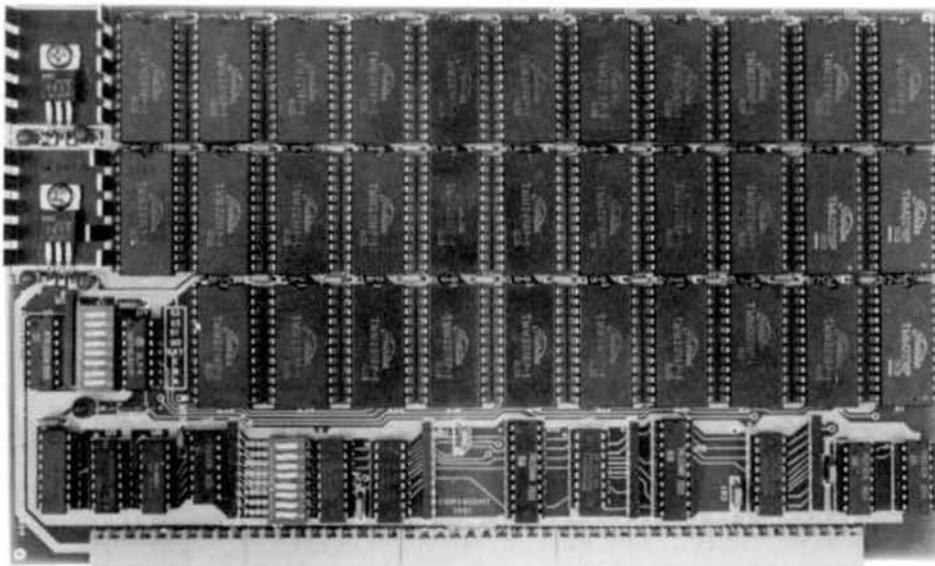
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MC6809 software for FLEX and OS9 (Level 1 or 2, Version 1.2).

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Menu driven software provides the following facilities:

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- b. MOVE ..... blocks of data.
- c. DUMP ..... the buffer in HEX and ASCII.
- d. FIND ..... a string of bytes in the buffer.
- e. EXAMINE/CHANGE ..... the contents of the buffer.
- f. CRC ..... checksum a selected area of the buffer.
- g. COPY ..... a selected area of an EPROM into the buffer.
- h. VERIFY ..... a selected area of an EPROM against the buffer.
- i. PROGRAM ..... a selected area of an EPROM with data in the buffer.
- j. SELECT ..... a new EPROM type (return to types menu).
- k. ENTER ..... the system monitor.
- l. RETURN ..... to the operating system.
- m. EXECUTE ..... any DOS utility (only in FLEX and OS9 versions).

FLEX AND OS9 VERSIONS AVAILABLE FROM GIMIX. SSB/MCOS CONTACT US DIRECT.

## PL/9

- Friendly inter-active environment where you have INSTANT access to the Editor, the Compiler, and the Trace-Debugger, which, amongst other things, can single step the program a SOURCE Line at a time. You also have direct access to any FLEX utility and your system monitor.
- 575+ page manual organized as a tutorial with plenty of examples.
- Fast SINGLE PASS compiler produces 8k of COMPACT and FAST 6809 machine code output per minute with no run-time overheads or license fees.
- Fully compatible with TSC text editor format disk files.
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- Mathematical expressions: (+), (-), (\*), (/), modulus (%), negation (-)
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- Bit operators: (AND), (OR), (EOR/XOR), (NOT), (SHIFT), (SWAP)
- Logical operators: (AND), (OR), (EOR/XOR)
- Control statements: IF..THEN..ELSE, IF..CASE1..CASE2..ELSE, BEGIN..END, WHILE.., REPEAT..UNTIL, REPEAT..FOREVER, CALL, JUMP, RETURN, BREAK, GOTO.
- Direct access to (ACC), (ACCD), (ACCO), (XREG), (CCR) and (STACK).
- FULLY supports the MC6809 RESET, NMII, FIRO, IRR, SW1, SW2, and SW3 vectors. Writing a self-starting (from power-up) program that uses ANY, or ALL, of the MC6809 interrupts is an absolute snap!
- Machine code may be embedded in the program via the 'GEN' statement. This enables you to code critical routines in assembly language and embed them in the PL/9 program (see 'MACE' for details).
- Procedures may be passed and may return variables. This makes them functions which behave as though they were an integral part of PL/9.
- Several fully documented library procedure modules are supplied: IOS95, BITIO, HARDIO, MEMIO, FLEK95, SCIPACK, STMSUBS, BASTRING, and REALCON.

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## MACE/XMACE/ASM05

All of these products feature a highly productive environment where the editor and the assembler reside in memory together. Gone are the days of tedious disk load and save operations while you are debugging your code.

- Friendly inter-active environment where you have instant access to the Editor and the Assembler, FLEX utilities and your system monitor.
- MACE can also produce ASHPROCs (GEM statements) for PL/9 with the extended mnemonics of the 6305.
- XMACE is a cross assembler for the 6800/1/2/3/8 and supports the extended mnemonics of the 6305.
- ASM05 is a cross assembler for the 6805.

## D-BUG

LOOKING for a single step tracer and edit-in-line disassembler that is easy to use? Look no further, you have found it. This package is ideal for those small assembly language program debugging sessions. D-BUG occupies less than 6K (including its stack and variables) and may be loaded anywhere in memory. All you do is LOAD IT, AIM IT and GO! (80 col VDU's only).

## McCOSH C

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- Built-in optimizer will shorten object code by about 11%.
- Supports interleaved assembly language programs.
- INCLUDES its own assembler. The TSC relocating assembler is only required if you want to generate your own libraries.
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\* Fully documented with a complete reprint of the K1000000 article on the IEEE bus and the Motorola publication 'Getting aboard the IEEE Bus'.

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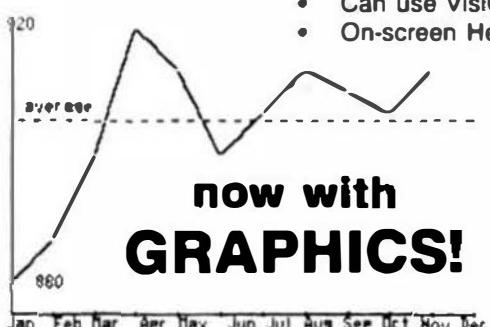
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| 2816        |       |      |       |       |       |       |       |
| 68764       |       |      |       |       |       |       |       |
| 8748        |       |      |       |       |       |       |       |
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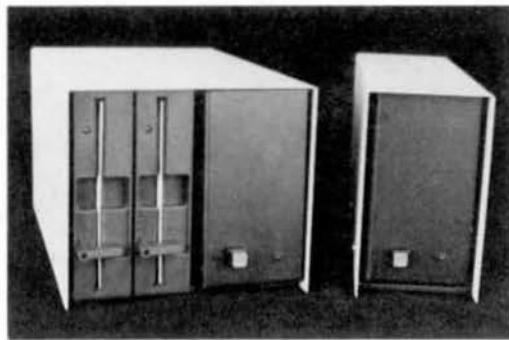
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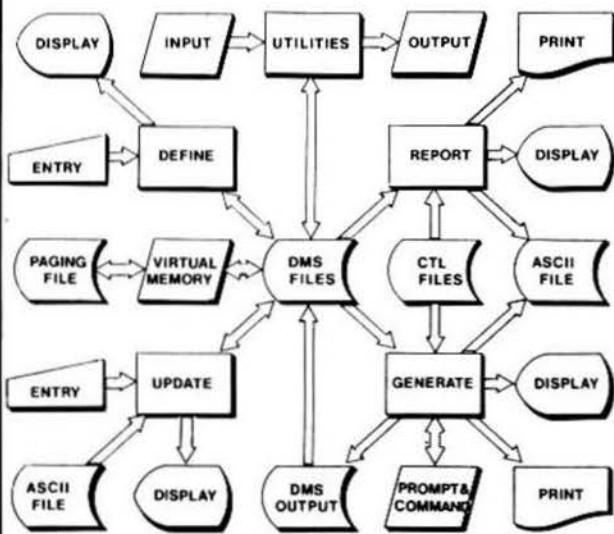
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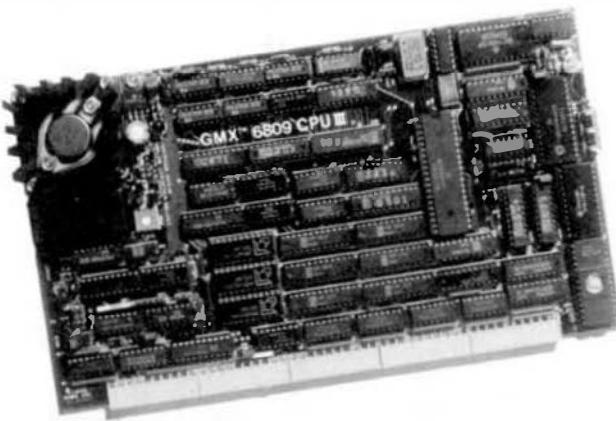
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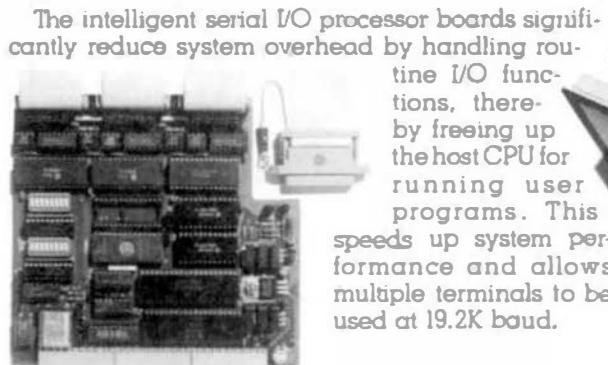
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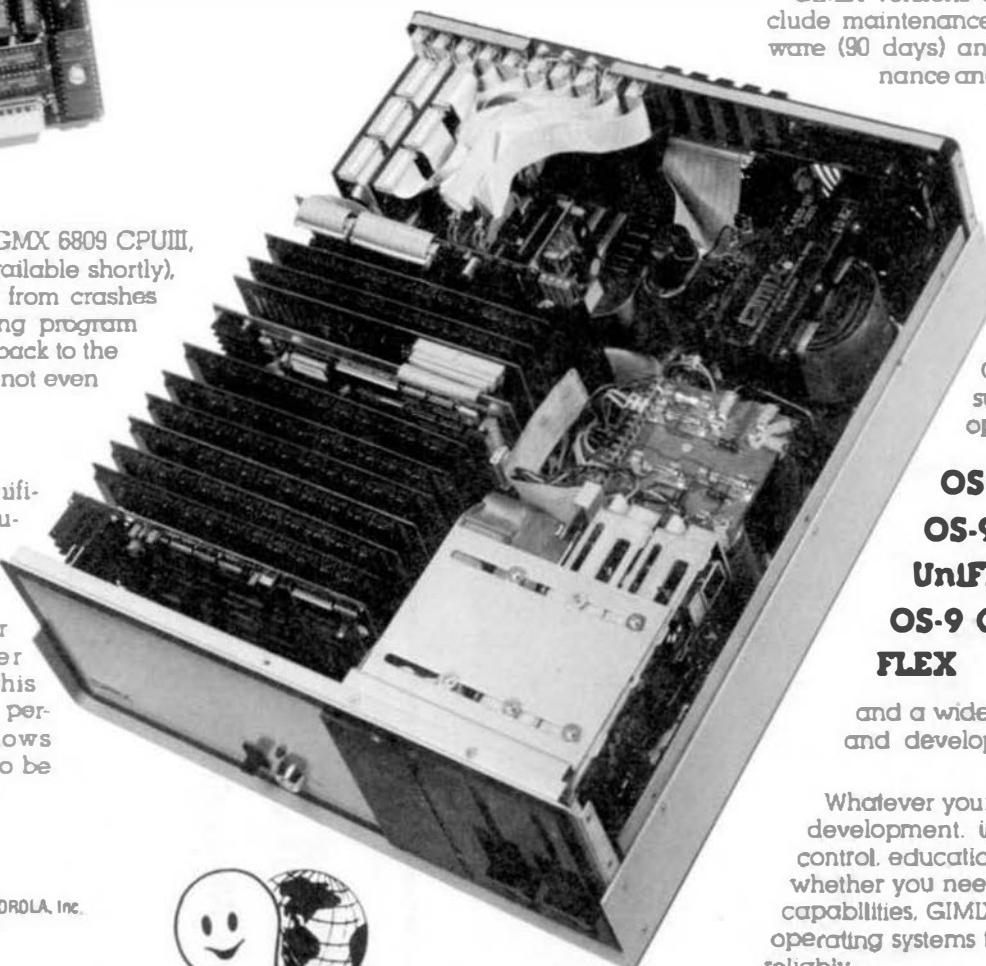


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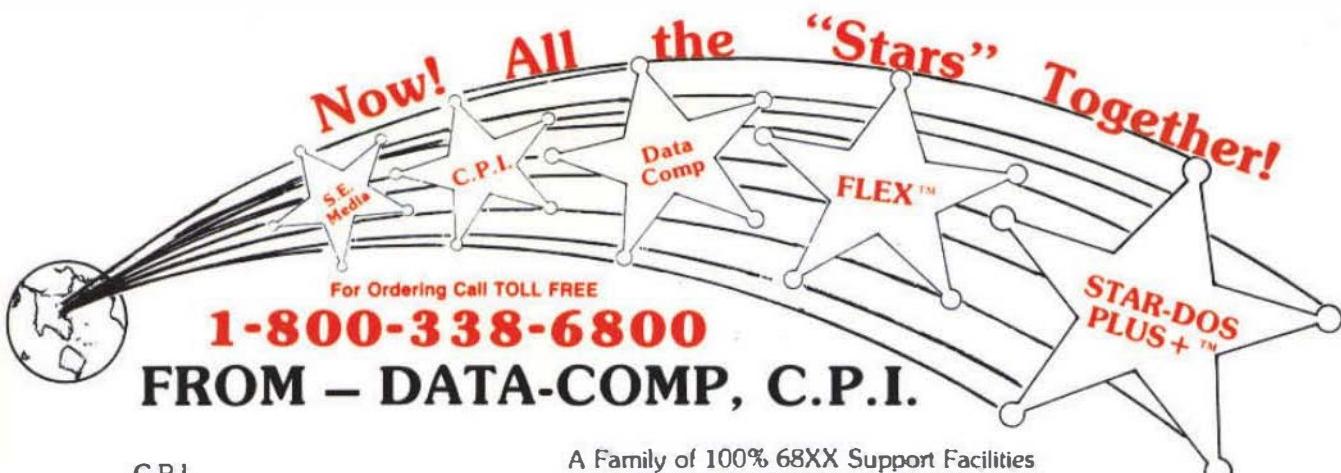
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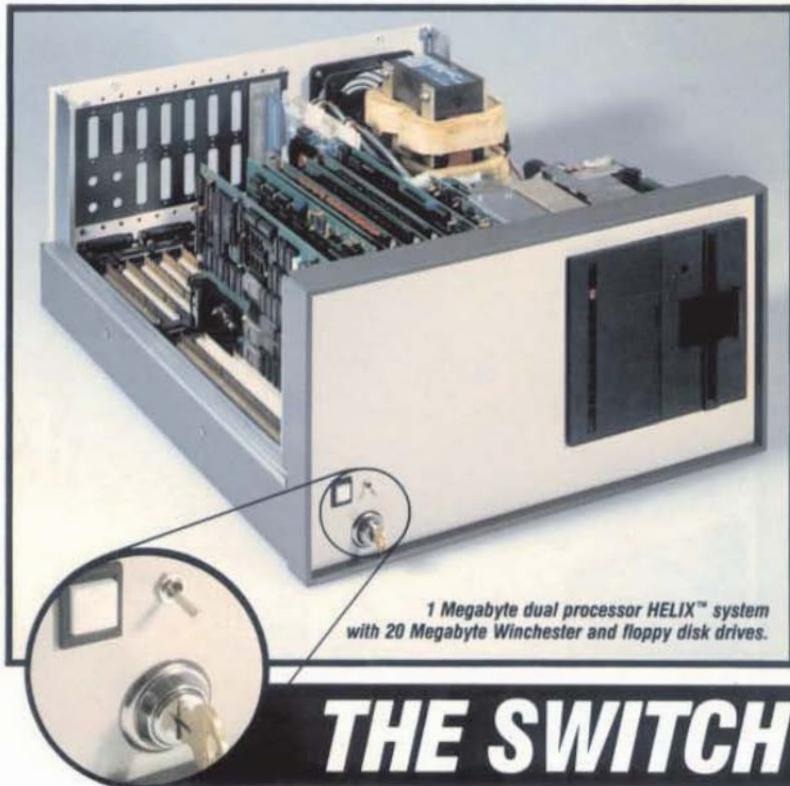
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1 Megabyte dual processor HELIX™ system  
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**ONCE AGAIN HAZELWOOD COMPUTER SYSTEMS** demonstrates its leadership in computer technology by delivering the only computer system capable of switching between either the 6809 or the 68000 processor. Switching is easily accomplished by a simple front panel toggle switch. The reason we can offer this exclusive feature now, is that when our proven 6809 processor board was designed several years ago, we had the foresight to include the bus controls that allow processor switching.

Hazelwood Computer Systems is also proud to be the first S-50/S-64 bus manufacturer to license and deliver the OS9/68K Operating System from Microware Systems Corporation. OS9/68K is the 68000 version of the popular and powerful OS9 Operating System. Utilizing our proven MC-20 disk controller, OS9/68K can conveniently share a Winchester disk with OS9. Changing from 6809 to 68000 operation is as simple as switching processors and booting the new system from the Winchester disk.

The ease of switching processors and operating systems makes a HELIX™ dual processor system the natural choice for software development. In addition, the advanced design of HELIX™ equipment, emphasizing performance and reliability, makes HELIX™ boards and systems the best value in computing offered anywhere.

System prices vary with configuration. Call for exact pricing.

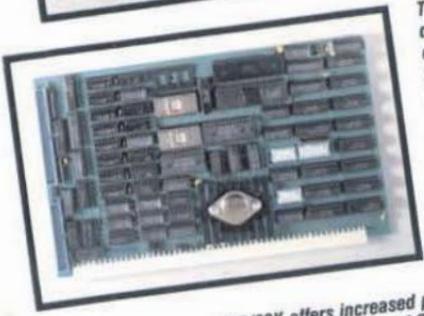
## THE SWITCH IS ON...



The CP-08 processor board utilizes a 68008 processor running at 10 MHz clock rate. Using proprietary bus synchronization circuitry and single cycle DMA, the CP-08 achieves a marked performance increase over a 2 MHz 6809. Offering absolute compatibility with the 68000 instruction set, the 68008 addresses up to 1 Megabyte of memory. Also included on the CP-08 are up to 4K of ROM, an interrupt timer, and with battery backup operation, a clock/calendar and 2K RAM. Implemented as a standard S-50 board, the CP-08 brings 68000 operation to S-50 bus computers.

ORDER: CP-08

PRICE: \$595



The MC-20 Mass Storage Controller board interfaces up to 4 floppy and 8 Winchester disk drives to the S-50/S-64 bus. The MC-20 is an intelligent controller with its own 2 MHz 6809 processor and 56K RAM. It provides DMA data transfers to a full 24 bit address. All disk operation requests are by logical block number, with the controller performing the necessary track/sector address calculations. Any combination of 5½ or 8 inch floppy drives can be accommodated with all individual drive. Winchester drives are connected via a SASI bus interface. Block address mapping is provided which allows a single drive to be segmented into several logical units. The MC-20 is the controller of the MS-20 Mass Storage Subsystem which includes a 20 Megabyte Winchester drive.

PRICE: \$695

ORDER: MC-20

OS9/68K offers increased performance and larger user memory space while retaining all of the features of OS9. Disk file compatibility and operational similarity assures that present OS9 users can easily transfer their operations to the 68000. Included are an editor, assembler, linker, and debugger. A C compiler is available now. BASIC09 and other languages will be available soon.

**OS9/68K**

ORDER: OS9/68K

PRICE: \$250

All items available stock to 30 days.  
Prices subject to change without notice.

## HAZELWOOD COMPUTER SYSTEMS

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314-281-1055

**HELIX**

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